

## AMD Athlon II X4 605e (rev. C2) specifications

### General information

Type	<a href="#">CPU / Microprocessor</a>
Market segment	Desktop
Family	<a href="#">AMD Athlon II X4</a>
Model number <span>?</span>	<b>605e</b>
CPU part numbers	AD605EHDK42GI is an OEM/tray microprocessor <ul style="list-style-type: none"><li>AD605EHDGIBOX is a boxed microprocessor with fan and heatsink</li></ul>
Stepping codes	<a href="#">AADACAD</a> <a href="#">CADACAD</a>
Frequency <span>?</span>	<b>2300 MHz</b>
Bus speed <span>?</span>	<b>667 MHz Memory controller</b> <b>One 2000 MHz 16-bit HyperTransport link</b>
Clock multiplier <span>?</span>	<b>11.5</b>
Package	938-pin organic micro-PGA
Sockets	<b>Socket AM2+</b> <b>Socket AM3</b>
Introduction date	<a href="#">Oct 20, 2009</a>
Price at introduction	\$143
<b>Architecture / Microarchitecture</b>	
Microarchitecture	K10
Processor core <span>?</span>	<a href="#">Propus</a>
Core stepping <span>?</span>	BL-C2
CPUID	100F52
Manufacturing process	0.045 micron silicon-on-insulator (SOI) technology
Die	169mm <sup>2</sup>
Data width	<b>64 bit</b>
The number of CPU cores	<b>4</b>
The number of threads	<b>4</b>
Floating Point Unit	Integrated
Level 1 cache size <span>?</span>	4 x 64 KB 2-way set associative instruction caches 4 x 64 KB 2-way set associative data caches
Level 2 cache size <span>?</span>	<b>4 x 512 KB 16-way set associative caches</b>
Level 3 cache size	<b>None</b>
Multiprocessing	Uniprocessor
Extensions and Technologies	<ul style="list-style-type: none"><li>MMX instructions</li><li>Extensions to MMX</li><li>3DNow! technology</li><li>Extensions to 3DNow!</li><li>SSE / Streaming SIMD Extensions</li><li>SSE2 / Streaming SIMD Extensions 2</li><li>SSE3 / Streaming SIMD Extensions 3</li><li>SSE4a <span>?</span></li><li>AMD64 / AMD 64-bit technology <span>?</span></li><li>AMD-V / AMD Virtualization technology</li><li>EVP / Enhanced Virus Protection <span>?</span></li></ul>
Low power features	<ul style="list-style-type: none"><li>Cool'n'Quiet 3.0</li><li>CoolCore Technology <span>?</span></li><li>Dual Dynamic Power Management <span>?</span></li><li>Core C1 and C1E states</li><li>Package S0, S1, S3, S4 and S5 states</li></ul>
<b>Integrated peripherals / components</b>	
Integrated graphics	None
Memory controller	The number of controllers: 1 Memory channels: 2 Channel width (bits): 72 Supported memory: DDR2-1066, DDR3-1333

DIMMs per channel: up to 2  
Maximum memory bandwidth (GB/s): 21.3  
HyperTransport 3 technology

Other peripherals

**Electrical / Thermal parameters**

V core [?](#) 0.775V - 1.2V

Maximum operating temperature [?](#) 55°C - 70°C

**Thermal Design Power** [?](#) **45 Watt**