

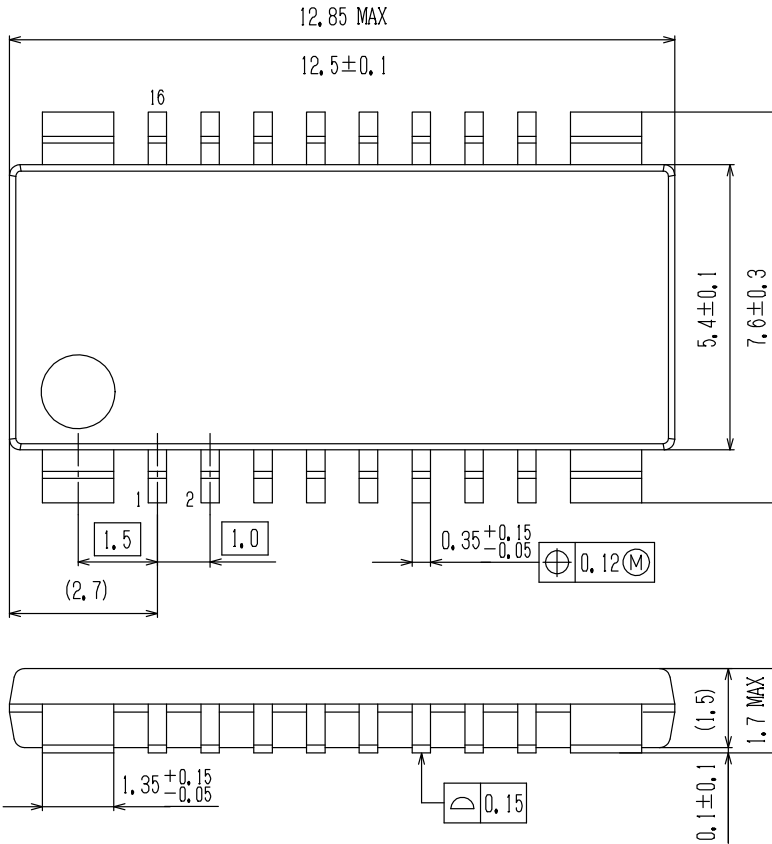
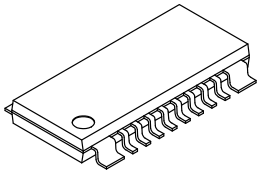
**MECHANICAL CASE OUTLINE**  
**PACKAGE DIMENSIONS**

ON Semiconductor®

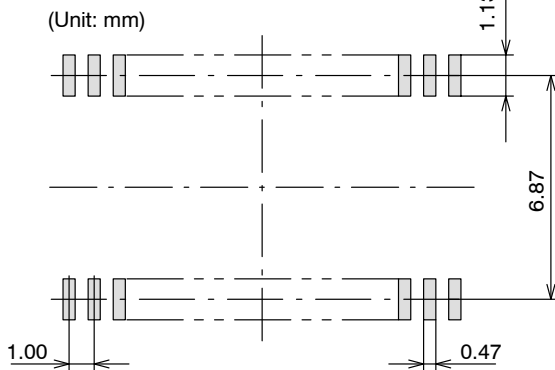


**SOIC16-TL / MFP16FS (300 mil)**  
**CASE 751CD**  
**ISSUE A**

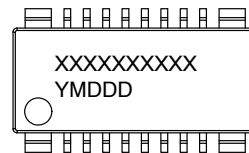
DATE 08 NOV 2013



**SOLDERING FOOTPRINT\***



**GENERIC MARKING DIAGRAM\***



XXXXX = Specific Device Code  
 Y = Year  
 M = Month  
 DDD = Additional Traceability Data

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.

NOTE: The measurements are not to guarantee but for reference only. Land pattern design in Fin area to be altered in response to customers' individual application.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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<b>STATUS:</b>	<b>ON SEMICONDUCTOR STANDARD</b>	
<b>NEW STANDARD:</b>		
<b>DESCRIPTION:</b>	<b>SOIC16-TL / MFP16FS (300 MIL)</b>	<b>PAGE 1 OF 2</b>



ISSUE	REVISION	DATE
O	RELEASED FOR PRODUCTION FROM SANYO ENACT# S-030 TO ON SEMICONDUCTOR. REQ. BY D. TRUHITTE.	29 FEB 2012
A	ADDED MARKING AND SOLDER FOOTPRINT INFORMATION. REQ. BY D. TRUHITTE.	08 NOV 2013

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