

# VIA Nano L2007 specifications

## General information

Type	CPU / Microprocessor
Market segment	Mobile
Family	VIA Nano 2000 Series
Model number <a href="#">?</a>	<b>L2007</b>
<b>Frequency</b> <a href="#">?</a>	<b>1600 MHz</b>
<b>Bus speed</b> <a href="#">?</a>	<b>800 MHz</b>
Package	400-ball NanoBGA2
<b>Socket</b>	<b>BGA400</b>
Size	0.83" x 0.83" / 2.1cm x 2.1cm

## Architecture / Microarchitecture

Microarchitecture	Isaiah
Manufacturing process	0.065 micron
Die	63.3mm <sup>2</sup> (7.65mm x 8.275mm)
<b>Data width</b>	<b>64 bit</b>
<b>The number of CPU cores</b>	<b>1</b>
<b>The number of threads</b>	<b>1</b>
Floating Point Unit	Integrated
Level 1 cache size <a href="#">?</a>	64 KB 16-way associative instruction cache 64 KB 16-way associative data cache

**Level 2 cache size** [?](#) **1 MB 16-way set associative exclusive cache**

Features	<ul style="list-style-type: none"><li>• MMX instructions</li><li>• SSE / Streaming SIMD Extensions</li><li>• SSE2 / Streaming SIMD Extensions 2</li><li>• SSE3 / Streaming SIMD Extensions 3</li><li>• SSSE3 / Supplemental Streaming SIMD Extensions 3</li><li>• x86-64 / 64-bit extensions</li><li>• Padlock Security Engine:</li><li>• AES encryption engine</li><li>• Secure hash algorithm</li><li>• NX Execute protection</li><li>• Random Number Generators</li></ul>
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Low power features	<ul style="list-style-type: none"><li>• C6 state</li><li>• Adaptive PowerSaver technology</li><li>• Adaptive Thermal Limit</li></ul>
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## Integrated peripherals / components

Integrated graphics	None
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## Electrical / Thermal parameters

<b>Thermal Design Power</b> <a href="#">?</a>	<b>25 Watt</b>
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