

SINGLE-CHIP MICROCONTROLLER WITH ON-CHIP PRESCALER, PLL FREQUENCY SYNTHESIZER, AND IF COUNTER FOR CAR-MOUNTED FM/MW/LW RADIO

DESCRIPTION

JEC

 μ PD17012GF-055 is 4-bit CMOS microcontroller for PLL frequency synthesizer-method digital tuning, which is capable of receiving European FM, MW, and LW.

The external appearance is 64-pin plastic QFP in which the prescaler (150 MHz MAX.), PLL frequency synthesizer, and IF counter are integrated. Also, since it can use various RDS (Radio Data System) functions in the FM band, it is possible for a car-mounted stereo and the high-performance multi-functional FM, MW, and LW tuner to be configured in a single chip.

FEATURES

- Can receive European FW, MW, and LW bands.
- Preset memory of 6 stations in FM1, FM2, and AM, respectively (thus totaling 18 stations)
- One last-channel station memory for each band, FM1, FM2, and AM
- Rich in station-select functions, such as selection of stations through MANUAL or AUTO-SEEK UP/DOWN, preset memory scan, auto store memory (sorting stations with strong SD signals in the order of frequency), etc.
- RDS decode function
- Traffic information stand-by (TA/DK stand-by) function
- μPD16431A is used for the LCD controller/driver
- Clock function with 12- or 24-hour display (No-clock also possible)
- Program name display function based on RDS-broadcast data
- AF function for 25 stations, handling methods A and B
- Supporting electronic volume
- Supporting detachable panel
- 5 V \pm 10 %: single power supply

ORDERING INFORMATION

Part Number

Package

μPD17012GF-055-3BE

64-pin plastic QFP (14 \times 20 mm)

The information in this document is subject to change without notice.

FUNCTION OVERVIEW

Receive Frequency, Channel Space, Reference Frequency, Intermediate Frequency

ltem Band	Receive Frequency	Channel Space	Reference Frequency	Intermediate Frequency
FM	87.50 – 108.00 MHz	50 kHz	50 kHz	10.7 MHz
MW	522 – 1620 kHz	9 kHz	9 kHz	450 kHz 459 kHz 10.71 MHz
LW	144 – 281 kHz	1 kHz	1 kHz	450 kHz 459 kHz 10.71 MHz

Channel Selection Function

(1) Manual tuning

Туре	Description
Manual up / Manual down	Pressing the key once moves the frequency up/down by a step. Keeping the key pressed for over 0.5 second will result in fast forward.

(2) Auto tuning

Туре	Description
Seek up / Seek down	Searches for broadcasting stations in up/down directions. If a station is detected, the frequency of the station is retained. In RDS mode, only RDS broadcasting stations are searched for. In TP/SK mode, only traffic information stations are searched for.

(3) Preset memory

Data on 6 broadcasting stations can be stored for each of the bands (FM1, FM2, AM), totaling 24 stations.

(4) Auto store memory

Broadcasting stations are searched for starting from the lowest frequency. Detected stations are written in preset memory starting from the highest SD level. Afterwards, they are sorted in order of the frequency.

(5) Last channel memory

Equipped with last channel memory independently for each of the FM1, FM2, and AM bands.

(6) Auto retuning

If no SD signal can be detected for about 20 seconds or longer during reception of a broadcasting station, auto retuning is started automatically.

(7) TP/SK auto retuning

If no SD or TP/SK signal is detected for 30 seconds or longer in TP/SK mode during reception of a broadcasting station, auto retuning to detect TP/SK stations is started automatically.

RDS Function

- Broadcasting station name display
 Displays the name of the broadcasting station whose programs are currently being received, by using the PS code.
- (2) AF operation
 - AF list of up to 25 stations can be incorporated to handle METHOD A and METHOD B.
- (3) Switchover to a traffic information station
 TA or TP data detected during TP/SK stand-by cause a switch to a traffic information station.
- (4) PTY alarm

(5) RDS memory

Equipped with RDS memory for 14 stations, namely, preset memory for 6 stations each in FM1 and FM2, and the last channel memory for each of FM1 and FM2.

Clock Function

- (1) Capable of 12-hr (with "AM" and "PM" showing) and 24-hr displays.
- (2) Use of a flashing colon (":") (1 Hz) can be selected.
- (3) In no-clock mode, backup is possible with low power consumption.

Tape Function

- (1) Sound switchover can be made by tape signal input.
- (2) The tape run direction can be displayed.
- (3) Capable of noise reduction output
- (4) Metal tape compatible

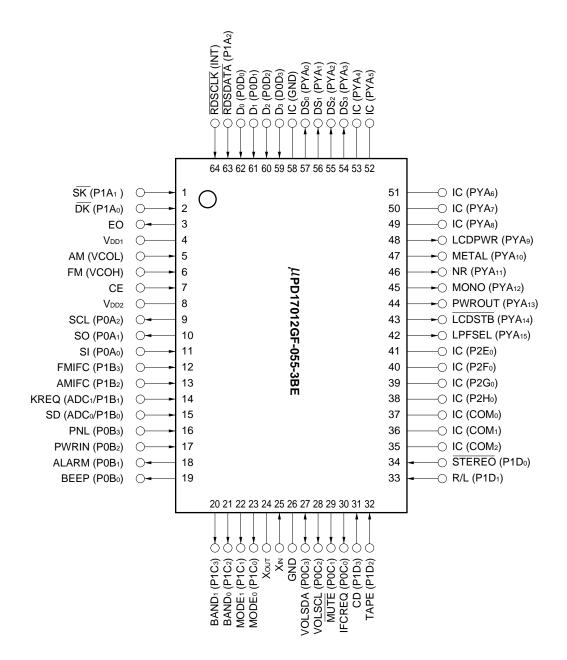
CD Function

(1) Capable of sound switchover by CD signal input

Electronic Volume Function

- (1) The values of volume/bass/treble/balance/fader can be set.
- (2) Supporting attenuator/loudness

PIN CONFIGURATION (TOP VIEW)



Caution Connect "IC" pin to GND directly.

Remarks 1. IC: internally connected pins

2. (): Applies to the μ PD17012GF pins.

CONTENTS

1.	PIN	FUNC	TIONS	7
2.	KEY		RIX CONFIGURATION	12
	2.1	Alloc	ation of Initialize Diode Matrix	12
	2.2	Conr	ection of Initialize Diode Matrix	12
	2.3	Alloc	ation of Momentary Key Matrix	13
	2.4	Desc	ription of Key Matrix	14
		2.4.1	Initialize diode matrix	
		2.4.2	Momentary key	
3.	MOI	DE TR	ANSITIONS	28
4.	DAT		TPUT TO LCD CONTROLLER/DRIVER (μPD16431A)	29
	4.1		Input/Output Timing	
5.	RDS	6 (Rad	io Data System) FUNCTION	33
	5.1	RDS	Data Incorporation	33
	5.2	RDS	Data Processing	
		5.2.1	PI (Program Identification)	
		5.2.2	PS (Program Service Name)	35
		5.2.3	PTY (Program Type)	35
		5.2.4	AF (Alternative Frequency)	35
		5.2.5	TP (Traffic Program Identification), TA (Traffic Announcement Identification)	40
6.	MUT		IING	41
	6.1	Tune	r Operation	41
		6.1.1	Preset memory reading	41
		6.1.2	Preset scan	44
		6.1.3	Preset memory writing	44
		6.1.4	Seek up/down	46
		6.1.5	Manual up/down	49
		6.1.6	Auto store memory	50
		6.1.7	AF switchover	51
	6.2	Mode	e Switchover	54
		6.2.1	$RADIO\ mode \leftrightarrow TAPE/CD\ mode\$	54
		6.2.2	Traffic information broadcasting/PTY alarm \leftrightarrow TAPE/CD mode (TP/SK mode)	55
	6.3	CE P	in	55
		6.3.1	Low level \rightarrow high level	55
		6.3.2	$High\;level\tolow\;level\ldots$	55
	6.4	Dete	cting the Detachable Panel	56
	6.5	Powe	er Control	
		6.5.1	Timing of POWER ON \leftrightarrow OFF transitions by \fbox{POWER} key	57
		6.5.2	Timing of POWER ON \leftrightarrow POWER OFF transitions by detachable panel	58

7.	LCD PANEL	59
	7.1 LCD Panel Configuration	59
	7.2 LCD Pin Assignment	59
	7.3 Description of LCD Panel Display	62
8.	SYSTEM CONFIGURATION EXAMPLE	68
9.	ELECTRICAL SPECIFICATIONS (PRELIMINARY)	69
10.	PACKAGE DRAWING	72
		70
11.	RECOMMENDED SOLDERING CONDITIONS	73
AP	PENDIX DESCRIPTION OF ELECTRONIC VOLUME CONTROL	.74
АР	PENDIX DESCRIPTION OF ELECTRONIC VOLUME CONTROL	
AP	Appendix 1. Function of Electronic Volume	74
AP		74 74
AP	Appendix 1. Function of Electronic Volume Appendix 2. Description of the Electronic Volume Control	74 74 75
AP	Appendix 1. Function of Electronic Volume Appendix 2. Description of the Electronic Volume Control Appendix 3. Initial Electronic Volume Value Settings	74 74 75 76
AP	 Appendix 1. Function of Electronic Volume Appendix 2. Description of the Electronic Volume Control Appendix 3. Initial Electronic Volume Value Settings Appendix 4. Electronic Volume Data Output Timing 	74 74 75 76
AP	 Appendix 1. Function of Electronic Volume	74 74 75 76 76 77
AP	 Appendix 1. Function of Electronic Volume	74 75 76 76 77 77
AP	Appendix 1. Function of Electronic Volume Appendix 2. Description of the Electronic Volume Control Appendix 3. Initial Electronic Volume Value Settings Appendix 4. Electronic Volume Data Output Timing Appendix 4.1 Initial data setting output timing Appendix 4.2 Volume data output timing Appendix 4.3 Loudness data output timing	74 75 76 76 77 77 77
AP	Appendix 1. Function of Electronic Volume Appendix 2. Description of the Electronic Volume Control Appendix 3. Initial Electronic Volume Value Settings Appendix 4. Electronic Volume Data Output Timing Appendix 4.1 Initial data setting output timing Appendix 4.2 Volume data output timing Appendix 4.3 Loudness data output timing Appendix 4.4 Attenuator data output timing	74 75 76 76 77 77 78 79

1. PIN FUNCTIONS

Pin No.	Symbol	Pin Name	Description	I/O Format
1	SK	SK signal input	This input pin detects the \overline{SK} signal (traffic information broadcasting station identification) of a VF broadcasting station. It is output at low level. If VF broadcasting stations are not in use, please pull it up.	Input
2	DK	DK signal input	This input pin detects the $\overline{\rm DK}$ signal (traffic information on-air identification) of a VF broadcasting station. It is output at low level. If VF broadcasting stations are not in use, pull it up.	Input
3	EO	Error out	This refers to the output from the charge pump of the PLL frequency synthesizer. If the value which has divided the local oscillator frequency is higher than the reference frequency, high level is output from these pins. If the value is lower, low level is output. If the value is the same as the reference frequency, it results in floating.	CMOS 3-state output
4 8	Vdd1 Vdd2	Power input	This is the pin for positive power. It supplies the 5 V ±10% voltage when running the CPU and peripheral devices. It is possible to retain data at 2.2 V when the clock has stopped. When Vbb is started up, the device is reset by the built-in power-ON reset circuit. Avoid applying a voltage higher than Vbb to any pins other than Vbb (Vbb1, Vbb2). Be careful about this especially when simultaneously starting up the Vbb and CE pins, because application of a higher voltage may cause latch-up. Ensure that the Vbb1 pin and the Vbb2 pin are connected to the same electric potential.	_
5	АМ	AM local oscillation input	This pin is for inputting the local oscillation output (VCO output) of the AM (MW, LW) band. When the MW or LW band is received, the pin becomes active; in other cases, it is internally pulled down. The inputtable frequency range is 0.5 to 30 MHz (0.3 V_{P-P}). Since the pin connects to a built-in AC amplifier, please use a capacitor to cut out the DC portion before inputting.	Input
6	FM	FM local oscillation input	This pin is for inputting the local oscillation output (VCO output) of the FM band. When the FM band is received, the pin becomes active; in other cases, it is internally pulled down. The inputtable frequency range is 9 to 150 MHz (0.3 V_{p-p}). Since the pin connects to a built-in AC amplifier, please use a capacitor to cut out the DC portion before inputting.	
7	CE	Chip enable	This is the device selection signal input pin. To make a device perform usual operations (such as radio, tape, CD, clock), high level is input. At low level, this pin is placed in the backup state, with the radio, tape, and CD turned OFF. It is possible to place this pin in the backup state with low current consumption by turning off the clock display (initialize diode NOCLK = 1).	Input

Pin No.	Symbol	Pin Name	Description	I/O Format
9	SCL	Clock signal output	This is the LCD controller/driver (µPD16431A) clock signal output pin.	CMOS push-pull output
10	SO	Serial data output	This is the LCD controller/driver (μ PD16431A) serial data output pin. For the connection to the μ PD16431A, refer to Figure 4-1 . μ PD16431A Pin Configuration.	CMOS push-pull output
11	SI	Serial data input	This is the LCD controller/driver (μPD16431A) serial data input pin. Connect pull-up resistor externally.	CMOS push-pull
12	FMIFC	FM intermediate frequency input	This is the intermediate frequency (IF) input pin for the FM band. The inputtable frequency range is 5 to 15 MHz (0.3 V _{P-P}). Since the pin connects to a built-in AC amplifier, use a capacitor to cut out the DC portion before inputting. This pin is used to detect the presence/absence of a broadcasting station during auto tuning when the initialize diode is set to "FM SD/IF switch = 1". The input frequency condition for determining that there is a broadcasting station is as follows:	Input
			Band Input Frequency Range	
			FM 10.7 MHz ± 12.5 kHz	
			The input frequency range refers to the frequency range in which input must be made within 10 ms after PLL is locked.	
13	AMIFC	AM intermediate frequency input	This is the intermediate frequency (IF) input pin for the AM band. The inputtable frequency range is 0.1 to 1.0 MHz (0.3 V _{P-P}). Since the pin connects to a built-in AC amplifier, please use a capacitor to cut out the DC portion before inputting. This pin is used to detect the presence/absence of a broadcasting station during auto tuning when the initialize diode is set to "AM SD/IF switch = 1". The input frequency condition for determining that there is a broadcasting station is as follows:	Input
			Band Input Frequency Range	
			$\frac{1}{450 \text{ kHz} \pm 3 \text{ kHz}}$	
			MW 459 kHz ± 3 kHz	
			450 kHz ± 3 kHz	
			LW 459 kHz ± 3 kHz	
			The input frequency range refers to the frequency range in which input must be made within 10 ms after PLL is locked.	
14	KREQ	Key request signal input	This input pin detects the key request signal of LCD controller/driver μ PD16431A. Key detection occurs at high level.	Input

Pin No.	Symbol	Pin Name	Description	I/O Format
15	SD	Station detection signal input	This is the input pin for detecting broadcasting station detection signal. This pin is used to detect the presence of a broadcasting station during auto-tuning. Also, for the case where initialization diode RETUNE = 0, if, while receiving a broadcasting station, this pin is at less than the SD input voltage continuously for at least about 20 seconds, an upward seek operation is started. After the PLL has locked, if a voltage greater than the SD input voltage is applied during the SD stabilization wait period, SD is assumed present. In RDS mode in the FM band, the pin is checked as one of the conditions for the start of AF operation.	Input
16	PANEL	Panel detach detection signal input	This is the detachable panel detection signal input pin. Low level is interpreted as panel in the mounted state; high level is interpreted as panel in the detached state.	Input
17	PWRIN	Power key input	This is the input pin for power key. It turns the power ON/OFF at the rising edge. Also, it operates the same as the POWER key of the momentary key.	Input
18	ALARM	Traffic information alarm output	This is the output pin for traffic information alarm. When there is no traffic information station identification signal in the TP/SK mode in the FM band, an alarm sound with a frequency of 3 kHz is output on and off about every 0.5 second.	CMOS push-pull output
19	BEEP	Beep output	 This is the beep output pin. It outputs about 40 ms of square waves with frequency of 3 kHz and duty factor of 50 %. The beeping sound is issued in the following cases: When a valid key is pressed When the hold of about 5 seconds during a preset scanning operation is ended When writing is performed in preset memory. 	CMOS push-pull output
20 21	BAND1 BAND0	Band switchover signal output	This is the band switchover signal output pin. If a new receiving band is switched to by means of the band switchover key, depending on the band, the following output. $ \begin{array}{c c c c c c c c c c c c c c c c c c c $	CMOS push-pull output
22 23	MODE1 MODE0	Mode signal output	These output pins show the operation mode of the μ PD17012GF-055. The modes are as follows:ModeMODE_0MODE_1Tuner00CD01Tape10(0 : low level1 : high level)	CMOS push-pull output

Pin No.	Symbol	Pin Name	Description	I/O Format
24 25	Xout Xin	Crystal resonator	This pin is for connecting the crystal resonator. A 4.5-MHz crystal resonator is connected. When using the clock function, the precision of the oscil- lator frequency affects that of the clock. Adjust the oscillator frequency while observing the PLL local oscillator frequency.	CMOS push-pull input
26	GND	Ground	This is the ground pin.	_
27	VOLSDA	Electronic volume data input/output	This is the serial data I/O pin with electronic volume. Because this is an N-ch open-drain output, connect pull-up resistor externally.	N-ch open-drain output
28	VOLSCL	Electronic volume data output	This is the serial data input pin with electronic volume. Because this is an N-ch open-drain output, connect pull-up resistor externally.	N-ch open-drain output
29	MUTE	MUTE signal output	This is the sound MUTE signal output pin. It is output at low level. It is used to remove the shock noise occurring when the PLL lock is not in place in RADIO mode, or to switchover the MODE pin output. For this is N-ch open-drain output, connect pull-up resistor externally.	N-ch open-drain output
30	IFCREQ	IF count request signal output	This is the IF count request signal output pin. It outputs high-level with SD during seek. Because this is an N-ch open-drain output, connect pull-up resistor externally.	N-ch open-drain output
31	CD	CD play signal input	This is the CD play signal input pin. By inputting high level to this pin, the sound source (MODE output) can be switched to CD. The CD play signal takes precedence over the tape signal.	Input
32	TAPE	Tape signal input	This is the tape signal input pin. By inputting high level to this pin, the sound source (MODE output) can be switched to tape.	Input
33	R/L	Tape run signal input	This is the tape run signal input pin.It is used for display on the LCD panel.Enter data as follows. R/L PinTape Run DirectionLow levelLeft \rightarrow rightHigh levelRight \rightarrow left	Input
34	STEREO	Stereo signal input	This is the stereo signal input signal. Enter data as follows.	Input
			STEREO Pin Description Low level Stereo broadcasting High level Monaural broadcasting Bands other than FM are invalid.	
35-41, 49-53, 58	IC	Internal connection	Connect this pin to the GND directly.	-

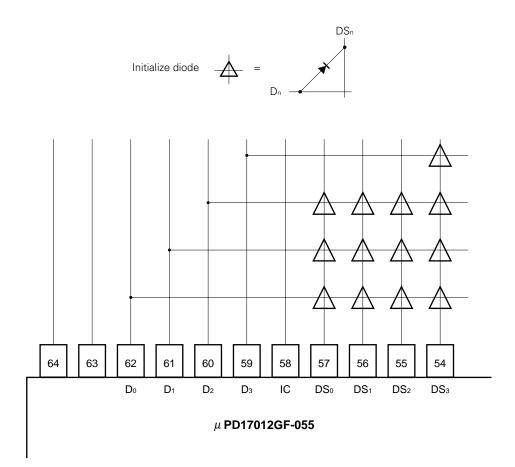
Pin No.	Symbol	Pin Name	Description	I/O Format
42	LPFSEL	LPF time-constant switchover signal output	This signal output pin is for switching over the LPF time- constant of the tuner during AF operation. During AF operation, high level is output as shown below. $LPFSEL 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\$	CMOS push-pull output
43	LCDSTB	Strobe signal output	This is the LCD controller/driver (μ PD16431A) strobe signal output pin. This is output at low level.	CMOS push-pull output
44	PWROUT	Power-ON detection signal output	This is the power-ON detection signal output pin.	CMOS push-pull output
45	MONO	Monaural output	This is the monaural output pin.	CMOS push-pull output
46	NR	Noise reduction output	This is the noise reduction output pin.	CMOS push-pull output
47	METAL	Metal output	This is the metal output pin.	CMOS push-pull output
48	LCDPWR	LCD power output	This is the power signal output pin of the LCD controller/driver (μ PD16431A).	CMOS push-pull output
54 57	DS₃ DS₀	Initialize diode source signal output	This is the source signal output pin of the initialize diode matrix.	CMOS push-pull output
59 62	D3 D0	Initialize diode return signal input	This is the return signal input terminal of the initialize diode matrix.	Input with pull-down resistor
63	RDSDATA	RDS data signal input	This is the RDS data signal input pin. Please input the data signal from the RDS signal detection part. Data is read on the falling edge of the RDS clock.	Input
64	RDSCLK	RDS clock input	This is the RDS clock input pin. Please enter the clock signal from the RDS signal detection part. Please input as accurate a clock as possible, because bit synchronization detection by clock signal width is not performed in the μ PD17012GF-055.	Input

2. KEY MATRIX CONFIGURATION

2.1 Allocation of Initialize Diode Matrix

Input Pin Output Pin	DS₃ (54)	DS2 (55)	DS1 (56)	DSº (57)
D ₀ (62)	FM SD/IF	AM SD/IF	AMIF1	AMIF2
D1 (61)	CLK24	FLASH	NOCLK	RETUNE
D ₂ (60)	MESEL	PRIDISP	VOLFUNC	PRIMANU
D₃ (59)	CLKDSP	—	—	—

2.2 Connection of Initialize Diode Matrix



2.3 Allocation of Momentary Key Matrix

• KS1-KS7 : Key source signal output (connected to pins 25-31 of μ PD16431A)

• KEY1-KEY4: Key return signal input (connected to pins 2-5 of μ PD16431A)

	KS1	KS2	KS3	KS4	KS5	KS6	KS7
KEY1	M1	M2	M3	M4	M5	M6	POWER
KEY2	PSCAN	ASM	MONO	PI	NR	METAL	_
KEY3	RDS	TP/SK	ME	DISP	SEEKUP (MANU.UP)	SEEKDOWN (MANU.DOWN)	_
KEY4	BAND	AUTO	ATT/LOUD	SELECT	VOLUP	VOLDOWN	—

Remark The values in the parentheses are valid only when the mode is set to SHIFT mode by the **SHIFT** key.

2.4 Description of Key Matrix

2.4.1 Initialize diode matrix

The initialize diode matrix contains the following 18 types (43 items). All these types are read only when the power is initially turned on (power-ON reset) to the V_{DD} pin on when the CE pin is changed from low level to high level. At other times, they are ignored.

- (1) Switch for setting the method of detecting broadcasting stations in auto tuning. FM SD/IF, AM SD/IF
- (2) Switch for setting the intermediate frequency of the AM band AMIF1, AMIF2
- (3) Switch for setting clock functions CLK24, FLASH, NOCLK, CLKDSP
- (4) Switch for selecting between ON/OFF of auto retune RETUNE
- (5) Switch for setting the ME key MESEL
- (6) Switch for selecting whether a priority display should be made PRIDISP
- (7) Switch for selecting electronic volume source switching VOLFUNC
- (8) Switch for selecting the tuning mode PRIMANU

These switches are set on the matrix after short-circuiting it with diodes.

Functions of the initialize diode matrix are described on the following pages. For the diodes, "1" indicates shortcircuited; and "0" indicates open.

	Description of Functions		
		detecting broadcasting stations during auto tuning of the FM band.	
FM SD/IF		Methods of Detecting Broadcasting Stations	
0	SD only used		
1	SD and IF co	punter	
(1: Short-cire	cuited with diod	e 0: Open)	
		detecting broadcasting stations during auto tuning of the AM band.	
AM SD/IF		Methods of Detecting Broadcasting Stations	
0	SD only used		
1	SD and IF co	punter	
(1: Short-cire	cuited with diod	e 0: Open)	
		e frequency of the AM (MW, LW) band.	
AMIF1	AMIF2	Intermediate Frequency	
0	0	450 kHz	
0	1	459 kHz	
1	×	10.71 MHz	
(1: Short-cire	cuited with diod	e 0: Open × : Don't care)	
		24-hour clock display.	
CLK24		Clock Display	
0	12-hr display	(with "AM", "PM" display)	
1	24-hr display	,	
(1: Short-circ	cuited with diod	e 0: Open)	
	-	ng in the clock display.	
FLASH		Colon (:) Display	
	Always ON		
0			
0	Flash		
1	-	: 1 Hz	
	It is set as follow FM SD/IF 0 1 (1: Short-circ This switch sets It is set as follow AM SD/IF 0 1 (1: Short-circ This switch sets It is set as follow AMIF1 0 1 (1: Short-circ This switch sets It is set as follow CLK24 0 1 (1: Short-circ This switch selec It is set as follow CLK24 0 1 (1: Short-circ This switch selec It is set as follow	It is set as follows: FM SD/IF 0 SD only used 1 SD and IF co. (1: Short-circuited with diod This switch sets the method of or It is set as follows: AM SD/IF 0 SD only used 1 SD and IF co. (1: Short-circuited with diod This switch sets the intermediat It is set as follows: AMIF1 AMIF2 0 0 1 X (1: Short-circuited with diod This switch sets the intermediat It is set as follows: AMIF1 AMIF2 0 0 1 X (1: Short-circuited with diod This switch selects a 12-hour or It is set as follows: CLK24 0 12-hr display 1 24-hr display 1 Short-circuited with diod This switch sets the colon lighting It is set as follows:	

Symbol	Description of Functions				
NOCLK	This switch sets the clock function. It is set as follows:				
	NOCLK Setting of Clock Functions				
	0	Clock present			
	1	Clock absent.			
		In this case, the FLASH, CLK24, and CLKDSP switches are ignored.			
	(1: Short-circ	uited with diode 0: Open)			
RETUNE	This switch selects ON/OFF of the auto retune. It is set as follows:				
	RETUNE	Auto Retune			
	0	On			
	1	Off			
	(1: Short-circ	uited with diode 0: Open)			
MESEL	This switch sets whether the ME key is valid or not. It is set as follows:				
	MESEL	ME key			
	0	Invalid			
	1	Valid			
	(1: Short-circ	uited with diode 0: Open)			
PRIDISP	This switch selects whether a priority display should be made or not. For details of the priority display, please refer to the description of the DISP key. The PRIDISP switch is set as follows:				
	PRIDISP	Priority Display			
	0	Absent			
	1	Present			
	(1: Short-circ	uited with diode 0: Open)			
VOLFUNC	This switch selects transmission of the sound source switching data to electronic volume. It is set as follows:				
	VOLFUNC	Transmission of the Sound Source Switching Data to Electronic Volume			
	0	Not transmit			
	1	Transmit			
	(1: Short-circuited with diode 0: Open)				

Symbol	Description of Functions			
PRIMANU	This switch sets auto/manual mode at the start. Set as follows.			
	PRIMANU	Auto/Manual Mode		
	0	Auto mode		
	1	Manual mode		
	(1: Short-cire	cuited with diode 0: Open)		
CLKDSP	This key selects Set as follows.	display/non-display of clock in the power-off state caused by the POWER key		
	CLKDSP	Display of Clock in the Power-off State		
	0	Display		
	1	Non-display		
	When initial setti Initial setting dio	cuited with diode 0: Open) ng diode NOCLK = 1, although display of clock is selected, clock is not displayed. de CLKDSP is linked with the control of power supply (LCDPWR pin) to LCD (µPD16431A) in the power-off state. The states are shown below.		
	CLKDSP	Description		
	0	No power is supplied to LCD driver/controller (μ PD16431A) in the power-off state. (LCDPWR pin goes to low level)		
	1	Power is supplied to LCD driver/controller (μ PD16431A) in the power-off state. (LCDPWR pin goes to high level)		
	(1: Short-cire	cuited with diode 0: Open)		

2.4.2 Momentary key

Symbol	Description of Functions		
M1 M2 M3 M4 M5 M6	 Performs reading or writing of the preset memory. The read/write procedure differs depending on the MESEL switch of the initialize diode. If MESEL = 0: If keys <u>M1</u> to <u>M6</u> are released within 2 seconds, it will result in a memory read operation. If they are kept pressed for 2 seconds or longer, it will result in memory write operation. If MESEL = 1: If the key is pressed while the ME segment of the LCD display is lit, it results in a memory write operation. If the key is pressed while the ME segment of the LCD display is unlit, it results in a memory read operation. 		
	If the station to be received is an RDS station, the PI code of the station is also memorized by the preset memory write operation. If the PI code is memorized in the preset memory, and the frequency cannot be received or the received PI code is different from that of the station, the PI search starts seeking the station that meets the stop condition, searching from the preset frequency upward. If it detects the station, it will receive from the station. If no station meets the stop condition, the PI search retains the initial frequency. When preset memory is called or written, band/preset is displayed for 5 seconds.		
POWER	This is the power key. By pressing this key, it switches power-ON/OFF.		

Symbol	Description of Functions		
PSCAN	By pressing this key, p If a preset memory is (for example, from M4 In other cases, each p memory 1. It is valid only in RADI If the preset station is searching. During the 5-second re performed. During preset scan op	nory scan operation key. preset scan operation starts. currently being received, reception is made from the next memory if M3 is being received). preset memory is received for five seconds sequentially starting from IO mode. an RDS broadcasting station, AF operation is performed but not PI eception period, no RDS operations such as AF judgment are eration, "PSCAN" display is lit on the LCD panel. The operation of each ration is shown below.	
	Кеу	Description of Operations	
	PSCAN	The preset scan operation is stopped and the frequency when the key was pressed is retained.	
	M1 M6	The preset scan operation is stopped, and preset memory of the pressed key is called.	
	SEEKUP SEEKDOWN BAND ASM	The preset scan operation is stopped, and the operation of the pressed key is performed.	
	AUTO SELECT VOLUP VOLDOWN ATT/LOUD	The preset scan operation is continued. The operation of the pressed key is performed.	
	TP/SK RDS PI MONO	 Upon reception of FM band The preset scan operation is continued. The operation of the pressed key is performed. Upon reception of AM band The preset scan operation is continued. The operation of the pressed key becomes invalid. 	

Symbol	Description of Functions		
ASM	 This is the auto store memory operation key. It is valid only in RADIO mode. If it is pressed, a search is made from the lowest frequency to the highest frequency of the relevant band to write six stations into preset memory in ascending order of the frequency, starting from the station whose SD is strongest. If the searched station is an RDS, PI code of the station is stored simultaneously. Data on the detected broadcasting stations are written to one preset memory after another, starting from preset memory 1. If the maximum frequency is reached before the six stations written, the previous frequencies stay in the remaining preset memories. If this key is pressed again during the auto store memory operation, the operation is suspen However, the broadcasting stations which have already been detected are written in the pre memory. During auto store memory operation, the band received and ASM are displayed. The operation of each key during an auto store memory operation is shown below. 		
	Key Description of Operations		
	ASM The auto store memory operation is stopped, and if more than one station is found, M1 is received. If no station is found, the frequency at the beginning of the operation is received.		
	SELECT The auto store memory operation is continued. VOLUP The operation of the pressed key is performed. VOLDOWN ATT/LOUD		
	Operating any momentary key other than above keys is invalid.		
MONO	This key sets forced monaural mode in RADIO mode. It is valid only when the FM band is selected in RADIO mode. If this key is pressed, the "MONO" display on the LCD panel is lit thus placing the device in forced monaural mode. During forced monaural mode, high level is output from the "MONO" pin. If this key is pressed again during forced monaural mode, the "MONO" display is extinguished and the mode is canceled. In the forced monaural mode, the "STEREO" display is unlit even when a stereo signal is input.		
PI	This is a key that sets permission for PI seek operation. When this key is pressed, the "PI" display lights on the LCD panel, and the PI seek permission status is entered. In the PI seek permission status, if a preset is called up, and the received PI code differs from the PI code stored in preset memory, PI seek is performed. When the key is pressed again while in PI seek permission status, the "PI" display is extinguished, and then even if the PI code stored in preset memory differs from the called out broadcast station PI code, no PI seek is performed.		
NR	This is the ON/OFF key for tape noise reduction. It is invalid in other than TAPE mode. If this key is pressed, the "NR" display on the LCD panel is lit, turning on NR. While the "NR" display is lit, high level is output from the NR pin. If this key is pressed again while the "NR" display is lit, the "NR" display is extinguished, thus turning off NR.		
METAL	This is the normal/metal tape switchover key. It is invalid in other than TAPE mode. If this key is pressed, the "METAL" display on the LCD panel is lit. While the "METAL" display is lit, high level is output from the METAL pin. If this key is pressed again while the "METAL" display is lit, the "METAL" display is extinguished.		

Symbol	Description of Functions
RDS	This key selects AF operation mode. It is valid only when the FM band is been received. If this key is pressed, the "RDS" display on the LCD panel lights thus placing the device in AF operation enable mode. While the "RDS" display is on, when the AF operation start condition is met, AF operations performed. If AUTO SEEK is performed by the SEEKUP / SEEKDOWN key in this mode, it results in operation of RDS broadcasting station detection. If the key is pressed again while the "RDS" display is lit, the "RDS" display is extinguished, thus placing the device in the AF operation disable mode. If an RDS broadcasting station is being received, incorporation of RDS data is performed regardless of the ON/OFF state of the "RDS" display.
TP/SK	This key selects traffic information interrupt operation mode. When this key is pressed, the "TP/SK" display on the LCD panel is lit placing the device in traffic information stand-by status. If both RDS TP and TA bits become 1 at this time, the device is placed in the traffic information interrupt status, thus displaying " T INF ."" on the 14 segments of the LCD panel and switching the sound to RADIO mode. If the TP/TA bit becomes other than 1 during the traffic information interrupt, the traffic informa- tion stand-by mode is restored and the sound before the traffic information interrupt is returned. If the key is pressed again while the "TP/SK" display is lit, the "TP/SK" display is extinguished and the traffic information interrupt at this time, the traffic infor- mation interrupt is canceled and the sound before the traffic information interrupt is returned. If the device is in the midst of a traffic information interrupt at this time, the traffic infor- mation interrupt is canceled and the sound before the traffic information interrupt is returned. While "TP/SK" remains unlit, no traffic information interrupt is accepted. The SEEK operation while the "TP/SK" display is lit is for searching traffic information stations only.

Symbol		Description of Functions
ME	When initial setting did While frequency, PS or on the LCD panel and memory key <u>M1</u> is written into the prese time, the frequency can the frequency and the If the <u>ME</u> becomes writable for a If tuning is in progress	preset memory writable. bde MESEL = 1, the key is used to write into preset memory. band/preset is being displayed, if the key is pressed, the "ME" display lights the memory becomes writable for 5 seconds. During this time, if a preset to <u>M6</u> is pressed, the frequency currently being received et memory. If the <u>ME</u> key is being pressed and held during this not be written. When an RDS station is being received in the FM band, both PI code of the station are written. key is again pressed while the "ME" display is lighted, then the memory another 5 seconds from the release of the key. a, the procedure has no effect. in the ME state is as follows.
	Кеу	Description of Operations
	M1 M6	Cancel preset memory writable state. When the key is pressed, the frequency being received is written in the corresponding preset memory.
	ME	Make preset memory writable for another 5 seconds after releasing the key.
	SEEKUPSEEKDOWNPSCANBANDASMAUTOSELECTVOLUPVOLDOWNATT/LOUDDISP	Cancel preset memory writable state. Do the function of the key pressed after the last channel.
	TP/SK RDS PI	 Upon reception of FM band The preset memory writable state is canceled and the operation of the pressed key is performed. Upon reception of AM band The operation of the pressed key becomes invalid.
	Momentary keys other	than those given above have no effect.

Symbol	Description of Functions				
DISP	This key switches the display and adjusts the clock in conjunction with SEEKUP SEEKDOWN (1) Display switchover function Pressing the key switches the display.				
	<1> Operation in RADIO mode While PS is in operation, pressing the key changes the screen to frequency display. While frequency is being displayed, if the initial setting diode NOCLK = 0, pressing the key changes the screen to clock display; if NOCLK = 1 and PS code is loaded on the FM band pressing the key changes the screen to PS display; otherwise, the key has no effect. While clock, band/preset, or electronic volume is being displayed, if PS code is loaded on the FM band, pressing the key changes the screen to PS display; otherwise, pressing the key changes the screen to frequency display. Even if PRIDISP = 0, when PS code is loaded, the frequency display changes to PS display in 5 seconds.				
	 (PS) -> Frequency -> (Clock) Invalid About 5 seconds If the screen shows the clock display, electronic volume display or band/preset display 				
	<2> Operation in TAPE/CD mode If "T I I E" or "[]]" is being displayed, and initialize diode NOCLK = 0, pressing the key changes the screen to clock display; NOCLK = 1, the key has no effect. While the clock or electronic volume is being displayed, pressing the key changes the screen to "T I I E" or "[]]". T I I E " or "[]]" display — Clock display				
	Invalid If the screen shows the - clock display or electronic volume display				

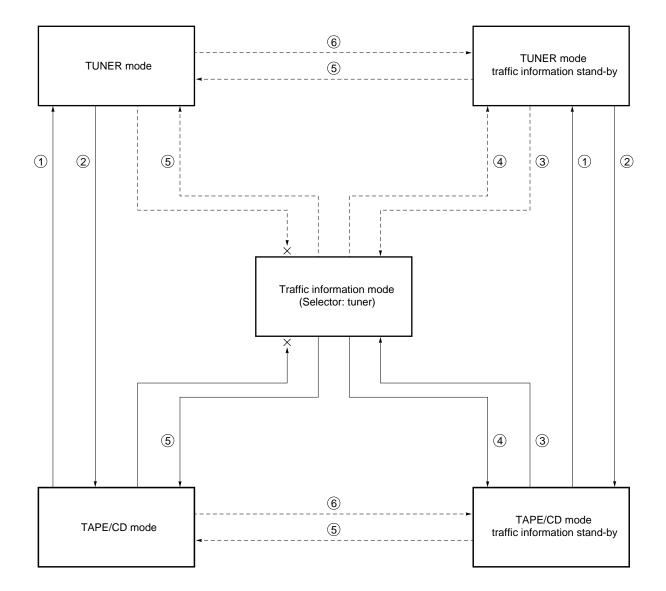
Symbol	Description of Functions
DISP	<3> Operation in ALARM/traffic information interrupt mode While "ALARM" is displayed, the key has no effect. While "TINF" is displayed, if PS code is loaded, pressing the key changes the screen to the PS display; otherwise, pressing the key changes the screen to frequency display. While PS is displayed, pressing the key changes the screen to the frequency display. While the frequency is displayed, if the initialize diode NOCLK = 0, pressing the key changes the screen to the clock display; NOCLK = 1, pressing the key changes the screen to the "TINF"" display. While the clock or electronic volume is displayed (volume, bass, treble, balance, fader), pressing the key changes the screen to "TINF"". Even if the initialize diode PRIDISP = 0, the screen changes from frequency to "TINF"" in 5 seconds.
	<pre>if INF: [] " display -+ (PS) -+ Frequency+ (Clock)</pre>

Description of Functions		
While the frequency is being displayed, the key operates as the auto seek/manual seek operat function key for the receiving frequency. While the clock is being displayed, the key operates the clock adjustment key in conjunction with the DISP key.		
 (1) Auto seek operation While the "AUTO" display is lit, auto seek is in operation. Auto seek operation is divided into three modes by the setting the current RDS and traffic information stand-by. 		
<1> Normal seek mode Pressing the key starts auto seek from the current frequency upwards (<u>SEEKUP</u> key) or downwards (<u>SEEKDOWN</u> key) in single steps (100 kHz per step in the FM band) and lights the "AUTO" display. The presence/absence of a broadcasting station is detected at each frequency. If a station is detected, the frequency is retained. For a seek operation on LW, even if a broadcasting station is detected, if the frequency is not multiple of 9, the seek operation continues.		
<2> RDS seek mode While the "RDS" display is lit in the FM band, pressing the key changes the mode to RDS seek mode. RDS seek mode operation is the same as normal seek mode operation until a broadcasting station is detected. However, when a station is found, RDS seek mode operation begins RDS signal synchronous detection, checks the synchronous status after 200 ms, and if an RDS station is detected (synchronous RDS signal is detected), the frequency is retained. If an RDS station is not detected, the seek operation continues.		
 <3> TP/SK seek mode While the "TP/SK" display is lit, pressing the key in the FM band changes the mode to TP/SK mode. TP/SK seek mode operation is the same as RDS seek mode operation until an RDS broadcasting station is detected. However, when a station is found, TP/SK seek mode begins TP/SK signal synchronous detection, checks the RDS data TP bit after 300 ms, and if TP is detected, the frequency is retained. If TP is not detected, the seek operation continues. If an RDS station is not detected, the SK pin is checked after 300 ms, and if an SK signal is detected, the frequency is retained. If SK is not detected, the seek operation continues. 		
key operation during auto seek operation is as follows.		
Key Description of Operations		
M1 The seek operation is stopped. Call up the content of the preset memory for the pressed key. M6 Writing is inhibited.		

Symbol	Description of Functions	
SEEKUP (MANU.UP)		
	Key Description of Operations	
SEEKDOWN (MANU.DOWN)	SEEKDOWN while seek down is in progress, pressing the SEEKDOWN stops the seek operation and retains the frequency at that • While seek up is in prgress, pressing the SEEKDOWN k while seek down is in progress, pressing the SEEKUP	time. key, and key
	changes the operation. (e.g. Press the SEEKUP key to seek up.)	changes
	PSCAN The seek operation is stopped. BAND The operation of the pressed key is performed. ASM AUTO	
	SELECT The seek operation is continued. VOLUP The operation of the pressed key is performed. VOLDOWN ATT/LOUD	
	TP/SK • Upon reception of FM band RDS The seek operation is continued. The operation of the pressed key is started. • Upon reception of AM band It becomes invalid. The seek operation is continued.	
	 Momentary keys other than the above have no effect. (2) Manual seek operation Pressing the key updates the frequency from the frequency when the key was press upward direction (<u>SEEKUP</u> key) or downward direction (<u>SEEKDOWN</u> key) Pressing and holding the key for more than 500 ms updates the frequency every 4 the key is released. While receiving an RDS station, pressing the key clears PI code settings. While a manual seek operation is in progress, pressing and holding the key renders keys ineffective.	by 1 step. 0 ms until
	(3) Clock adjustment operation While the clock is being displayed, pressing the key and the DISP key adjusts the hour (SEEKUP key) and minute (SEEKDOWN key).	y together
	<1> Adjusting the hour Pressing the SEEKDOWN key once adds an hour; holding the key down for more seconds adds an hour every 200 ms. The hour adjustment operation affects neither t digit nor count value.	

Symbol	Description of Functions								
SEEKUP (MANU.UP) SEEKDOWN (MANU.DOWN)	<2> Adjusting the minute Pressing the <u>SEEKUP</u> key once adds a minute; holding the key down for more than 0.5 seconds adds a minute every 100 ms. The minute adjustment operation does not affect the hour. Whenever the minute is adjusted, the value of the second counter is reset.								
BAND	This key switches the radio band. While the sound is set to radio mode, pressing the key switches the band as follows. FM1 \rightarrow FM2 \rightarrow AM Every time the band is switched, band/preset displays for 5 seconds.								
AUTO	This key switches auto seek/manual seek. While the "AUTO" display on the LCD panel is not lighted, pressing the key lights the "AUTO" display. Subsequently pressing the SEEKUP / SEEKDOWN key operates auto seek up/ down. While the "AUTO" display on the LCD panel is lighted, pressing the key turns off the display. Subsequently pressing the SEEKUP / SEEKDOWN key operates manual seek up/down. If the mode is other than radio mode, the key has no effect.								
ATT/LOUD	This key sets attenuator/loudness operation. Pressing the key and releasing it within 2 seconds turns on the attenuator and lights the "ATT" display on LCD panel. While "ATT" is displayed, pressing the key again for less than about 2 seconds switches off the "ATT" display and turn off attenuator. Pressing the key for more than 2 seconds turns on loudness and switches on the "LOUD" display on the LCD panel. While "LOUD" is displayed, pressing the key again for more than about 2 seconds switches off the "LOUD" display and turns off loudness.								
SELECT	The key selects the electronic volume adjustment function. Pressing the key switchs the display on the LCD panel to the electronic volume display. Pressing it again switches the adjustment function as follows. \rightarrow Bass \rightarrow Treble \rightarrow Balance \rightarrow Fader \rightarrow Volume								
	While the electronic volume is displayed, you press the VOLUP, VOLDOWN keys for each adjustment function. Regarding the electronic volume displays, even if the initial setting diode PRIDISP = 1, releasing any electronic volume adjustment key (SELECT, VOLUP, VOLDOWN) changes the screen to the highest priority display in about 5 seconds.								
VOLUP VOLDOWN	This key adjusts any electronic volume function. When the display on the LCD panel is other than electronic volume, pressing the key displays electronic volume and pressing VOLUP or VOLDOWN increases or decreases the sound volume. While the electronic volume is displayed on the LCD panel, pressing the key adjusts the function currently selected (bass, treble, balance, fader, or volume).								

3. MODE TRANSITIONS



- ------- : Actural mode (MODE pin output, MUTE, etc.) changes.
- -----: Actual mode does not change.
- \times ----- : This mode cannot be changed.
 - (1) : TAPE pin = low level ; and CD mode is OFF.
 - (2) : TAPE pin = high level ; or CD mode is ON.
 - ③:TA or DK ON
 - (4): TA or DK OFF
 - ⑤ : TP/SK mode OFF
 - 6 : TP/SK mode ON

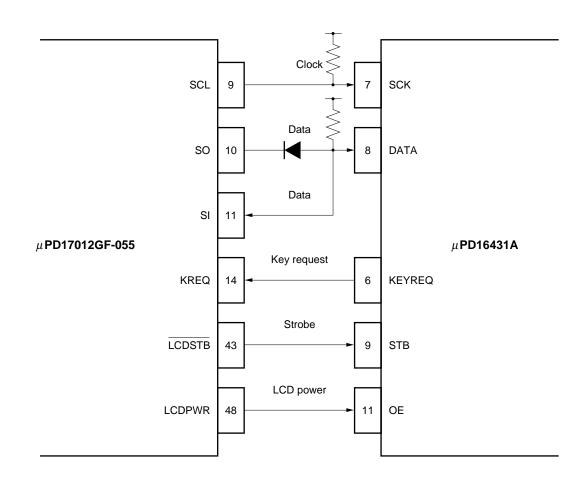
4. DATA OUTPUT TO LCD CONTROLLER/DRIVER (µPD16431A)

The $\mu PD17012GF\text{-}055$ uses the $\mu PD16431A$ for LCD display and key sensing.

The μ PD17012GF-055 transfers initialization data to the μ PD16431A about 480 to 500 ms after the LCDPWR pin (pin 48) changes from low level to high level.

The pin configurations of the μ PD17012GF-055 and the μ PD16431A are shown below.

Figure 4-1. μ PD16431A Pin Configuration

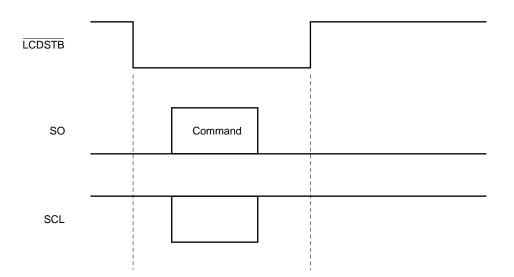


4.1 Data Input/Output Timing

(1) Initialization data output

The initialization data output to the μ PD16431A is shown in Figure 4-2.

Figure 4-2. Initialization Data Output



Command: 00000000 (Initialization command)

1/4 duties, (fosc/128)×n, internal driving voltage, master, and normal operation are initialized.

(2) Display data output

The display data output to the μ PD16431A is shown in Figure 4-3.

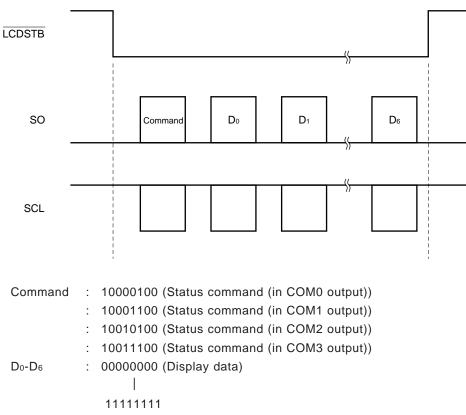


Figure 4-3. Display Data Output

The display output above is repeated four times by approx. 10 ms and the display data is transmitted. At this time, the status commands COM0 to COM3 are transmitted.

(3) Key data input/output

The key data input/output to μ PD16431A are shown in Figure 4-4.

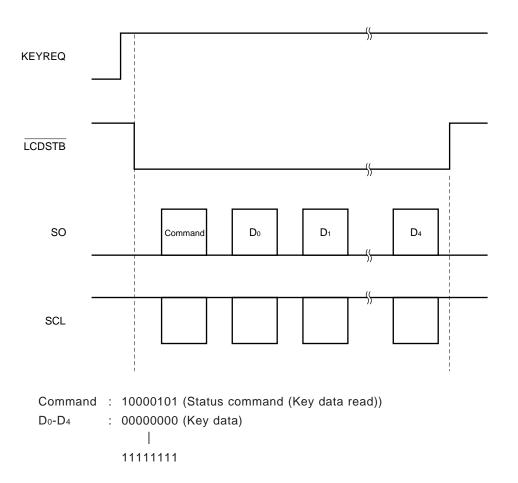


Figure 4-4. Key Data Input/Output

5. RDS (Radio Data System) FUNCTION

5.1 RDS Data Incorporation

 μ PD17012GF-055 internally decodes RDSDATA and RDSCLK from the RDS-compound IC. Synchronization detection is limited to block synchronization, and error correction is not performed.

Block synchronization is detected for the following four types of block patterns.

- 1 : A-B-C-D
- 2 : A-B-C'-D
- 3 : A-B-E-E
- 4 : A–B–F–F

The method of detecting synchronization is as follows: the synchronous status is checked per block from the current point to 5 previous blocks before; if synchronization of three or more blocks out of 5 blocks is detected, it is judged that block synchronization exists.

If block synchronization is not obtained for 1.5 seconds or more, the status of each of TP, TA, and PTY is cleared. If an error is detected from the incorporated blocks and if block synchronization has taken place, synchronization detection is performed every 26 bits until the block synchronization is removed.

ОК	ОК	NG	ОК	NG	NG	OK	ОК	OK	ОК	
 A	В	С	D	A	В	с	D	A	В	
Î Î		È constant de la constant	Î Â	 	↑ ^	1		Î Î	1	`
* :		re judged	* :		Blocks a	* re judged /nchronized.		* 1	Blocks ar to be sync	e judged

Figure 5-1. Block Synchronization Detection

(NG0 time) (NG1 time) (NG1 time) (NG2 times)(NG3 times)(NG2 times)(NG2 times)(NG2 times) (NG1 time)

Block synchronization status

Block asynchronization status

- * : The synchronization status for the preceding 5 blocks is checked. In this case, if at least 3 blocks are not synchronized out of 5 blocks, the blocks are judged to be asynchronous.
- A to D : Indicates offset check words.

5.2 RDS Data Processing

The μ PD17012GF-055 contains an RDS data decoder part. The μ PD17012GF-055 uses the following six types of data:

- (1) PI (Program Identification)
- (2) PS (Program Service Name)
- (3) PTY (Program Type)
- (4) AF (Alternative Frequency)
- (5) TP (Traffic Program Identification)
- (6) TA (Traffic Announcement Identification)

5.2.1 PI (Program Identification)

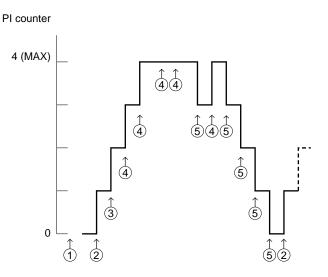
This data is used for program identification.

After a tuning operation is completed, receipt of the same PI code two or more times causes the RDS data that has that PI code to be decoded. The PI counter can be incremented up to four counts.

When RDS data of a different PI code is received, the PI counter is decremented. RDS data other than TP and TA at this time are not decoded.

If the PI counter is decremented to zero, the different PI code is judged to be a new correct PI code, thus incrementing the PI counter; when the maximum count of the PI counter is reached two or more times, the RDS data is decoded.





- (1) : Tuning operation end
- (2): The PI code is entered in the PI code area for comparison. Counter + 1
- (3): The PI code is compared with the PI code. Counter + 1 when the two codes are the same.
- (4) : The PI code is compared with the PI code. Counter + 1 when the two codes are the same. RDS data is decoded.
- (5): The PI code is compared with the PI code. Counter 1 when the two codes are different.

5.2.2 PS (Program Service Name)

This data is used for the PS display.

The PS display is made once when the same PS data is input two or more times.

The PS display appears about 5 seconds after the tuning operation is completed. If the PS data cannot be received during about 5 seconds, the PS display appears when the PS data is eventually received.

Once PS data has been received, if the display is changed by the **DISP** key or the TP/SK mode ON/ OFF change is performed even if no PS data can be received thereafter, the last PS data is stored, and 3 seconds after displaying frequency, the stored PS data is displayed.

5.2.3 PTY (Program Type)

Used for alarm identification. When the alarm is received, the device is switched over to RADIO mode if in TAPE/ CD mode.

At this time, the LCD panel displays " $\Pi [\Pi \square M".$

5.2.4 AF (Alternative Frequency)

Used as a switchover frequency list.

(1) AF list input

The AF function can be used for both METHOD A and METHOD B. Up to 25 lists can be input. If AF's header block is received, the AF pointer is reset to the front and the lists are stored in the order of their transmission. If more than 25 AF lists are sent, data is overwritten starting from the top of the lists. If, in METHOD B, blocks for the same frequency arrive one after another, they are linked together to form a single AF list. Even when lists are sent in pairs of a descending sequence, all the AF lists are input.

The method of input for AF lists is shown in Figure 5-3 Flow of AF List Input.

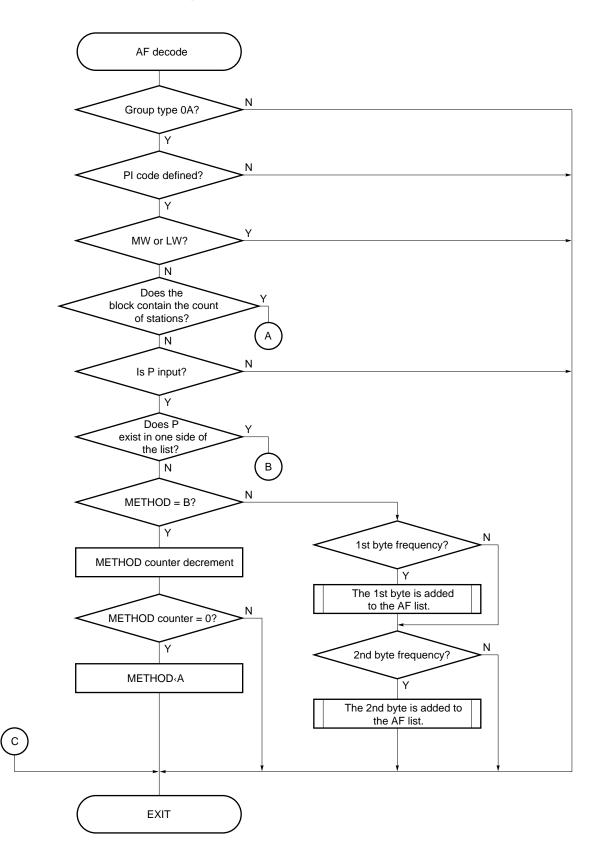
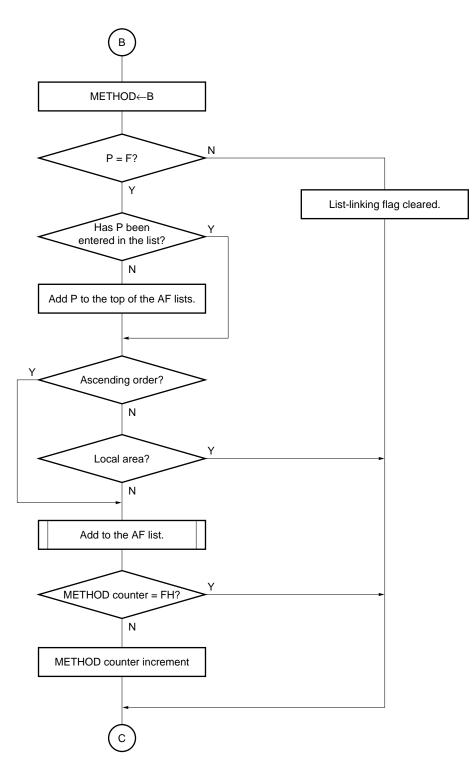


Figure 5-3. Flow of AF List Input (1/3)





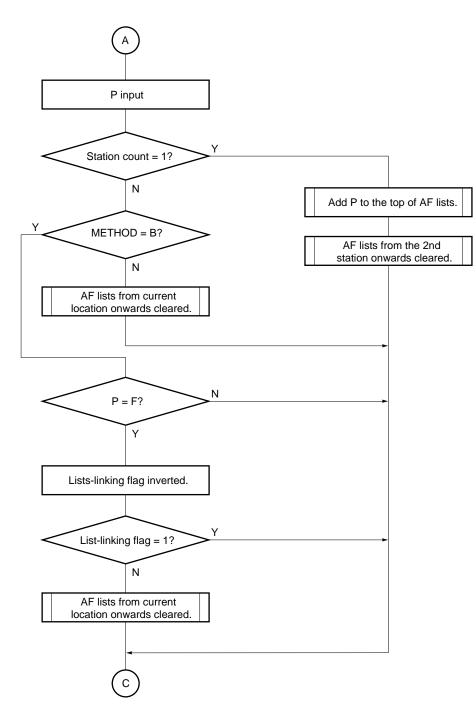


Figure 5-3. Flow of AF List Input (3/3)

- P: Frequency entered in blocks containing the station count
- F : Tuning frequency

(2) AF check

Data of the AF operation memory is used for AF checking. One station is checked at a time.

1 AF check start condition:

The electric field strength of the broadcast being received is divided into the following three stages (the numeric values are those when the read A/D values are 0-3FH) by the voltage which is input from SD pin.

 $\mathsf{L} < \mathsf{0CH} < \mathsf{M} \leq \mathsf{20H} \leq \mathsf{H}$

SD Level	Description of Operations
Н	AF check not started.
М	AF check started on a station every 5 seconds.
L	All the AFs at the time checked at once.

(2) AF check stop condition:

AF checking is stopped when the SD level of the AF-checked broadcast is higher than that of the broadcast originally being listened to, and the PI of the broadcast at the check destination has satisfied the STOP condition.

Conditions for broadcast (PI) capable of AF switchover are as follows:

- Broadcast whose 16 bits are completely identical, including the PI code and the area cover code of the broadcast being currently received.
- Broadcast whose area cover code is '4' to 'F' when the PI code and the area cover code of the broadcast being currently received is '1' to '3', and whose remaining 12 bits are identical.
- Broadcast whose area cover code is '1' to '3' when the PI code and the area cover code of the broadcast being currently received is '4' to 'F', and whose remaining 12 bits are identical.
- (3) AF check for calling when CE low level has been switched over to high level or when the last station is an FM RDS station:

AF checking is performed for calling when the CE low level has been switched over to low level or when the last station is an FM RDS station.

At this time, the AF data of the last channel memory is moved to the AF operation memory, and immediately all the AFs are checked. The broadcasting station whose PI stored in the preset memory has satisfied the STOP condition and whose SD level is the highest is selected for reception. This AF checking is performed during MUTE output.

If no station satisfies the STOP condition as a result of checking the broadcast stations of the preset frequency and all their AF, the SEEK operation is performed upwards from the preset frequency to detect the broadcasting station which satisfies the STOP condition.

If a broadcasting station which satisfies the STOP condition appears, the SEEK operation is halted to receive the relevant broadcasting station (PI search). Even if the SEEK operation is carried out across the entire width of reception band, if a broadcasting station which satisfies the STOP condition cannot be found, the SEEK operation is halted to receive the originally set frequency.

5.2.5 TP (Traffic Program Identification), TA (Traffic Announcement Identification)

These are used for traffic information station identification and traffic information announcement identification. Depending on the TP and TA statuses of the current receiving station, the methods of identifying a traffic information station are as follows:

• If TP = 1,

Recognized as the traffic information station.

TP and TA are decoded by inputting the same data at least twice after the tuning operation is ended. If 1 is input as the TP or TA data, the TP or TA counter is incremented up to 4 counts. If 0 is input, the counter is decremented; and when the counter becomes 0, it is determined that there is no TP or TA.

The method of identifying the traffic information announcement is as follows.

• If it results that TA=1 when TP=1, the traffic information is recognized as being broadcast.

The methods of switching over to the traffic information are as follows:

• If TA = 1,

If the device is in TAPE/CD mode, it is switched over to RADIO mode.

 If TA = 0, Returns to the original mode.

6. MUTE TIMING

6.1 Tuner Operation

The operation of the tuner function and the output of the MUTE pin are explained in the following order.

- (1) Preset memory reading (refer to 6.1.1 Preset memory reading)
- (2) Preset scan (refer to 6.1.2 Preset scan)
- (3) Preset memory writing (refer to 6.1.3 Preset memory writing)
- (4) Seek up/down (refer to 6.1.4 Seek up/down)
- (5) Manual up/down (refer to 6.1.5 Manual up/down)
- (6) Auto store memory (refer to 6.1.6 Auto store memory)
- (7) AF switchover (refer to 6.1.7 AF switchover)

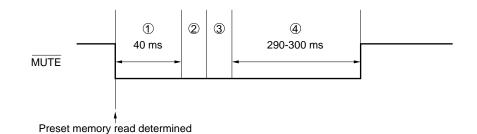
6.1.1 Preset memory reading

Reading in the preset memory is performed by pressing key M1 to M6 for less than 2 seconds when initialize diode MESEL = 0 in TUNER mode, or by pressing these keys in modes other than preset memory write enable mode when MESEL = 1.

The timing chart showing the preset memory reading operation is shown below.

PI seek is performed if the broadcasting station being read is an FM RDS station.

Figure 6-1. Timing Chart in Preset Memory Read (1/3) (When the Broadcasting Station Being Read Is Not RDS Station)



- (1) MUTE first-out and beep output
- (2) Frequency division ratio setting
- ③ PLL lock wait
- (4) MUTE last-out output

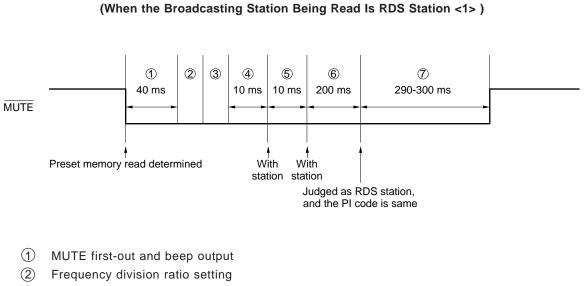


Figure 6-1. Timing Chart in Preset Memory Read (2/3)

- ③ PLL lock wait
- (d) SD stability wait (1)
- 5 SD stability wait (2)
- 6 RDS station judgment wait
- ⑦ MUTE last-out output

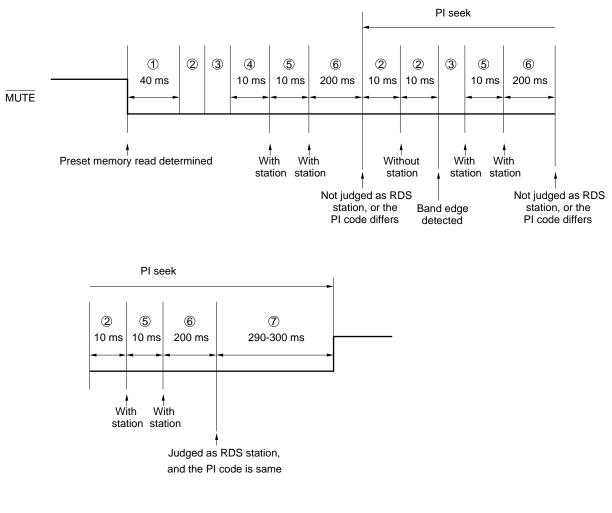


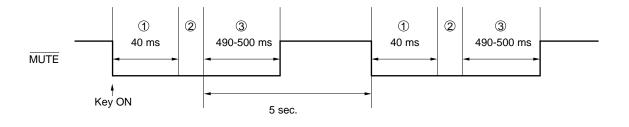
Figure 6-1. Timing Chart in Preset Memory Read (3/3) (When the Broadcasting Station Being Read Is RDS Station <2>)

- (1) MUTE first-out and beep output
- 2 Frequency division ratio setting
- ③ PLL lock wait
- (4) SD stability wait (1)
- (5) SD stability wait (2)
- 6 RDS station judgment wait
- ⑦ MUTE last-out output

6.1.2 Preset scan

The preset scan operation is started by pressing the **PSCAN** key in TUNER mode. The timing chart showing the preset scan operation is shown below.

Figure 6-2. Timing Chart in Preset Scanning



- ① MUTE first-out and beep output
- 2 Frequency division ratio setting
- ③ MUTE last-out output

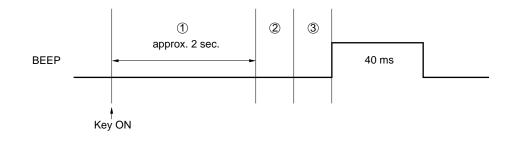
6.1.3 Preset memory writing

Writing in the preset memory is performed by pressing key <u>M1</u> to <u>M6</u> for 2 seconds or longer when initialize diode MESEL = 0 in TUNER mode, or by pressing these keys in the preset memory write enable mode when MESEL = 1.

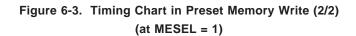
The timing chart showing the preset memory writing operation is shown below.

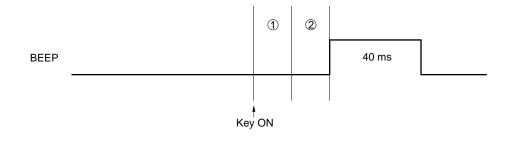
If the station being received is an FM RDS station, PI code of the station is written simultaneously.





- 1 Preset memory read/write judgment wait
- 2 Preset memory write
- ③ PI code write (upon reception of the FM RDS station)





- ① Preset memory write
- ② PI code write (upon reception of the FM RDS station)

6.1.4 Seek up/down

The operation is started by pressing **SEEKUP** / **SEEKDOWN** when the device is in TUNER mode but other than in SHIFT mode.

The broadcasting station detection operation judges the IF count if the SD level and initialize diodes (AM SD/IF, FM SD/IF) are ON and terminates the SEEK operation if the condition for "with broadcasting station" is satisfied twice at the interval of 10 ms. In RDS mode and TP/SK mode, after the condition above has been satisfied, the operations of detecting the RDS broadcasting station and the traffic information station are performed according to the timing chart below.

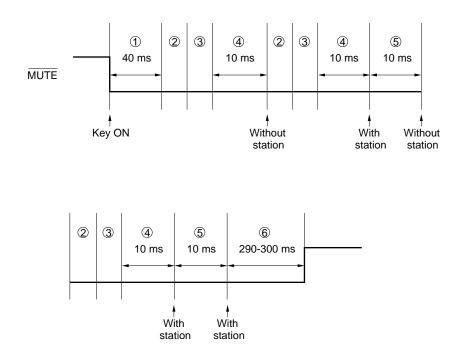


Figure 6-4. Timing Chart in Seek Up/Down (1/3) (Normal Mode)

- ① MUTE first-out and beep output
- ② Frequency division ratio setting
- ③ PLL lock wait
- ④ SD stability wait (1)
- (5) SD stability wait (2)
- 6 MUTE last-out output (490-500 ms in band edge detection)

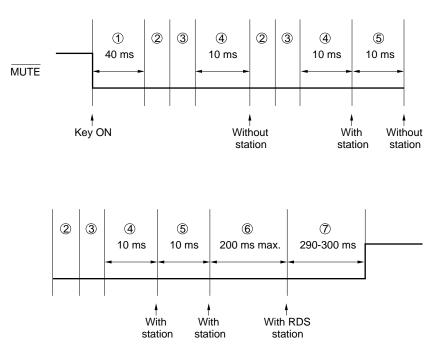


Figure 6-4. Timing Chart in Seek Up/Down (2/3) (RDS Mode)

- 1 MUTE first-out and beep output
- 2 Frequency division ratio setting
- ③ PLL lock wait
- ④ SD stability wait (1)
- (5) SD stability wait (2)
- 6 RDS station detection wait
- ⑦ MUTE last-out output (490-500 ms in band edge detection)

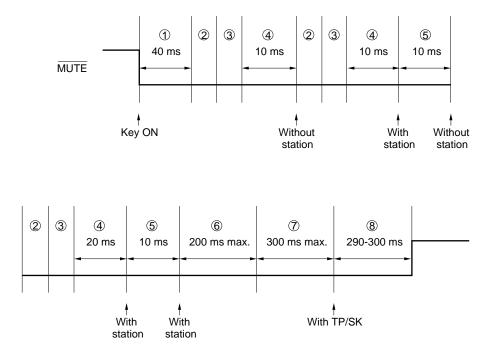


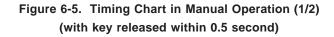
Figure 6-4. Timing Chart in Seek Up/Down (3/3) (TP/SK Mode)

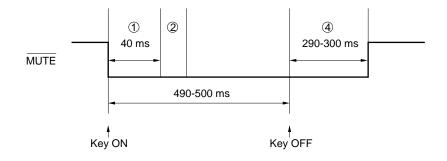
- 1 MUTE first-out and beep output
- 2 Frequency division ratio setting
- ③ PLL lock wait
- (4) SD stability wait (1)
- (5) SD stability wait (2)
- (6) RDS station detection wait
- (7) Traffic information station identification (TP/SK) wait
- (8) MUTE last-out output (490-500 ms in band edge detection)

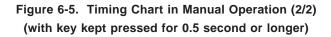
6.1.5 Manual up/down

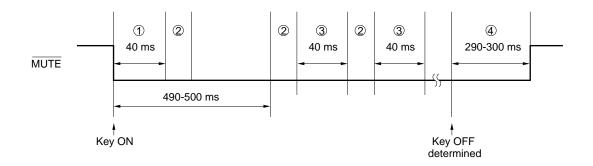
The operation is started by pressing **SEEKUP** / **SEEKDOWN** when the device is in TUNER mode and in SHIFT mode.

The timing chart below shows the manual operation.









- (1) MUTE first-out and beep output
- 2 Frequency division ratio setting
- ③ Key repeat time (500 ms for both AM and FM when the band edge has been detected.)
- ④ MUTE last-out output (490-500 ms in band edge detection)

6.1.6 Auto store memory

The operation is started by pressing the **ASM** key when the device is in TUNER mode. The timing chart below shows the auto store memory operation.

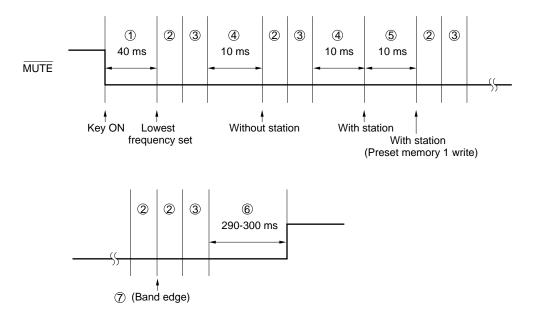


Figure 6-6. Timing Chart in Auto Store Memory

- 1 MUTE first-out and beep output
- (2) Frequency division ratio setting
- ③ PLL lock wait
- (4) SD stability wait (1)
- (5) SD stability wait (2)
- 6 MUTE last-out output
- (7) ASM end. Sorted in the ascending order of the frequency to call preset memory M1. If no station is detected, the frequency before pressing the key is retained. If the relevant station is detected after writing the preset memories up to M6, it is compared with the SD levels of the written preset memories and stored whose SD levels is high.

6.1.7 AF switchover

There are two types of AF switchover operations.

- (1) AF switchover of all stations at once (refer to Figure 6-7)
- (2) AF switchover of a station at a time (Interval 5 seconds (refer to Figure 6-8))

The timing charts of the respective operations are shown below and on the following page. For conditions for occurrence of the AF operations, please refer to **5.2.4** AF (Alternative Frequency).

(1) All-Station AF switchover at once

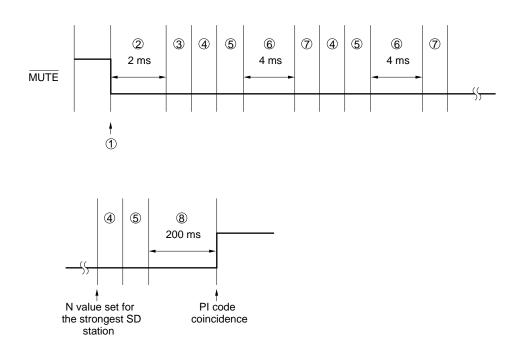


Figure 6-7. Timing Chart for All-Station AF Switchover

- 1 Occurrence of AF switchover condition
- (2) MUTE first-out wait
- ③ SD sort (Stations with SD are determined beforehand on the AF list and sorted in the order of the frequency.)
- (4) Frequency division ratio setting
- 5 PLL lock wait
- 6 SD judgment wait
- ⑦ SD level comparison
- 8 PI code input, judgment wait

(2) One-station AF switchover at a time

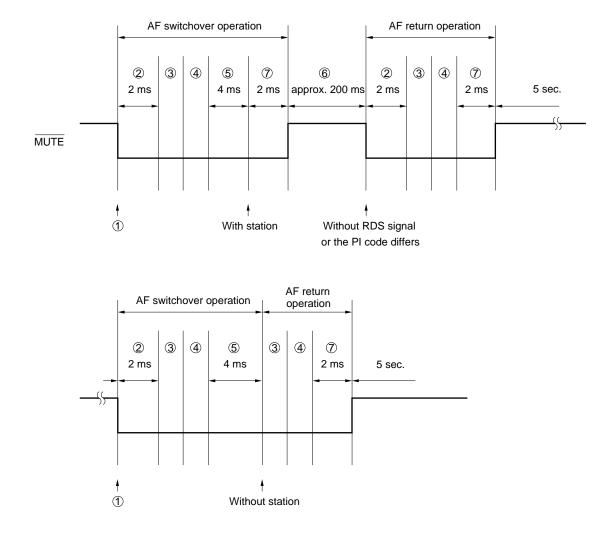


Figure 6-8. Timing Chart for One-Station AF Switchover (1/2)

- ① Occurrence of AF switchover condition
- 2 MUTE first-out wait
- ③ Frequency division ratio setting
- ④ PLL lock wait
- 5 SD stability/IF count wait
- 6 RDS station detection/PI code input wait
- ⑦ MUTE last-out output

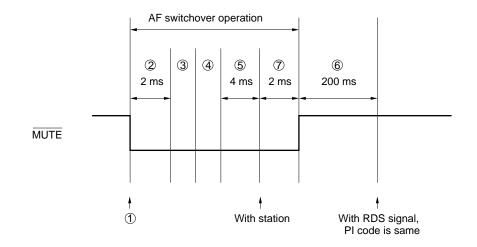
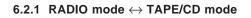


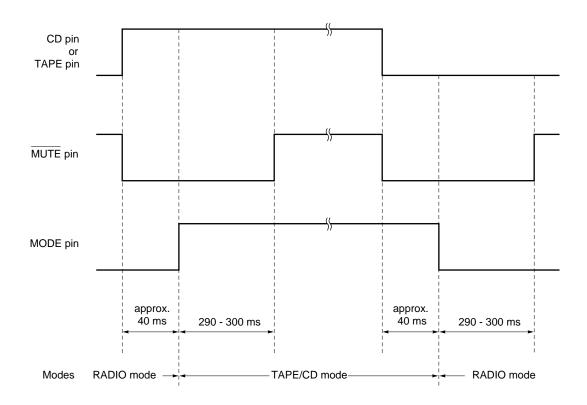
Figure 6-8. Timing Chart for One-Station AF Switchover (2/2)

- (1) Occurrence of AF switchover condition
- 2 MUTE first-out wait
- ③ Frequency division ratio setting
- ④ PLL lock wait
- 5 SD stability/IF count wait
- 6 RDS station detection/PI code input wait
- ⑦ MUTE last-out output

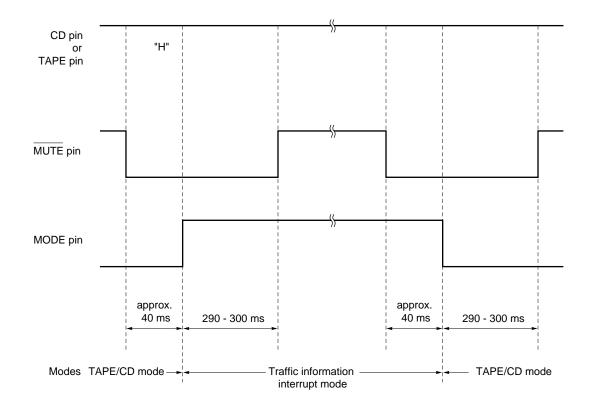
6.2 Mode Switchover

The mode pin switchover and the MUTE output timing chart are shown below.





6.2.2 Traffic information broadcasting/PTY alarm ↔ TAPE/CD mode (TP/SK mode)

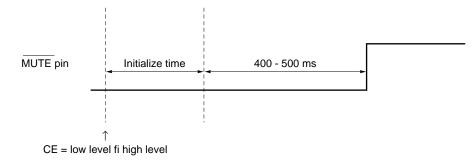


6.3 CE Pin

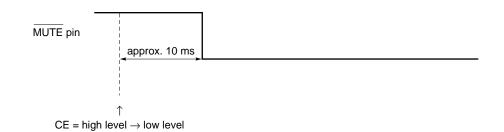
NEC

The MUTE output timing charts in level change of the CE pin are shown below.

6.3.1 Low level \rightarrow high level

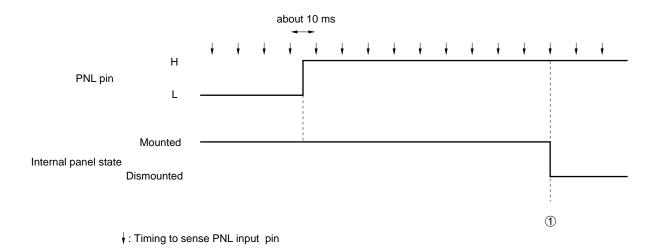


6.3.2 High level \rightarrow low level



6.4 Detecting the Detachable Panel

Timing chart for eliminating chattering

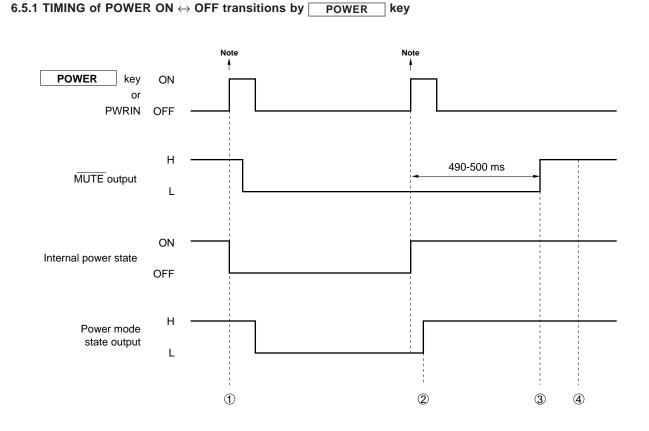


If high level on the PNL pin is detected 10 times in succession, the panel is regarded as being dismounted (1) and the power is unconditionally turned off.

Detection of the mounted state proceeds according to the same timing.

If the panel is regarded as being mounted, the power state is checked when the panel is dismounted. If the panel was dismounted in the power-on state, power-on is set. (For details about power transitions, refer to 6.5.2. Timing of POWER ON \leftrightarrow POWER OFF transitions by detachable panel)

6.5 Power Control

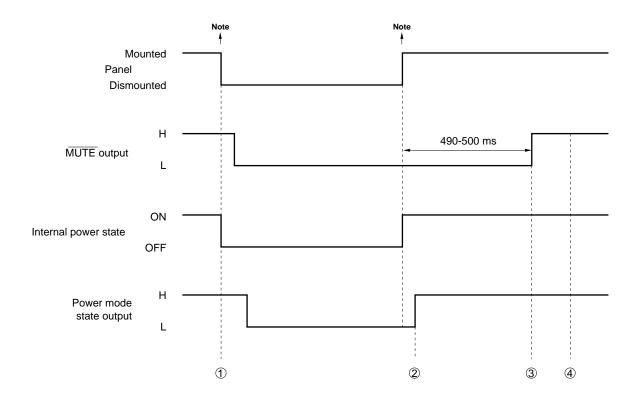


- (1) The power-off port state is set, the tuner is turned off, and serial communication is broken. But if the initialize diode CLKDSP = 1 (short-circuited with diode), the operations of breaking serial communication and turning off the LCD power output are not implemented.
- (2) If initialize diode CLKDSP = 0 (open), the LCD power output is turned on with the same timing.
- (3) Tuner is turned on, sound source mode is started, serial communication is started and call back for the last preset memory is started (If the sound source is the tuner, mute on remains.)
- (4) Call for last preset memory is completed.

Note Timing for detecting key and panel changes (time to eliminate chattering is not included).

6.5.2 Timing of POWER ON \leftrightarrow POWER OFF transitions by detachable panel

NEC



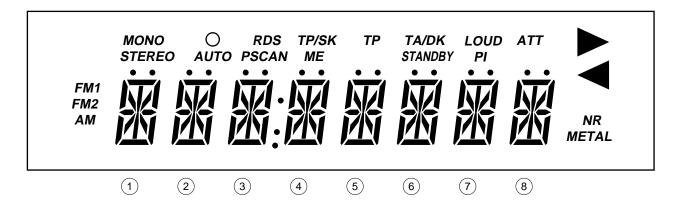
- (1) The power-off port state is set, the tuner is turned off, and serial communication is broken. But if the initial setting diode CLKDSP = 1 (shorted with diode), the operations of breaking serial communication and turning off the LCD power output are not implemented.
- 2 The LCD power pin is also turned ON by this timing.
- (3) Tuner is turned on, sound source mode is started, serial communication is started and call back for the last preset memory is started (If the sound source is the tuner, mute on remains.)
- (4) Call for last preset memory is completed.

Note Timing for detecting key and panel changes (time to eliminate chattering is not included).

7. LCD PANEL

7.1 LCD Panel Configuration

An example of the LCD panel configuration is shown below.



For inquiries on the LCD panel, please contact the following address or phone.

Address : 1-4-33 Kitakyuhoji, Yao-shi, Osaka 581

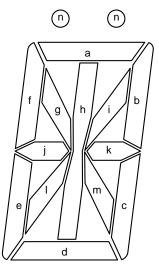
Administration Section of Displayer Product Division, Hoshiden, Ltd.

Tel : 0729-93-1010 (key number)

7.2 LCD Pin Assignment

The LCD pin assignment table of μ PD16431A is shown in Table 7-1.

① to ⑧ indicate the column locations of the 14 segments. "a" to "n" show the following 14 segments respectively.



Common				
Segment	COM1 (21)	COM ₂ (22)	COM3 (23)	COM4 (24)
SEG1 (25)	(8) n	8 b	(8) c	_
SEG2 (26)	8 i	(8) k	(8) m	_
SEG3 (27)	(8) a	(8) h	(8) d	_
SEG4 (28)	8 g	8 j	8	_
SEG₅ (29)	PI	(8) f	(8) e	ATT
SEG6 (30)	_	_	_	_
SEG7 (31)	⑦ n	⑦ b	⑦ c	LOUD
SEG8 (32)	(7) i	(7) k	(7) m	_
SEG9 (33)	(7) a	⑦ h	(7) d	_
SEG10 (34)	⑦ g	⑦ j	7 1	—
SEG11 (35)	STANDBY	⑦ f	(7) e	•
SEG12 (36)		_	_	_
SEG13 (37)	⑥ n	6 b	6) c	TA/DK
SEG14 (38)	6 i	6 k	6 m	_
SEG15 (39)	<u>6</u> a	6 h	6) d	_
SEG16 (40)	6 g	6 j	6 1	_
SEG17 (41)	ME	6 f	6 e	•
SEG18 (42)		_	_	_
SEG19 (43)	(5) n	(5) b	(5) c	TP
SEG20 (44)	(5) i	(5) k	(5) m	_
SEG ₂₁ (45)	(5) a	(5) h	(5) d	_
SEG22 (46)	⑤ g	(5) j	5 ا	_
SEG ₂₃ (47)	_	(5) f	(5) e	TP/SK
SEG ₂₄ (48)	—	—	_	—
SEG ₂₅ (49)	④ n	(4) b	④ c	_
SEG ₂₆ (50)	(4) i	④ k	(4) m	_
SEG ₂₇ (51)	(4) a	④ h	(4) d	
SEG ₂₈ (52)	④ g	④ j	(4) I	—
SEG ₂₉ (53)	PSCAN	④ f	④ e	:
SEG30 (54)		NR	METAL	
SEG31 (55)	③ n	3 b	(3) c	
SEG32 (56)	3 i	3 k	(3) m	
SEG33 (57)	<u>(</u> 3) а	(3) h	(3) d	_
SEG34 (58)	3 g	3 j	3 ।	—
SEG35 (59)	AUTO	3 f	(3) e	RDS
SEG ₃₆ (60)	_	-	_	_

Table 7-1. LCD Pin Assignment Table of μ PD16431A (1/2)

- : Unused

Remark The value in parenthesese indicates the pin number of μ PD16431A.

Common Segment	COM1 (21)	COM ₂ (22)	COM₃ (23)	COM4 (24)
SEG37 (61)	(2) n	2 b	(2) c	0
SEG ₃₈ (62)	2 i	(2) k	(2) m	—
SEG ₃₉ (63)	② a	2 h	2 d	—
SEG40 (64)	② g	② j	21	—
SEG41 (65)	STEREO	② f	② e	MONO
SEG42 (66)		_	_	—
SEG43 (67)	(1) n	(1) b	(1) c	—
SEG44 (68)	(1) i	(1) k	(1) m	—
SEG45 (69)	(1) a	1) h	(1) d	—
SEG ₄₆ (70)	(1) g	① j	1	—
SEG47 (71)	FM2	(1) f	(1) e	FM1
SEG48 (72)	AM			—

Table 7-1.	LCD Pin	Assignment	Table	of	μ PD16431A (2/2)
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- : Unused

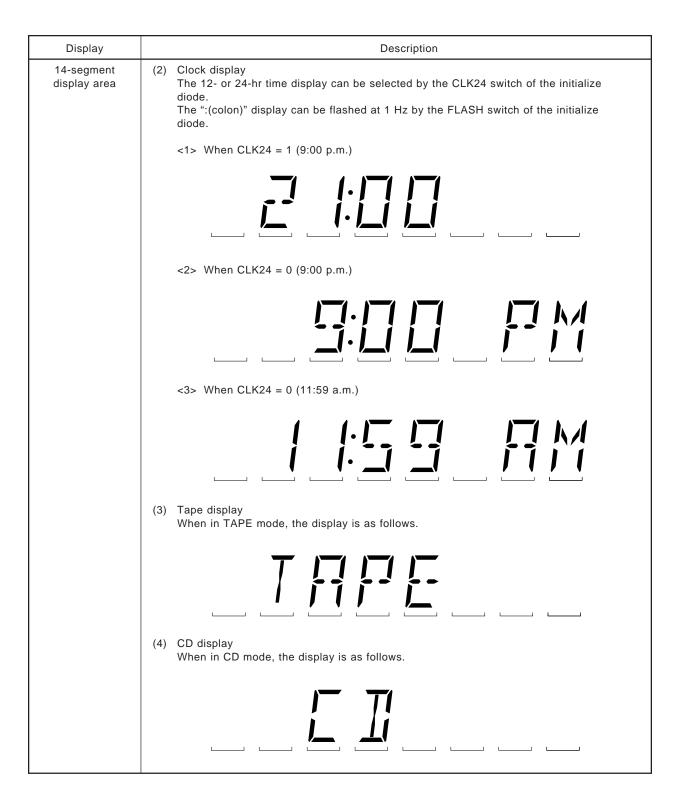
Remark The value in parentheses indicates the pin number of μ PD16431A.

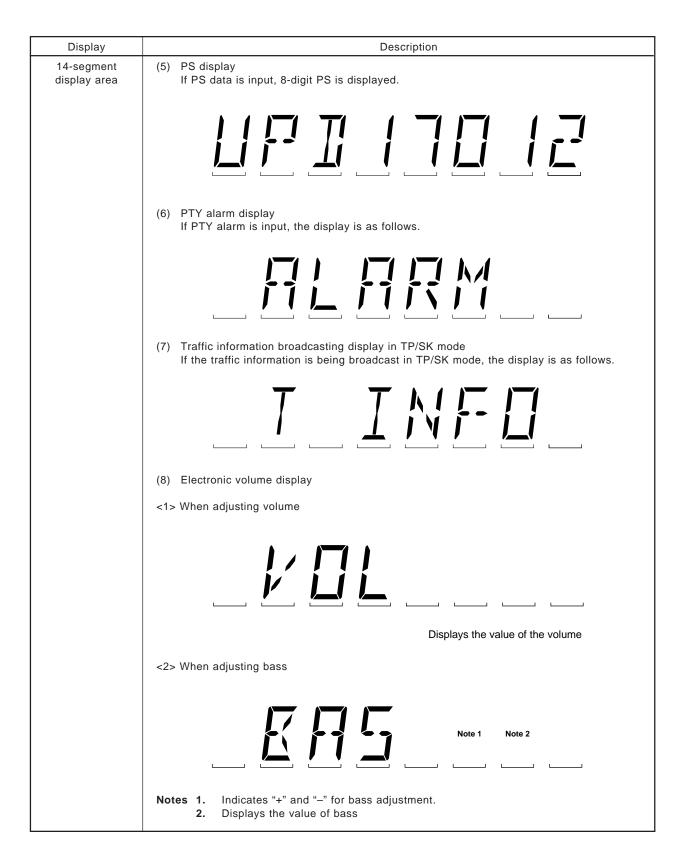
7.3 Description of LCD Panel Display

Display	Description
MONO	Indicates that the device is in forced monaural voice output mode. Inverted by pressing the MONO key while receiving the FM band in RADIO mode.
\bigcirc	Indicates that the broadcasting station being currently received is an RDS station. Lit when an RDS station is received on the FM band.
RDS	Indicates that the device is in RDS mode. Inverted by pressing the RDS key upon reception of FM band during RADIO mode
TP/SK	Indicates that the device is in TP/SK mode. Inverted by pressing the TP/SK key upon reception of FM band during RADIO mode
TP	 Indicates that the broadcasting station currently being received is broadcasting the traffic information. Lit when the TP signal of an RDS broadcasting station or the SK signal of a VF broadcasting station is detected.
TA/DK	 Indicates that the broadcasting station currently being received is broadcasting the traffic information. Lit when the TA signal of an RDS broadcasting station or the DK signal of a VF broadcasting station is detected.
LOUD	Indicates the loudness is ON. Inverted by pressing the ATT/LOUD key for more than 2 seconds.
ATT	Indicates the attenuator is ON. Inverted by pressing the ATT/LOUD key for less than 2 seconds.
STEREO	Indicates that the STEREO signal is being input. Lit when the STEREO pin is at a low level in the FM band. Always unlit in MONO mode.
AUTO	Indicates that the tuning mode of the radio is AUTO (SEEK). Inverted by pressing the AUTO key during RADIO mode.
PSCAN	Indicates that the device is in preset memory scan operation. Lit if the device is placed in preset memory scan operation by the PSCAN key.
ME	Indicates that the device is in the preset memory write status. Lit if the device is placed in preset memory write status by the ME key.
STANDBY	Indicates the traffic information is in the state of standby during CD/tape mode. Also indicates it is upon traffic information interrupt/alarm interrupt during RADIO mode.
PI	Indicates the mode to check a PI code when preset memory is called. Inverted by pressing the PI key upon reception of FM band during RADIO mode
FM1 FM2 AM	Indicates the receiving band of the radio.
NR	Indicates that the device is in noise reduction mode. Inverted by pressing the NR key during TAPE mode.
METAL	Indicates that the device is in metal tape compatible mode. Inverted by pressing the METAL key during TAPE mode.
•	Indicates the running direction of the tape. In TAPE mode, "▶" is lit when the R/L pin is low level; and "◄" when high level.

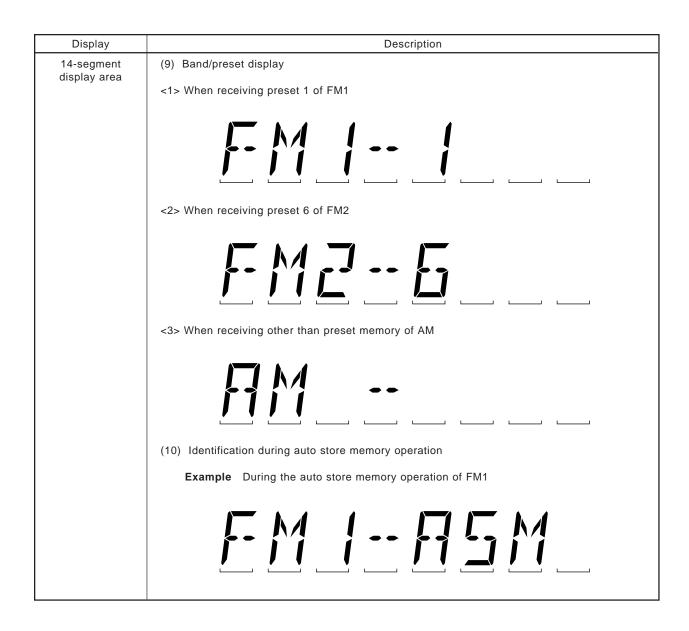
Description
Displays the following: (1) Receiving frequency (2) Clock (3) Tape (4) CD (5) PS (Program Service Name) (6) PTY alarm (7) Traffic information being broadcast in TP/SK mode (8) Electronic volume (9) Reception band and preset code (10) Identification during auto store memory operation (1) Receiving frequency display <1> FM band (108.00 MHz)

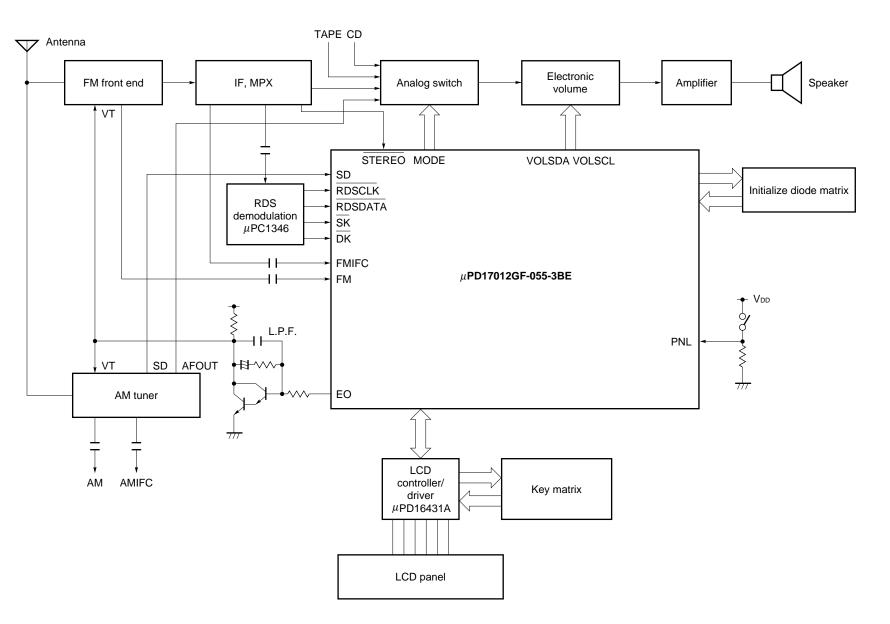






Display	Description
14-segment display area	<3> When adjusting treble
	Note 1 Note 2
	 Notes 1. Indicates "+" and "-" for treble adjustment. 2. Displays the value of treble
	<4> Adjusting balance
	Notes 1. Indicates "L" and "R" for balance adjustment.2. Displays the value of balance
	<5> When adjusting fader
	Image: Note 1 Note 2
	 Notes 1. Indicates "F" and "R" for fader adjustment. 2. Displays the value of fader





8. SYSTEM CONFIGURATION EXAMPLE

89

9. ELECTRICAL SPECIFICATIONS (PRELIMINARY)

Absolute Maximum Ratings (T_A = 25 $^{\circ}$ C)

Item	Symbol	Condition	Rating	Unit
Supply voltage	Vdd		-0.3 to +6.0	V
Input voltage	Vi		-0.3 to VDD +0.3	V
Output voltage	Vo	Other than P0C ₀ -P0C ₃	-0.3 to VDD +0.3	V
High-level output current	Іон	One pin	-12.0	mA
		Total of all pins	-20.0	mA
Low-level output current	lol	One pin	15.0	mA
		Total of all pins	30.0	mA
Output pressure proof	VBDS	P0C ₀ -P0C ₃	14.0	V
Total power dissipation	Р⊤		200	mW
Operating ambient temperature	TA	All functions operation	-40 to +85	°C
Storage temperature	Tstg		-55 to +125	°C

Caution If the absolute maximum rating of even one of the items above is exceeded, product quality may deteriorate. In other words, the absolute maximum rating is the rating value which if exceeded, may result in damage to the product. Be certain not to exceed the absolute maximum rating.

Recommended Operation Range (T_A = -40 to +85 °C)

Item	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Supply voltage	Vdd1	All functions operation	4.5	5.0	5.5	V
	Vdd2	CPU operation, PLL stop	3.5	5.0	5.5	V
Data retention voltage	Vddr	Crystal oscillation stop	2.3		5.5	V
Output pressure proof	VBDS	P0C0-P0C3			12.0	V
Supply voltage rise time	trise	$V_{\text{DD}} = 0 \rightarrow 4.5 \text{ V}$			500	ms

DC Characteristics (TA = -40 to +85 $^\circ\text{C},$ VDD = 5 V ±10 %)

Item	Symbol		Condition		MIN.	TYP.	MAX.	Unit
Supply current	Idd1	XIN pin sine	tion, PLL stop; e wave input IHz, VIN = VDD)			1.0	2.0	mA
	Idd2	XIN pin sine	tion, PLL stop, wave input Hz, VIN = VDD) mand used			0.5	1.0	mA
Data retention voltage	Vddr1	In crystal oscillation						V
	Vddr2	In crystal oscillation	Uses the pow detection met	er failure hod by timer FF	2.3			V
	Vddr3	stop	top Retention of data memory					V
Data retention current	IDDR1	In crystal	$V_{DD} = 5 V, T_A$	= 25 °C		2.0	4.0	μΑ
	Iddr2	oscillation				2.0	20.0	μΑ
	Iddr3	stop	$V_{DD} = 2.3 V, T$	Γ _A = 25 °C		1.0	2.0	μΑ
	IDDR4		$V_{DD} = 2.3 V$			1.0	10.0	μΑ
Intermediate level output voltage	Vом	COM0-CON	12	$V_{DD} = 5.0 V$	2.3		2.7	V
High-level input voltage	VIH1	P0A ₁ , P0B ₀ -P0B ₃ , P1A ₀ -P1A ₂ , P1B ₀ -P1B ₃ , C P1D ₀ -P1D ₃		0.7 Vdd		Vdd	V	
	VIH2	P0A0, P0A2, CE, INT		0.8 Vdd		Vdd	V	
	Vінз	P0D0-P0D3			0.6 Vdd		Vdd	V
Low-level input voltage	VIL1)-P0B₃, P0D₀-P0 , P1B₀-P1B₃, P ⁻		0		0.2 Vdd	V
	VIL2	P0A0, P0A	2, CE, INT		0		0.2 Vdd	V
High-level output current	Іон1		, P0B0-P0B3, P , P1D0-P1D3	1A0-P1A2, Voh = Vdd-1 V	-1.0			mA
	Іон2	LCD0-LCD1	9, EO	Vон = Vdd-1 V	-1.0			mA
Low-level output current	Iol1		, P0B0-P0B3, P , P1D0-P1D3	1A0-P1A2, Vol = 1 V	1.0			mA
	IOL2	LCD0-LCD1	9, EO	Vol = 1 V	1.0			mA
	Іоіз	P0C0-P0C3		Vol = 1 V	10			mA
High-level input current	Іінт	VCOH pin	pull-down	Vih = Vdd	0.1			mA
	Іін2	VCOL pin p	oull-down	Vih = Vdd	0.1			mA
	Іінз	Xıℕ pin pull	-down	Vih = Vdd	0.1			mA
	Іін4	P0D0-P0D3	pin pull-down	Vih = Vdd	10		150	μΑ
Output off leakage current	IL1	P0C0-P0C3		Vон = 12 V			1.0	μΑ
	IL2	EO	Vон	= Vdd, Vol = 0 V			±1.0	μΑ

Item	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Operating frequencies	fin1	VCOL pin MF mode sine wave input V _{IN} = 0.15 V _{P-P}	0.90		3.0	MHz
		VCOL pin MF mode sine wave input VIN = 0.3 VP-P	0.50		20	MHz
	fin2	VCOL pin HF mode sine wave input V _{IN} = 0.15 V _{P-P}	5		25	MHz
		VCOL pin HF mode sine wave input V _{IN} = 0.3 V _{P-P}	5		40	MHz
	fіnз	VCOH pin VHF mode sine wave input V _{IN} = 0.15 V _{P-P}	60		130	MHz
		VCOH pin VHF mode sine wave input V _{IN} = 0.3 V _{P-P}	30		250	MHz
	fin4	AMIFC pin AMIF count mode sine wave input VIN = 0.3 VP-P	0.3		1.0	MHz
	fins	AMIFC pin AMIF count mode sine wave input V _{IN} = 0.1 V _{P-P}	0.44		0.46	MHz
	fing	FMIFC pin FMIF count mode sine wave input VIN = 0.3 VP-P	5		15	MHz
	fin7	FMIFC pin FMIF count mode sine wave input V _{IN} = 0.1 V _{P-P}	10.5		10.9	MHz

AC Characteristics (TA = -40 to +85 $^\circ\text{C},$ VDD = 4.5 to 5.5 V)

A/D Converter Characteristics (T_A = -40 to +85 °C, V_{DD} = 5 V \pm 10 %)

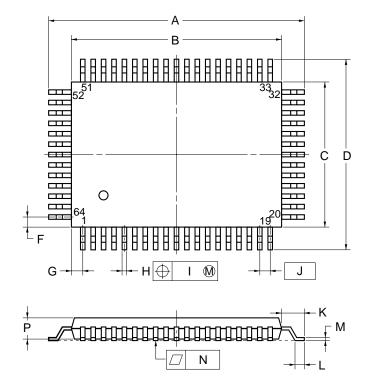
Item	Symbol	Condition	MIN.	TYP.	MAX.	Unit
A/D conversion resolution					6	bit
A/D conversion overall error				±1.0	±1.5	LSB

Reference Characteristics (T_A = 25 $^{\circ}$ C, V_{DD} = 5.0 V)

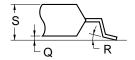
Item	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Supply current	Іддз	CPU and PLL operation, VCOH pin sine wave input (f _{IN} = 130 MHz, V _{IN} = 0.3 V _{P-P})		12		mA
	Idd4	CPU and PLL operation, VCOH pin sine wave input (f _{IN} = 250 MHz, V _{IN} = 0.3 V _{P-P})		13		mA
High-level output current	Іонз	COM0-COM2 VOH = VDD-1 V		-300		μΑ
Intermediate-level output current	IOL4	COM ₀ -COM ₂ Vol = 1 V		300		μΑ
Low-level output current	Іом1	СОМо-СОМ2 Vон = VDD-1 V		-25		μΑ
	Іом2	COM0-COM2 Vol = 1 V		25		μΑ

10. PACKAGE DRAWING

64 PIN PLASTIC QFP (14×20)



detail of lead end



NOTE

Each lead centerline is located within 0.20 mm (0.008 inch) of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS	INCHES
А	23.6±0.4	0.929±0.016
В	20.0±0.2	$0.795^{+0.008}_{-0.009}$
С	14.0±0.2	$0.551^{+0.009}_{-0.008}$
D	17.6±0.4	0.693±0.016
F	1.0	0.039
G	1.0	0.039
н	0.40±0.10	$0.016^{+0.004}_{-0.005}$
I	0.20	0.008
J	1.0 (T.P.)	0.039 (T.P)
к	1.8±0.2	$0.071\substack{+0.008\\-0.009}$
L	0.8±0.2	$0.031^{+0.009}_{-0.008}$
М	$0.15^{+0.10}_{-0.05}$	$0.006^{+0.004}_{-0.003}$
Ν	0.10	0.004
Р	2.7	0.106
Q	0.1±0.1	0.004±0.004
R	5°±5°	5°±5°
S	3.0 MAX.	0.119 MAX.
	P64GF-10	0-3B8,3BE,3BR-2

11. RECOMMENDED SOLDERING CONDITIONS

Solder the package of this product under the conditions recommended as follows.

For details of the recommended conditions for soldering, please refer to the information document "**Semiconductor Device Mounting Technology Manual**" (IEI-1207).

For soldering methods and conditions other than those recommended below, please contact NEC sales personnel.

Table 11-1. Soldering Conditions for Surface-Mount Type

μ PD17012GF-055-3BE: 64-pin plastic QFP (14 \times 20 mm)

Soldering Method	Soldering Condition	Recommended Condition Symbol
Infrared reflow	 Package peak temperature : 235 °C; time : within 30 secs (210 °C or more); count: 2 max.; day limit : 7 days^{Note} (hereafter, pre-baked for 20 hrs at 125 °C) <caution> (1) Start second reflow after device temperature (which has risen because of first reflow) has returned to room temperature. (2) Do not clean flux with water after first reflow. </caution> 	IR35-207-2
VPS	 Package peak temperature : 215 °C; time : within 40 secs (200 °C or more); count: 2 max.; day limit : 7 days^{Note} (hereafter, pre-baked for 20 hrs at 125 °C) <caution></caution> (1) Start second reflow after device temperature (which has risen because of first reflow) has returned to room temperature. (2) Do not clean flux with water after first reflow. 	VP15-207-2
Wave soldering	Solder bath temperature: no more than 260 °C; time : within 10 secs; count: once; preheating temperature : 120 °C max. (package surface temperature); day limit : 7 days ^{Note} (hereafter, pre-baked for 20 hours at 125 °C)	WS60-207-1
Pin part heating	Pin temperature : no more than 300 °C; time : within 3 secs (per device side)	_

Note Refers to the number of days for storage after the dry pack is opened. The storage conditions are 25 °C and no more than 65 % RH.

Caution Avoid using multiple soldering methods at the same time (except the pin part heating method).

APPENDIX DESCRIPTION OF ELECTRONIC VOLUME CONTROL

Appendix 1. Function of Electronic Volume

The μ PD17012GF-055 uses electronic volume to control and select the sound. The electronic volume functions are as follows.

- (1) Volume adjustment
 (0 to 50 steps)
 (2) Bass adjustment
 (-7 to +7 steps)
- (3) Treble adjustment (-7 to +7 steps)
- (4) Balance adjustment (L7 to R7 steps)
- (5) Fader adjustment (F7 to R7 steps)
- (6) Loudness (On/Off)
- (7) Attenuator (On/Off)
- (8) Sound selector

For adjustments for each function,	refer to the	descriptions	of the	SELECT	key and	VOLUP	/ ך
VOLDOWN key.							_

Appendix 2. Description of the Electronic Volume Control

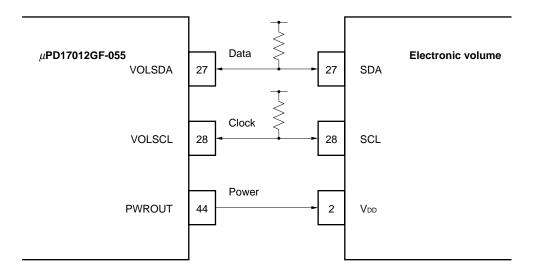
The μ PD17012GF-055 uses an electronic volume IC to control and select the sound.

The μ PD17012GF-055 transfers the initial data settings to electronic volume about 480-500 ms after the transition from initial low level to high level at the PWROUT pin, when the power supply for the V_{DD} pin is turned on (power on reset).

In addtion, the μ PD17012GF-055 uses the I²C bus.

The pin configuration between the μ PD17012GF-055 and the electronic volume is as follows.





Appendix 3. Initial Electronic Volume Value Settings

When initial power is supplied to the μ PD17012GF-055, the electronic volume state settings are as follows.

Function	Initial Value
Volume	20 steps
Bass	0 steps
Treble	0 steps
Balance	0 steps
Fader	0 steps
Loudness	Off
Attenuator	Off

Also, when CE is reset, the state settings prior to the CE reset are retained.

Appendix.4 Electronic Volume Data Output Timing

Appendix 4.1 Initial data setting output timing

The timing of initial data setting output to the electronic volume is shown in Figure A-2.

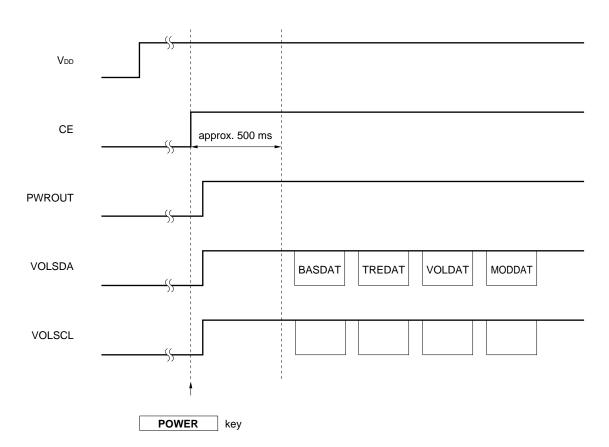


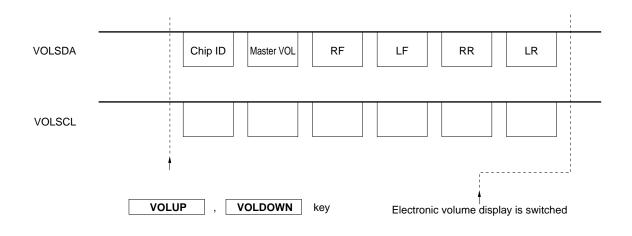
Figure A-2. Initial Setting Data Output Timing

- BASDAT : Bass initial data setting (0 steps)
- TREDAT : Treble initial data setting (0 steps)
- VOLDAT : Volume initial data setting (Master: 20 steps, balance fader: 0 steps)
- MODDAT : Sound source, loudness data setting (loudness off)

Appendix 4.2 Volume data output timing

The timing of volume data output to the electronic volume is shown in Figure A-3. When adjusting the balance fader, output is according to the following procedure.

Figure A-3. Volume Data Output Timing



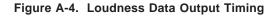
Master	VOL	:	0 t	o 50	steps
--------	-----	---	-----	------	-------

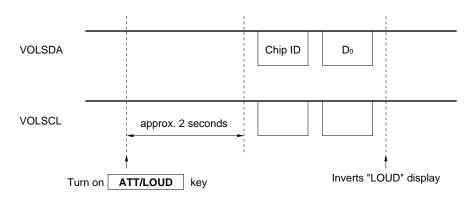
RF :	Right	front	volume	data
------	-------	-------	--------	------

- LF : Left front volume data
- RR : Right rear volume data
- LR : Left rear volume data

Appendix 4.3 Loudness data output timing

The timing of loudness data output to the electronic volume is shown in Figure A-4.

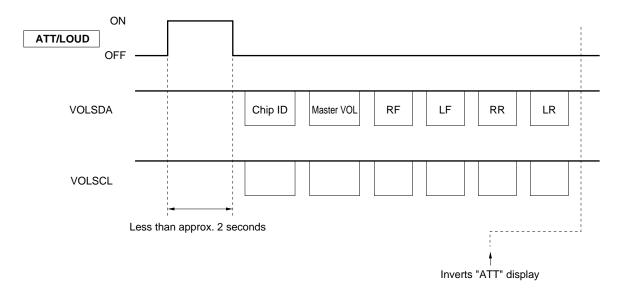






Appendix 4.4 Attenuator data output timing

The timing of attenuator data output to the electronic volume is shown in Figure A-5.





Master VOL : 0 to 50 steps

RF :	Right	front	volume	data
------	-------	-------	--------	------

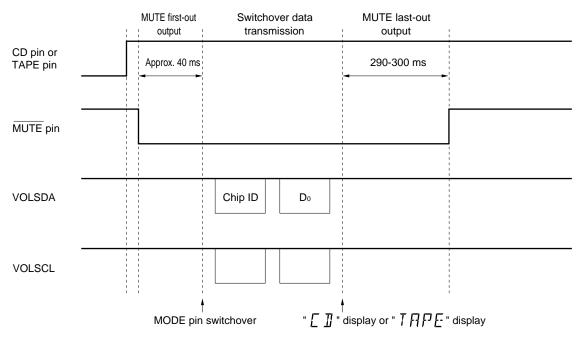
LF	:	Left	front	volume	data

- RR : Right rear volume data
- LR : Left rear volume data

When the attenuator is turned on, -20 dB is subtracted from each of master VOL, RF, LF, RR, LR, and the result is output.

Appendix 4.5 Sound source switchover data output timing

The timing sound source switchover data output to the electronic volume is shown in Figure A-6.



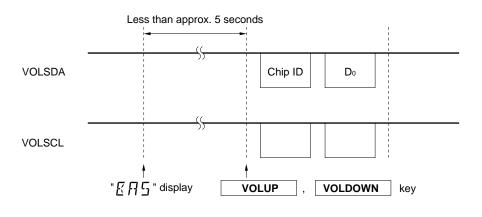


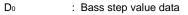
Do : Sound/loudness data

Appendix 4.6 Bass data output timing

The timing of bass data output to the electronic volume is shown in Figure A-7.

Figure A-7. Bass Data Output Timing





Appendix 4.7 Treble data output timing

The timing of treble data output to the electronic volume is shown in Figure A-8.

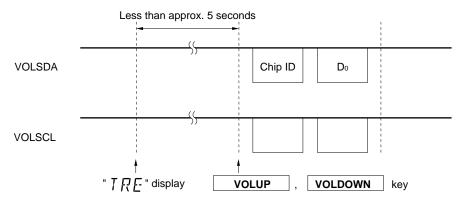
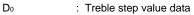


Figure A-8. Treble Data Output Timing



NOTES FOR CMOS DEVICES -

1 PRECAUTION AGAINST ESD FOR SEMICONDUCTORS

Note: Strong electric field, when exposed to a MOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred. Environmental control must be adequate. When it is dry, humidifier should be used. It is recommended to avoid using insulators that easily build static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work bench and floor should be grounded. The operator should be grounded using wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions need to be taken for PW boards with semiconductor devices on it.

(2) HANDLING OF UNUSED INPUT PINS FOR CMOS

Note: No connection for CMOS device inputs can be cause of malfunction. If no connection is provided to the input pins, it is possible that an internal input level may be generated due to noise, etc., hence causing malfunction. CMOS device behave differently than Bipolar or NMOS devices. Input levels of CMOS devices must be fixed high or low by using a pull-up or pull-down circuitry. Each unused pin should be connected to VDD or GND with a resistor, if it is considered to have a possibility of being an output pin. All handling related to the unused pins must be judged device by device and related specifications governing the devices.

③ STATUS BEFORE INITIALIZATION OF MOS DEVICES

Note: Power-on does not necessarily define initial status of MOS device. Production process of MOS does not define the initial operation status of the device. Immediately after the power source is turned ON, the devices with reset function have not yet been initialized. Hence, power-on does not guarantee out-pin levels, I/O settings or contents of registers. Device is not initialized until the reset signal is received. Reset operation must be executed immediately after power-on for devices having reset function.

[MEMO]

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NEC devices are classified into the following three quality grades:

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- Standard: Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots
- Special: Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)
- Specific: Aircrafts, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems or medical equipment for life support, etc.

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Anti-radioactive design is not implemented in this product.