Signetics

μ A733/733C Differential Video Amplifier

Product Specification

Linear Products

DESCRIPTION

The 733 is a monolithic differential input. differential output, wide-band video amplifier. It offers fixed gains of 10, 100, or 400 without external components, and adjustable gains from 10 to 400 by the use of an external resistor. No external frequency compensation components are required for any gain option. Gain stability, wide bandwidth, and low phase distortion are obtained through use of the classic series-shunt feedback from the emitter-follower outputs to the inputs of the second stage. The emitter-follower outputs provide low output impedance, and enable the device to drive capacitive loads. The 733 is intended for use as a high-performance video and pulse amplifier in communications, magnetic memories, display and video recorder systems.

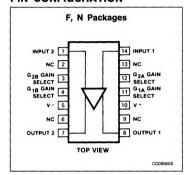
FEATURES

- 120MHz bandwidth
- 250kΩ input resistance
- Selectable gains of 10, 100, and 400
- No frequency compensation required
- MIL-STD-883A, B, C available

APPLICATIONS

- Video amplifier
- Pulse amplifier in communications
- Magnetic memories
- Video recorder systems

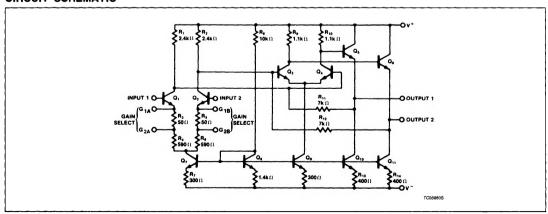
PIN CONFIGURATION



ORDERING INFORMATION

DESCRIPTION	TEMPERATURE	ORDER CODE		
14-Pin Ceramic DIP	-55°C to +125°C	μA733F		
14-Pin Plastic DIP	-55°C to +125°C	μA733N		
14-Pin Plastic DIP	0 to +70°C	μΑ733CN		
14-Pin Ceramic DIP	0 to +70°C	μΑ733CF		

CIRCUIT SCHEMATIC



μA733/733C

ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	RATING	UNIT
V _{DIFF}	Differential input voltage	±5	٧
V _{СМ}	Common-mode input voltage	± 6	٧
V _{CC}	Supply voltage	± 8	٧
lout	Output current	10	mA
TJ	Junction temperature	+ 150	ပ္
TSTG	Storage temperature range	-65 to +150	ပ္
TA	Operating ambient temperature range μΑ733C μΑ733	0 to +70 -55 to +125	ပံ့
P _{MAX}	Maximum power dissipation ¹ 25°C ambient temperature (still-air) F package N package	1190 1420	mW mW

NOTE:

DC ELECTRICAL CHARACTERISTICS $T_A = \pm 25^{\circ}C$, $V_S = \pm 6V$, $V_{CM} = 0$, unless otherwise specified. Recommended operating supply voltages $V_S = \pm 6.0V$.

SYMBOL	PARAMETER	TEST CONDITIONS	µА733С			µА733			
			Min	Тур	Max	Min	Тур	Max	UNIT
	Differential voltage gain Gain 1 ² Gain 2 ² Gain 3 ³	$R_1 = 2k\Omega$, $V_{OUT} = 3V_{P-P}$	250 80 8	400 100 10	600 120 12	300 90 9	400 100 10	500 110 11	V/V V/V V/V
BW	Bandwidth Gain 1 ¹ Gain 2 ² Gain 3 ³			40 90 120			40 90 120		MHz MHz MHz
t _R	Rise time Gain 1 ¹ Gain 2 ² Gain 3 ³	V _{OUT} = 1V _{P-P}		10.5 4.5 2.5	12		10.5 4.5 2.5	10	ns ns ns
t _{PD}	Propagation delay Gain 1 ¹ Gain 2 ² Gain 3 ³	V _{OUT} = 1V _{P.P}		7.5 6.0 3.6	10		7.5 6.0 3.6	10	ns ns ns
R _{IN}	Input resistance Gain 1 ² Gain 2 ² Gain 3 ³		10	4.0 30 250		20	4.0 30 250		kΩ kΩ kΩ
	Input capacitance ²	Gain 2		2.0			2.0		pF
los	Input offset current			0.4	5.0		0.4	3.0	μΑ
IBIAS	Input bias current			9.0	30		9.0	20	μΑ
V _{NOISE}	Input noise voltage	BW = 1kHz to 10MHz		12			12		μV _{RMS}
V _{IN}	Input voltage range		± 1.0			± 1.0			>
CMRR	Common-mode rejection ratio Gain 2 Gain 2	$V_{CM} = \pm 1V, f \le 100kHz$ $V_{CM} = \pm 1V, f = 5MHz$	60	86 60		60	86 60		dB dB
SVRR	Supply voltage rejection ratio Gain 2	$\Delta V_S = \pm 0.5 V$	50	70		50	70		dB

December 2, 1986 11-124

^{1.} The following derating factors should be applied above 25°C:

F package at 9.5mW/°C

N package at 11.4mW/°C.

μA733/733C

DC ELECTRICAL CHARACTERISTICS (Continued) $T_A = +25^{\circ}C$, $V_S = \pm 6V$, $V_{CM} = 0$, unless otherwise specified. Recommended operating supply voltages $V_S = \pm 6.0V$.

SYMBOL	PARAMETER	TEST CONDITIONS	μ Α 733C			μ Α733			
			Min	Тур	Max	Min	Тур	Max	UNIT
	Output offset voltage Gain 1 ¹ Gain 2 and 3 ^{2, 3}	R _L = ∞		0.6 0.35	1.5 1.5		0.6 0.35	1.5 1.0	V
V _{CM}	Output common-mode voltage	R _L = ∞	2.4	2.9	3.4	2.4	2.9	3.4	٧
	Output voltage swing, differential	$R_L = 2k\Omega$	3.0	4.0		3.0	4.0		V _{P-P}
Isink	Output sink current		2.5	3.6		2.5	3.6		mA
Rout	Output resistance			20			20		Ω
lcc	Power supply current	R _L = ∞		18	24		18	24	mA
THE FOLL	OWING SPECIFICATIONS APPLY	OVER TEMPERATURE	0°C	≤T _A ≤	70°C	-55°C	≤T _A ≤	125°C	
	Differential voltage gain Gain 1 ¹ Gain 2 ² Gain ³	$R_i = 2k\Omega$, $V_{OUT} = 3V_{P-P}$	250 80 8		600 120 12	200 80 8		600 120 12	V/V V/V V/V
R _{IN}	Input resistance Gain 2 ²		8			8			kΩ
los	Input offset current				6			5	μА
IBIAS	Input bias current				40			40	μА
V _{IN}	Input voltage range		± 1.0			± 1.0			٧
CMRR	Common-mode rejection ratio Gain 2	V _{CM} = ± V, F ≤ 100kHz	50		}	50			dB
SVRR	Supply voltage rejection ratio Gain 2	$\Delta V_S = \pm 0.5 V$	50			50			dB
V _{OS}	Output offset voltage Gain 1 ¹ Gain 2 and 3 ^{2, 3}	R _L = ∞			1.5 1.5			1.5 1.2	V V
V _{DIFF}	Output voltage swing, differential	$R_L = 2k\Omega$	2.8			2.5			V _{P-P}
ISINK	Output sink current		2.5			2.2			mA
Icc	Power supply current	R _L ±∞			27			27	mA

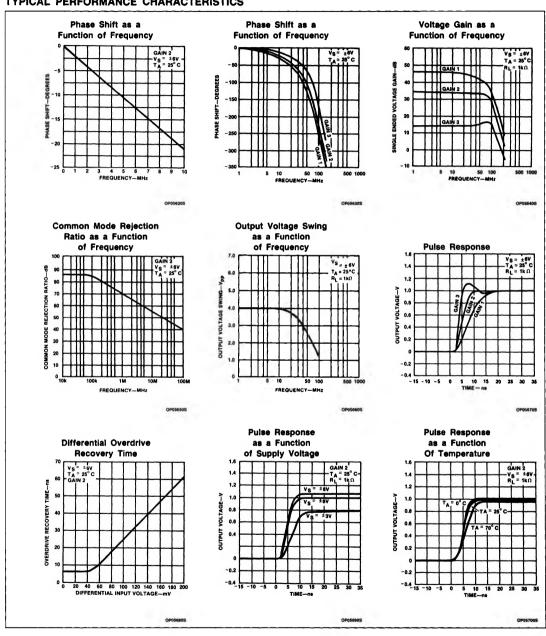
NOTES:

^{1.} Gain select pins $\mbox{G}_{\mbox{\scriptsize 1A}}$ and $\mbox{G}_{\mbox{\scriptsize 1B}}$ connected together.

Gain select pins G_{2A} and G_{2B} connected together.
 All gain select pins open.

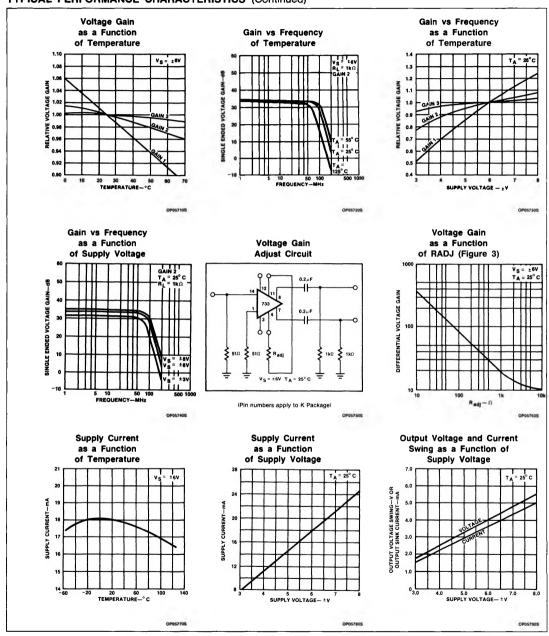
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TYPICAL PERFORMANCE CHARACTERISTICS



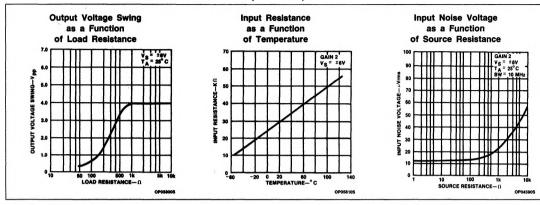
μ A733/733C

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)



μA733/733C

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)



TEST CIRCUITS T_A = 25°C, unless otherwise specified.

