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Power Management Unit

1 Introduction

1.1 Features

- Power Management Core
 - Dual Input Power Path
 - Switch Mode Charger
 - Integrated Charge Current Sense FET
 - Automatic Battery Supplement Mode
 - 2 Boost Converters
 - 1 Boost supports 2 strings of up to 6 LEDs with Internal and External Dimming Control
 - 1 Boost supports 1 string of 6 LEDs
 - Boost Converters can also be used in Constant Voltage Mode
 - LED Matrix Controller
 - RGB Controller
 - I²C[™] Interface to Device for Low Latency Communication

1.2 Applications

Portable Applications

1.3 Description

The TPS658310 Power Management Unit is a broad use, multi-channel device, for portable applications. The device consists of an Integrated Power Path Management and Switch Mode Li-Ion Battery Charger that provides system power from a regulated wall adapter or a USB port. It also handles lighting management with integrated Backlight Boosts, LED Matrix Controller for keypad, Camera Flash LED Controller, Current Source and RGB channels.

To request a full data sheet, please send an email to:

pmu_contact@list.ti.com



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

1.4

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Block Diagram

The simplified TPS658310 system diagram is shown in Figure 1-1.

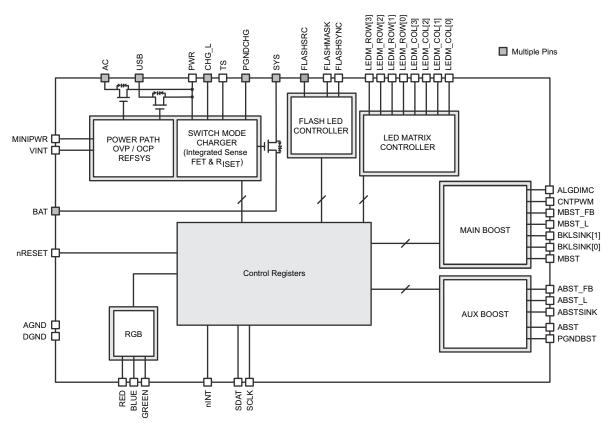
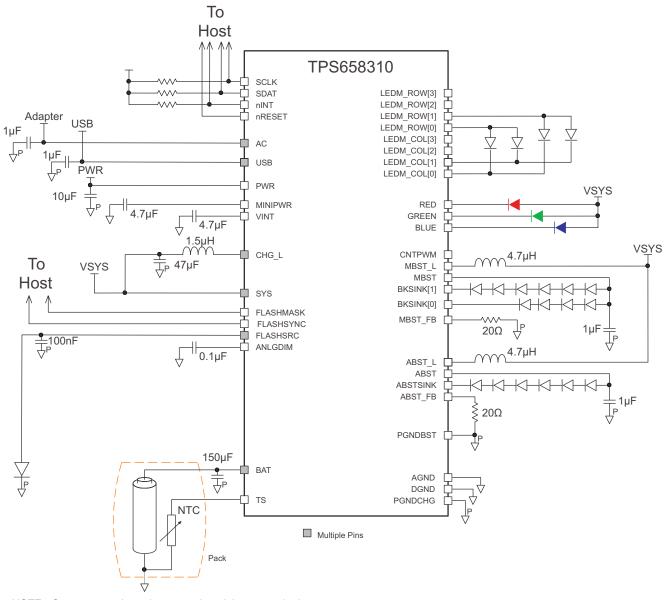


Figure 1-1. Simplified System Diagram

Instruments



2 Application Schematic



NOTE: Component values shown are the minimum required.

Figure 2-1. Application Schematic



PACKAGE OPTION ADDENDUM

29-Feb-2012

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/ Ball Finish	MSL Peak Temp ⁽³⁾	Samples (Requires Login)
TPS658310YFFR	ACTIVE	DSBGA	YFF	49	1500	Green (RoHS & no Sb/Br)	SNAGCU	Level-1-260C-UNLIM	

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

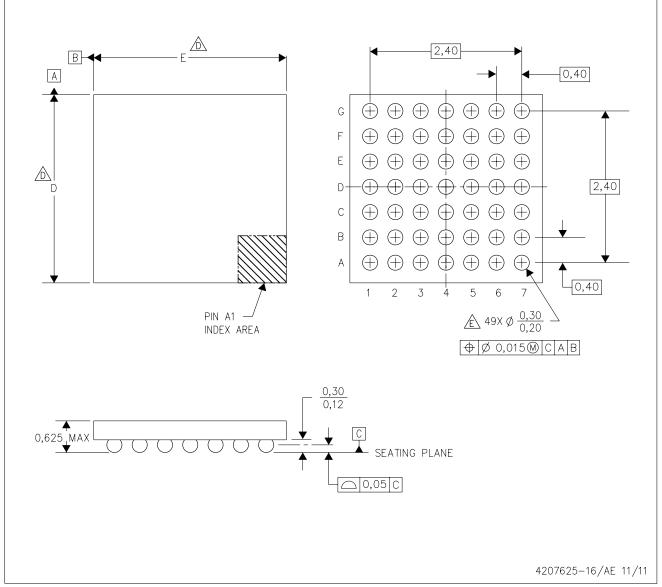
(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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YFF (R-XBGA-N49)

DIE-SIZE BALL GRID ARRAY



- NOTES: A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.
 - B. This drawing is subject to change without notice.
 - C. NanoFree™ package configuration.
 - The package size (Dimension D and E) of a particular device is specified in the device Product Data Sheet version of this drawing, in case it cannot be found in the product data sheet please contact a local TI representative.
 - E. Reference Product Data Sheet for array population. 7 x 7 matrix pattern is shown for illustration only.
 - F. This package contains Pb-free balls.

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