TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT MULTI CHIP

TD62M8501F

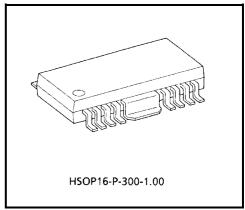
8CH LOW SATURATION VOLTAGE SINK DRIVER

TD62M8501F is Multi Chip IC incorporates 8 low saturation discrete (2SC3420) transistors. This IC is suitable for a battery use motor drive and LED display module applications.

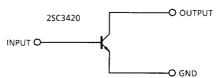
FEATURES

- Suitable for Motor drive circuit and LED display module
- External Bias Resistor
- Low Saturation Voltage V_{CE} (sat) = 0.12 V (Typ.) at I_C = 1 A V_{CE} (sat) = 0.25 V (Typ.) at I_C = 2 A
- HSOP16 (1 mm pitch) power small package sealed

SCHEMATIC







PIN CONNECTION (TOP VIEW)

		_	
IN1 [1	16] IN8
ουτι [2	15	0UT8
IN2 [3	14	IN7
Ουτ2 [4	13] Ουτ7
GND			GND
ουτ3 [5	12] OUT6
імз [6	11] IN6
OUT4 [7	10] OUT5
IN4 [8	9] IN5

MAXIMUM RATINGS (Ta = 25°C)

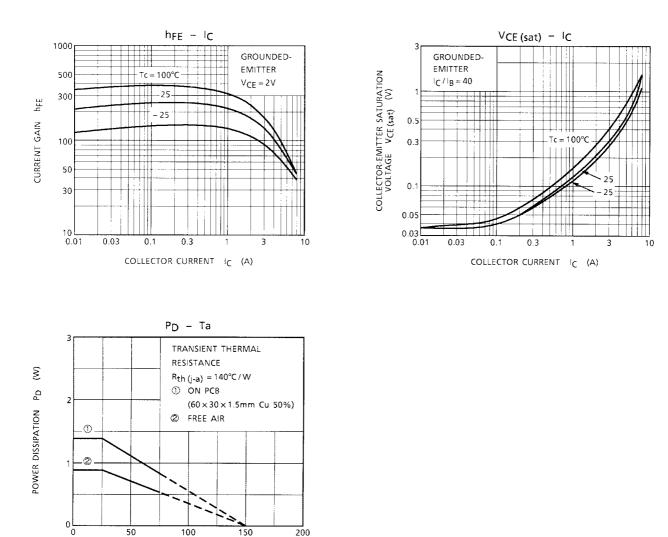
CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage	V _{CC}	20	V	
	V _{CBO}	20		
Breakdown Voltage	V _{CEO}	20	V	
	V _{EBO}	8		
Output Current	I _{O (AVE)}	2	A	
	I _{O (PRAK)}	4 (Note)		
Base Current	Ι _Β	1	А	
Power Dissipation	PD	900	mW	
Junction Temperature	Tj	150	°C	
Operating Temperature	T _{opr}	-40~85	°C	
Storage Temperature	T _{stg}	-55~150	°C	

Note: T = 10 ms MAX. and maximum duty is less than 30%.

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Current Gain	h _{FE (1)}	-	V _{CE} = 2 V, I _C = 0.5 A	140	—	600	—
	h _{FE (2)}	-	V _{CE} = 2 V, I _C = 2.0 A	70	140	—	
Saturation Voltage	V _{CE (sat)}	-	I _C = 1 A, I _B = 25 mA	_	0.12	0.25	V
Saturation voltage			I _C = 2 A, I _B = 50 mA	_	0.25	0.50	
Transition Frequency	f _T	_	V _{CB} = 2 V, I _C = 0.5 A	_	100	_	MHz
Leakage Current	I _{OL}	_	V _{CC} = 20 V	_	0	10	μA
Base-Emitter Forward Voltage	V_{BE}	_	V_{CE} = 2 V, I _C = 2.0 A	_	0.84	1.5	V

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PRECAUTIONS for USING

AMBIENT TEMPERATURE Ta (°C)

This IC does not integrate protection circuits such as overcurrent and overvoltage protectors. Thus, if excess current or voltage is applied to the IC, the IC may be damaged. Please design the IC so that excess current or voltage will not be applied to the IC.

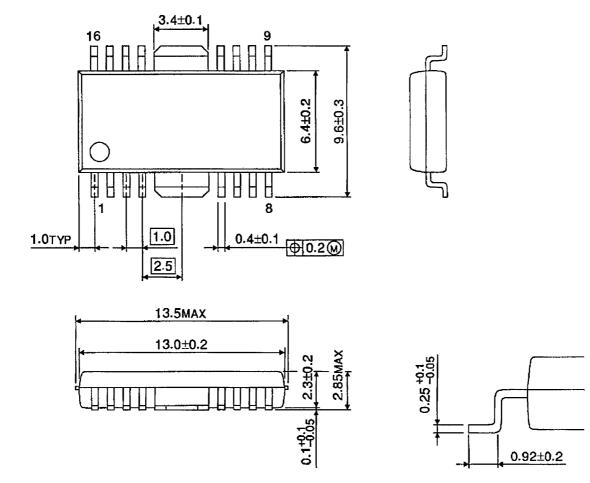
Utmost care is necessary in the design of the output line, V_{CC} and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

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PACKAGE DIMENSIONS

HSOP16-P-300-1.00

Unit: mm



Weight: 0.50 g (Typ.)

RESTRICTIONS ON PRODUCT USE

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