TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TD62381FN

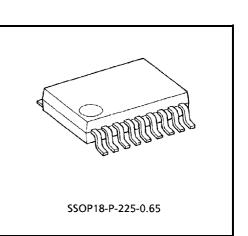
### 8CH LOW SATURATION SINK DRIVER

The TD62381FN is comprised of eight NPN low saturation drivers. This device are speciffically designed for multiplexed eight driving of eight digit common-cathode LED and also can be employed as a sink driver for multiplexed LED displays using with the TD62785FN at standard supply voltage, 5 V. Applications include relay, hammer, lamp and LED display drivers.

#### FEATURES

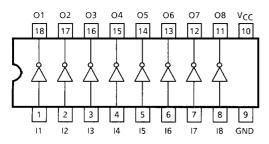
- Package Type : SSOP18 pin
- Low Saturation Output: VCE(sat) = 0.9 V (MAX.)
  - @ IOUT = 500 mAOutput Rating: VOUT = 15 V (MIN.)
- Output Rating : V<sub>OUT</sub> = 15 V (MIN
  Input Compatible with TTL and 5 V CMOS
- Low Level Active Inputs
- Standard Supply Voltage

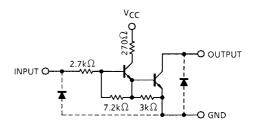
### **PIN CONNECTION (TOP VIEW)**



Weight: 0.09 g (Typ.)

### SCHEMATICS (EACH DRIVER)





Note: The input and output parasitic diodes cannot be used as clamp diodes.

### MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage	V <sub>CC</sub>	7	V	
Output Sustaining Voltage	V <sub>CE (SUS)</sub>	15	V	
Output Current	IOUT	500	mA / ch	
Input Voltage	V <sub>IN</sub>	7	mA	
Input Current	I <sub>IN</sub>	5	mA	
Power Dissipation	P <sub>D (Note)</sub>	0.96	W	
Operating Temperature	T <sub>opr</sub>	-40~85	°C	
Storage Temperature	T <sub>stg</sub>	-55~150	°C	

Note: On Glass Epoxy PCB (50 × 50 × 1.6 mm Cu 40%).

### **RECOMMENDED OPERATING CONDITIONS (Ta = -40~85°C)**

CHARACTERISTIC		SYMBOL	CONDITION		MIN	TYP.	MAX	UNIT
Supply Voltage		V <sub>CC</sub>			4.5	5.0	5.5	V
Output Voltage		V <sub>OUT</sub>			-	_	12	V
Output Current			DC 1 Circuit		0	_	400	
			T <sub>pw</sub> = 25 ms 8 Circuit On Ta = 85°C T <sub>j</sub> = 120°C	Duty = 10%	0	_	400	mA / ch
		IOUT (Note)		Duty = 50%	0	_	330	
		V <sub>IN</sub>			0	_	V <sub>CC</sub>	
Input Voltage	Output On	V <sub>IN (ON)</sub>			2.4	_	V <sub>CC</sub>	V
	Output Off	V <sub>IN (OFF)</sub>			0	—	0.4	
Power Dissipation		PD	(Note)		_	_	0.4	W

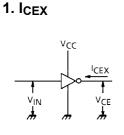
Note: On Glass Epoxy PCB (50 × 50 × 1.6 mm Cu 40%).

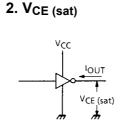
### ELECTRICAL CHARACTERISTICS (Ta = 25°C, V<sub>CC</sub> = 5 V)

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Output Leakage Current	ICEX	1	V <sub>IN</sub> = OPEN V <sub>OUT</sub> = 12 V, Ta = 85°C	_	_	100	μA
Output Saturation Voltage	V <sub>CE (sat)</sub>	2	I <sub>OUT</sub> = 500 mA	_	_	0.9	V
			I <sub>OUT</sub> = 350 mA	_	_	0.7	
Input Current	I <sub>IN (ON)</sub>	3	V <sub>CC</sub> = 5 V, V <sub>IN</sub> = 2.4 V		0.4	0.7	mA
Input Voltage (Output on)	V <sub>IN (ON)</sub>	—	V <sub>CC</sub> = 5 V	_	_	2.4	V
Supply Current	Icc	4	$V_{CC} = V_{IN} = 5 V$	_	_	17	mA / ch
Turn-On Delay	t <sub>ON</sub>	5	$V_{OUT}$ = 10 V, R <sub>L</sub> = 25 $\Omega$ C <sub>L</sub> = 15 pF	_	0.1	_	
Turn-Off Delay	t <sub>OFF</sub>	5			1.2	_	μs

### TOSHIBA

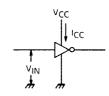
### **TEST CIRCUIT**





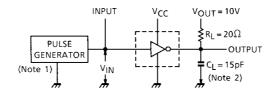


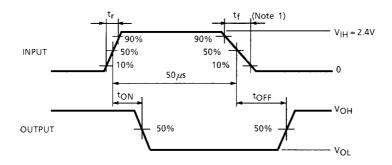
3. IIN (ON)



4. Icc

#### 5. t<sub>ON</sub>, t<sub>OFF</sub>





- Note 1: Pulse Width 50  $\mu$ s, Duty Cycle 10% Output Impedance 50  $\Omega$ , t<sub>r</sub> ≤ 5 ns, t<sub>f</sub> ≤ 10 ns
- Note 2: C<sub>L</sub> includes probe and jig capacitance.

### **PRECAUTIONS for USING**

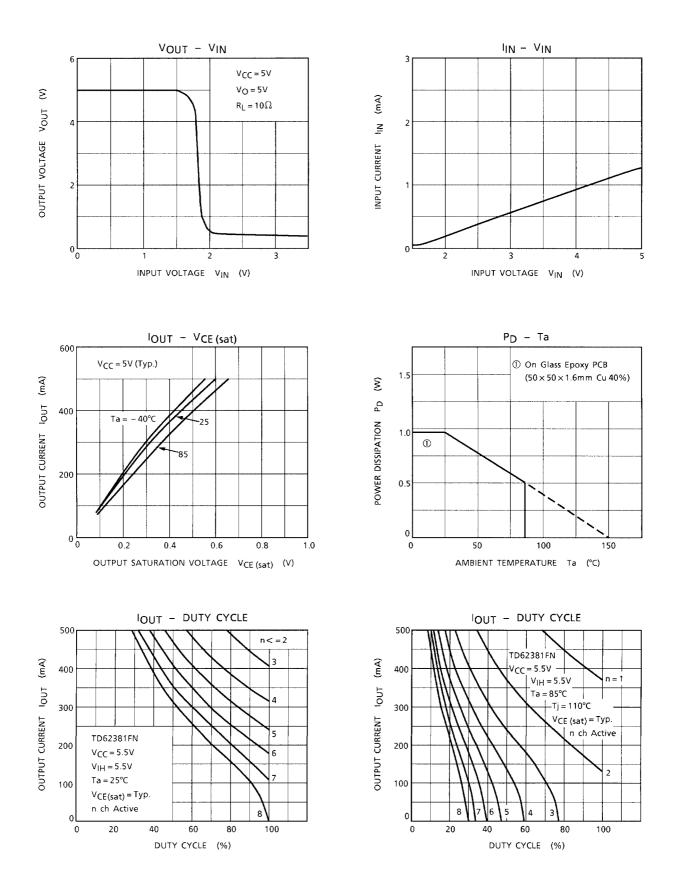
This IC does not include built-in protection circuits for excess current or overvoltage.

If this IC is subjected to excess current or overvoltage, it may be destroyed.

Hence, the utmost care must be taken when systems which incorporate this IC are designed.

Utmost care is necessary in the design of the output line, V<sub>CC</sub>, and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

## TOSHIBA

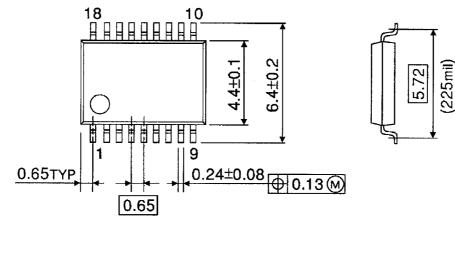


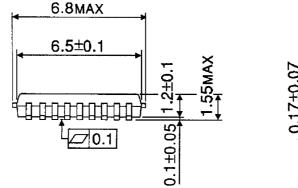
### **TOSHIBA**

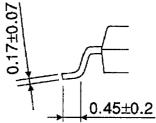
### PACKAGE DIMENSIONS

SSOP18-P-225-0.65

Unit: mm







Weight: 0.09 g (Typ.)

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