TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TD62300P,TD62300F

### 2CH LOW V<sub>CC</sub> SINK DRIVER

The TD62300P, TD62300F are comprised of two Low VCC drivers.

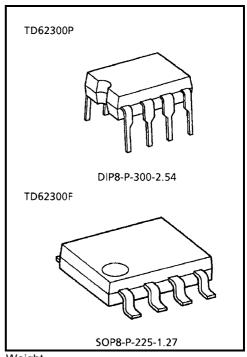
These devices can operate from  $V_{CC} = 1.0 \text{ V}$ , and suitable for various types of battery system.

Applications include relay, hammer, lamp and stepping motor drivers.

#### **FEATURES**

• Wide supply voltage range  $V_{CC} = 1.0 \sim 6.5 \text{ V}$ High output current (single output) : 200 mA (Max.) Low supply current :  $ICC (OFF) = 1 \mu A (Max.)$  $R_{IN} = 33 \text{ k}\Omega \text{ (Typ.)}$ Input resistor

Package type-P : DIP-8 pin Package type-F : SOP-8 pin

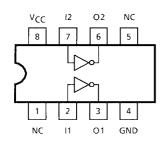


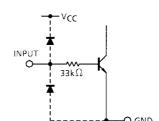
Weight

DIP8-P-300-2.54 : 0.52 g (Typ.) SOP8-P-225-1.27: 0.08 g (Typ.)

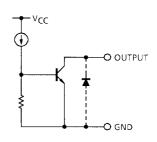
**Equivalent of output** 

#### **PIN CONNECTION (TOP VIEW) OUTPUT-INPUT EQUIVALENT CIRCUIT**





**Equivalent of input** 



The input and output parasitic diodes cannot be used as clamp diodes. Note:

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## **MAXIMUM RATINGS (Ta = 25°C)**

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Supply Voltage		V <sub>CC</sub>	7.0	V	
Output Sustaining Voltage		V <sub>CE</sub> (SUS)	8.0	V	
Output Current		lout	200	mA / ch	
Input Voltage		V <sub>IN</sub>	V <sub>CC</sub>	V	
Power Dissipation	TD62300P	Po	900	mW	
	TD62300F	- P <sub>D</sub>	480 (Note)		
Operating Temperature		T <sub>opr</sub>	0~70	°C	
Storage Temperature		T <sub>stg</sub>	-55~150	°C	

Note: On Glass Epoxy (20 × 20 × 1.6 mm Cu 50%)

## **RECOMMENDED OPERATING CONDITIONS (Ta = 0 \sim 70°C)**

CHARACTERISTIC		SYMBOL	CONDITION	MIN	TYP.	MAX	UNIT
Supply Voltage		V <sub>CC</sub>		1.0	_	6.5	V
Output Sustaining Voltage		V <sub>CE</sub> (SUS)		_	_	8	V
Output Current		lout		_	_	150	mA
Input Voltage		V <sub>IN</sub>		0	_	$V_{CC}$	V
Power Dissipation	TD62300P	P <sub>D</sub>		_	_	430	W
	TD62300F		(Note)	-	_	230	

Note: On Glass Epoxy (20 × 20 × 1.6 mm Cu 50%)

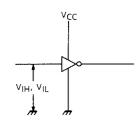
## **ELECTRICAL CHARACTERISTICS (Ta = 25°C)**

CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Input Voltage	"H" Level	$V_{IH}$	1		0.85	_	_	V
	"L" Level	$V_{IL}$	1		_	_	0.45	v
Input Current	"H" Level	I <sub>IH</sub>	2	V <sub>IN</sub> = 0.85 V	_	4.9	_	μA
Output Current	"H" Level	I <sub>OH</sub>	3	V <sub>CC</sub> = V <sub>OUT</sub> = 5.0 V	_	_	10	μA
Output Voltage	"L" Level	V <sub>OL</sub>	4	V <sub>CC</sub> = 1.4 V, I <sub>OUT</sub> = 140 mA	_	0.2	0.6	V
Supply Current		I <sub>CC (ON)</sub>	- 5	V <sub>CC</sub> = 1.4 V, V <sub>IN</sub> = 0.85 V	_	6.4	9.0	mA
		I <sub>CC (OFF)</sub>		V <sub>CC</sub> = 5.0 V, V <sub>IN</sub> = 0 V	_	_	1.0	μA
Turn-On Delay		t <sub>ON</sub>	6	$V_{CC}$ = 1.7 V, $R_L$ = 10 $\Omega$ $C_L$ = 15 pF	_	0.1	_	μs
Turn-Off Delay		t <sub>OFF</sub>			_	2.3	_	μs

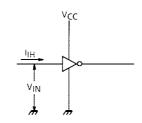
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#### **TEST CIRCUIT**

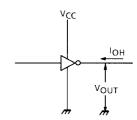
### 1. VIH, VIL



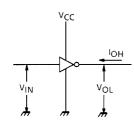
#### 2. I<sub>IH</sub>



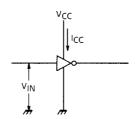
#### 3. I<sub>OH</sub>



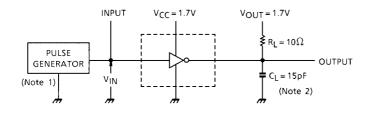
### 4. V<sub>OL</sub>

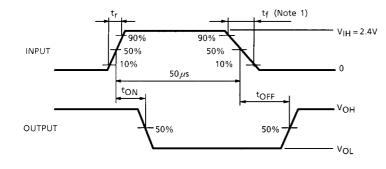


5. ICC (ON), ICC (OFF)



## 6. ton, toff





Note 1: Pulse Width 50  $\mu$ s, Duty Cycle 10% Output Impedance 50  $\Omega$ , tr  $\leq$  5 ns, tf  $\leq$  10 ns

Note 2: C<sub>L</sub> includes probe and jig capacitance.

## **PRECAUTIONS for USING**

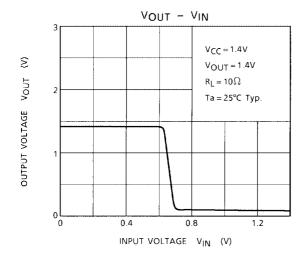
This IC does not include built-in protection circuits for excess current or overvoltage.

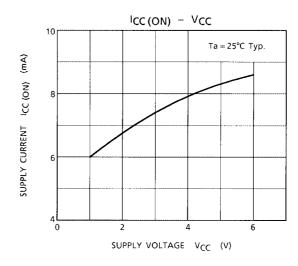
If this IC is subjected to excess current or overvoltage, it may be destroyed.

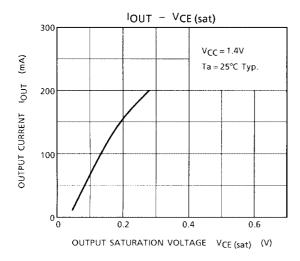
Hence, the utmost care must be taken when systems which incorporate this IC are designed.

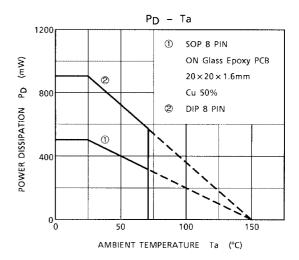
Utmost care is necessary in the design of the output line, V<sub>CC</sub>, COMMON and GND line since IC may be destroyed due to short–circuit between outputs, air contamination fault, or fault by improper grounding.

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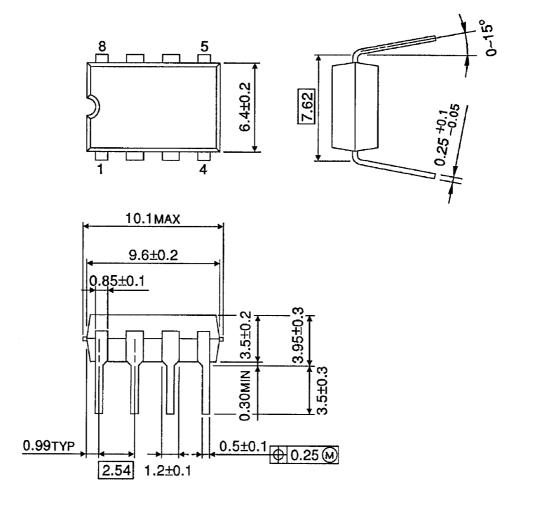




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### **PACKAGE DIMENSIONS**

DIP8-P-300-2.54 Unit: mm

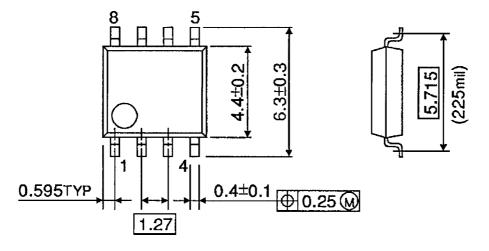


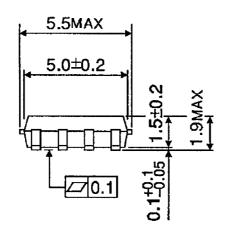
Weight: 0.52 g (Typ.)

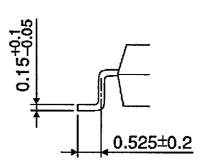
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### **PACKAGE DIMENSIONS**

SOP8-P-225-1.27 Unit: mm







Weight: 0.08 g (Typ.)

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#### **RESTRICTIONS ON PRODUCT USE**

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