TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7WZ00FU,TC7WZ00FK

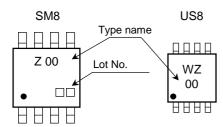
#### 2 Input Nand Gate

#### Features

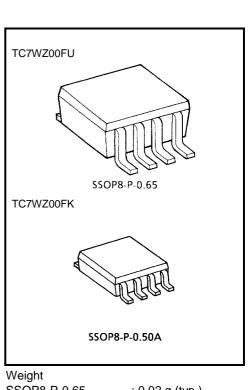
- High output drive: ±24 mA (min) @V<sub>CC</sub> = 3 V
- Super high speed operation: tpd 2.4 ns (typ.) @VCC = 5 V, 50 pF
- Operation voltage range:  $V_{CC}$  (opr) = 1.65~5.5 V
- Latch-up performance: ±500 mA or more
- ESD performance: ±200 V or more (JEITA)
  - $\pm 2000$  V or more (MIL)
- Power down protection is provided on all inputs and outputs.
- Matches the performance of TC74LCX series when operated at 3.3 V VCC.

### Marking

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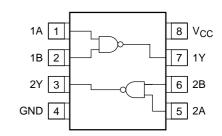
# Maximum Ratings (Ta = 25°C)



SSOP8-P-0.65 SSOP8-P-0.50A

: 0.02 g (typ.) : 0.01 g (typ.)

# Pin Assignment (top view)



Characteristics	Symbol	Rating	Unit	
Power supply voltage	V <sub>CC</sub>	-0.5~6	V	
DC input voltage	V <sub>IN</sub>	-0.5~6	V	
DC output voltage	V <sub>OUT</sub>	-0.5~6	V	
Input diode current	I <sub>IK</sub>	-20	mA	
Output diode current	IOK	-20	mA	
DC output current	IOUT	±50	mA	
DC V <sub>CC</sub> /ground current	ICC	±50	mA	
Power dissipation	PD	300 (SM8) 200 (US8)	mW	
Storage temperature	T <sub>stg</sub>	-65~150	°C	
Lead temperature (10s)	ΤL	260	°C	

# **TOSHIBA**

# **Truth Table**

# Logic Diagram

А	В	Y
L	L	Н
L	Н	Н
н	L	Н
Н	Н	L



# **Recommended Operating Conditions**

Characteristics	Symbol	Rating	Unit	
Supply voltage	Vcc	1.65~5.5	V	
Supply vollage	VCC	1.5~5.5 (Note 1)	v	
Input voltage	V <sub>IN</sub>	0~5.5	V	
Output voltage	Vout	0~5.5 (Note 2)	v	
	V001	0~V <sub>CC</sub> (Note 3)		
Operating temperature	T <sub>opr</sub>	-40~85	°C	
	d <sub>t</sub> /d <sub>v</sub>	0~20 (V_{CC} = 1.8 V $\pm$ 0.15 V, 2.5 V $\pm$ 0.2 V)	ns/V	
Input rise and fall time		0~10 (V_{CC} = 3.3 V $\pm$ 0.3 V)		
		0~5 (V <sub>CC</sub> = 5.5 V $\pm$ 0.5 V)		

Note 1: Data retention only

Note 2:  $V_{CC} = 0 V$ 

Note 3: High or low state

# **Electrical Characteristics**

### **DC Characteristics**

Characteristics Sym		Symbol Test Condition			Ta = 25°C			Ta = -40~85°C		Unit	
		Symbol			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
					1.65~ 1.95	$\begin{array}{c} 0.75 \\ \times  V_{CC} \end{array}$	_	_	$\begin{array}{c} 0.75 \\ \times  V_{CC} \end{array}$	_	
Input	High level	High level V <sub>IH</sub>				$0.7 \\ \times V_{CC}$	_	_	$0.7 \\ \times V_{CC}$	_	Ň
voltage	Low level				1.65~ 1.95	_		$\begin{array}{c} 0.25 \\ \times \ V_{CC} \end{array}$	_	$\begin{array}{c} 0.25 \\ \times \ V_{CC} \end{array}$	V
	Low level	VIL			2.3~5.5	_		$0.3 \\ \times V_{CC}$	_	$0.3 \\ \times V_{CC}$	
					1.65	1.55	1.65		1.55		
				I <sub>OH</sub> = −100 μA	2.3	2.2	2.3	_	2.2	_	
				10H – -100 μA	3.0	2.9	3.0		2.9	—	
		V <sub>OH</sub>	VIN = VIH or VIL		4.5	4.4	4.5		4.4	—	
	High level			$I_{OH} = -4 \text{ mA}$	1.65	1.29	1.52		1.29	—	
				I <sub>OH</sub> = -8 mA	2.3	1.9	2.15		1.9	—	
				I <sub>OH</sub> = -16 mA	3.0	2.4	2.8		2.4	—	
				I <sub>OH</sub> = -24 mA	3.0	2.3	2.68		2.3	—	
Output				I <sub>OH</sub> = -32 mA	4.5	3.8	4.2	_	3.8	—	
voltage	Low level	V <sub>OL</sub>	VIN = VIH	l <sub>OL</sub> = 100 μΑ	1.65		0	0.1		0.1	
					2.3		0	0.1		0.1	
					3.0	—	0	0.1		0.1	
					4.5		0	0.1		0.1	
				$I_{OL} = 4 \text{ mA}$	1.65	—	0.08	0.24	—	0.24	
				I <sub>OL</sub> = 8 mA	2.3		0.1	0.3		0.3	
				I <sub>OL</sub> = 16 mA	3.0		0.15	0.4		0.4	
				I <sub>OL</sub> = 24 mA	3.0		0.22	0.55		0.55	
				I <sub>OL</sub> = 32 mA	4.5		0.22	0.55		0.55	
Input leakage	Input leakage current $I_{IN}$ $V_{IN} = 5.5$ V or GND		0~5.5			±1		±10	μA		
Power off lea	kage current	IOFF	$V_{\text{IN}} \text{ or } V_{\text{OL}}$		0.0			1		10	μA
$\label{eq:Quiescent supply current} Quiescent supply current \qquad I_{CC} \qquad V_{IN} = 5.5 \ V \ or \ G$		/ or GND	1.65~5.5		—	1	—	10	μA		

# AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$ )

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit
Characteristics		Test Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	<sup>t</sup> pLH t <sub>pHL</sub>	C <sub>L</sub> = 15 pF, R <sub>L</sub> = 1 MΩ C <sub>L</sub> = 50 pF, R <sub>L</sub> = 500 Ω	$\textbf{1.8}\pm\textbf{0.15}$	2.0	5.3	9.6	2.0	9.8	ns
			$\textbf{2.5}\pm\textbf{0.2}$	1.2	3.2	5.3	1.2	5.7	
			$\textbf{3.3}\pm\textbf{0.3}$	0.8	2.4	3.7	0.8	4.0	
			$5.0\pm0.5$	0.5	1.9	2.9	0.5	3.2	
			$\textbf{3.3}\pm\textbf{0.3}$	1.2	3.0	4.6	1.2	4.9	
			$5.0\pm0.5$	0.8	2.4	3.6	0.8	3.9	
Input capacitance	C <sub>IN</sub>	—	0~5.5	_	3.0	_	_	—	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note)	3.3	_	22			_	рF
			5.5		32				

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

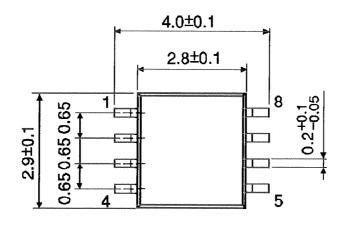
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$ 

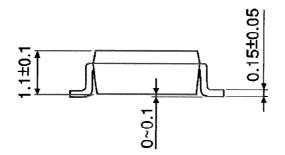
# **TOSHIBA**

# **Package Dimensions**

SSOP8-P-0.65

Unit : mm





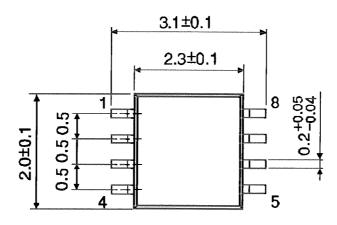
Weight: 0.02 g (typ.)

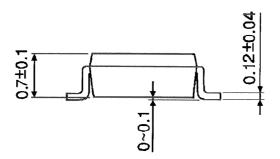
# **TOSHIBA**

# **Package Dimensions**

SSOP8-P-0.50A

Unit : mm





Weight: 0.01 g (typ.)

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