TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WBD125FK

Dual Bus Switch with Level Shift

The TC7WBD125FK is a low on-resistance, high-speed CMOS 2-bit bus switch. This bus switch allows the connections or disconnections to be made with minimal propagation delay while maintaining Low power dissipation which is the feature of CMOS.

When output enable (\overline{OE}) is at low level, the switch is on; when at high level, the switch is off.

The internal diode which adds to power supply line is enable to realize the shift of signal level from 5 V to 3.3 V. (Note 1)

All inputs are equipped with protector circuits to protect the device from static discharge.

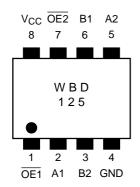
Features

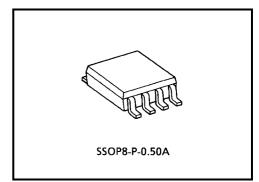
- Operating voltage: $V_{CC} = 4.5 \sim 5.5 V$
- High speed operation: $t_{pd} = 0.25 \text{ ns} (\text{max})$
- Ultra-low on resistance: $R_{ON} = 5 \Omega$ (typ.)
- Electro-static discharge (ESD) performance: ±200 V or more (JEITA)

±2000 V or more (MIL)

- TTL level input (control input)
- Package: US8
 - Note 1: In case that over-shoot noise is detected, this device should be used with clamp diode to prevent the next stage device from over-stress.

Pin Assignment (top view)





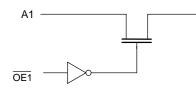
Weight: 0.01 g (typ.)

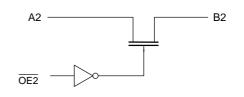
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Truth Table

Inputs	Function		
OE	Function		
L	A port = B port		
Н	Disconnect		

System Diagram





Maximum Ratings

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5~7.0	V
Control pin input voltage	V _{IN}	-0.5~7.0	V
Switch terminal I/O voltage	VS	-0.5~7.0	V
Clump diode current	I _{IK}	-50	mA
Switch I/O current	۱ _S	128	mA
Power dissipation	PD	200	mW
DC V _{CC} /GND current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65~150	°C

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Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	4.5~5.5	V
Control pin input voltage	V _{IN}	0~5.5	V
Switch I/O voltage	VS	0~5.5	V
Operating temperature	T _{opr}	-40~85	°C
Control pin input rise/fall time	dt/dv	0~10	ns/V

Electrical Characteristics

DC Characteristics (Ta = -40~85°C)

Character	istics	Symbol	Test Condition		V _{CC} (V)	Min	Typ. (Note 2)	Max	Unit
Control pin input	"H" level	VIH	_		4.5~5.5	2.0	—	_	V
voltage	"L" level	VIL	_		4.5~5.5			0.8	v
High-level output	voltage	V _{OH}	Figure 4				—	_	_
Input leakage cur	rent	I _{IN}	V _{IN} = 0~5.5 V		4.5~5.5	_	—	±1.0	μA
Power off leakage	e current	I _{OFF}	A, B, \overline{OE} = 0~5.5 V		0			±1.0	μA
Off-state leakage (switch		I _{SZ}	A, B = 0~5.5 V, $\overline{OE} = V_{CC}$		4.5~5.5		_	±1.0	μA
				I _{IS} = 64 mA	4.5		5	7	
ON resistance	(Note 3)	R _{ON}	$V_{IS} = 0 V$	I _{IS} = 30 mA	4.5		5	7	Ω
			$V_{IS} = 2.4 \text{ V}, I_{IS} = 15 \text{ mA}$		4.5		35	50	
		1	$V_{IN} = V_{CC}$ or GND	switch ON	5.5			1.5	mA
Quiescent supply	Quiescent supply current		$I_{OUT} = 0$	switch OFF	5.5		—	10	μA
		ΔI_{CC}	V _{IN} = 3.4 V (one input)		5.5	_	—	2.5	mA

Note 2: The typical values are at $V_{CC} = 5 \text{ V}$, Ta = 25°C.

AC Characteristics ($Ta = -40 \sim 85^{\circ}C$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{pLH}	Figure 1, Figure 2 (Note 4)	4.5		0.25	ns
(bus to bus)	t _{pHL}		ч.0		0.20	113
Output enable time	t _{pZL}	Figure 1, Figure 3	4.5		4.5	ns
	t _{pZH}		4.5		4.0	115
Output disable time	t _{pLZ}	Figure 1, Figure 3	4.5		5.0	ns
	t _{pHZ}		4.5		5.0	115

Note 4: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

Capacitive Characteristics (Ta = 25°C)

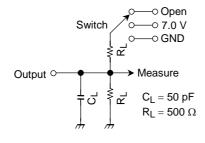
Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Control pin input capacitance	C _{IN}	(Not	e 5) 5.0	3	pF
Switch terminal capacitance	C _{I/O}	$\overline{OE} = V_{CC}$ (Not	e 5) 5.0	10	pF

Note 5: This parameter is guaranteed by design.

Note 3: Apply the specified current to the switch, then measure the voltages on pins A and B. The on-resistance is the lower of the two.

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AC Test Circuit



Parameter	Switch
t _{pLH} , t _{pHL}	Open
t _{pLZ} , t _{pZL}	7.0 V
t _{pHZ} , t _{pZH}	Open



AC Waveform

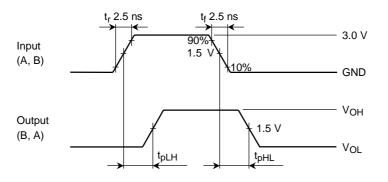
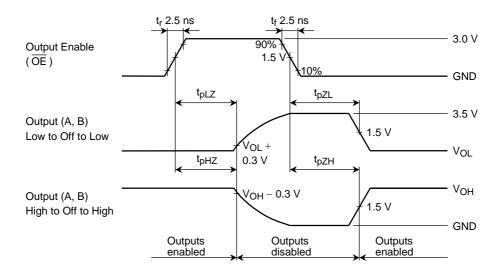
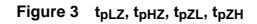
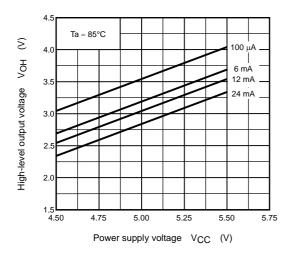


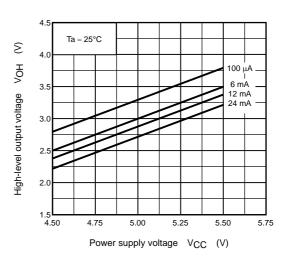
Figure 2 t_{pLH}, t_{pHL}





V_{OH} – V_{CC} Characteristics (typ.)





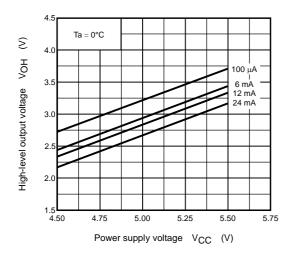


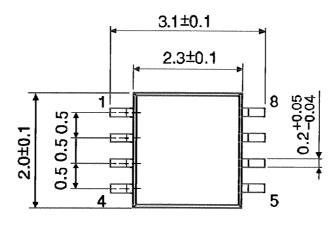
Figure 4

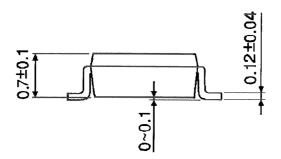
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Package Dimensions

SSOP8-P-0.50A

Unit : mm





Weight: 0.01 g (typ.)

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Handbook" etc..

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