TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SBD384AFU

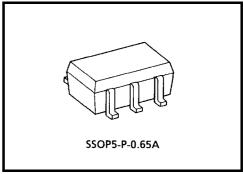
#### Single Bus Switch with Level Shifting

The TC7SBD384AFU provides single bit of high-speed TTL-compatible switching. The low on resistance of the switch allows connections to be made with minimal propagation delay.

The device is organized as just 1-bit low-impedance switch with output-enable  $(\overline{OE})$  input. When  $\overline{OE}$  is low, the switch is on and data can flow from port A to port B, or vice versa. When  $\overline{OE}$  is high, the switch is open and a high-impedance state exists between the two ports.

The device is enable to realize the shift of signal level from 5 V to 3.3 V.

All inputs are equipped with protection circuits against static discharge.



Weight: 0.006 g (typ.)

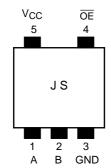
#### **Features**

- Operating voltage:  $V_{CC} = 4.5 \sim 5.5 \text{ V}$
- High speed operation:  $t_{pd} = 0.32 \text{ ns (max)}$
- Low on resistance:  $R_{ON} = 5 \Omega$  (typ.)
- ESD performance: Machine model  $> \pm 200 \text{ V}$

Human body model  $> \pm 2000 \text{ V}$ 

- TTL level input (control input)
- Low Power Dissipation: Icc = 10 μA (max.)
- Package: USV

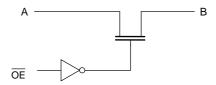
### Pin Assignment (top view)



## **Truth Table**

Input	Function		
ŌĒ	Function		
L	A port = B port		
Н	Disconnect		

## **System Diagram**



## **Maximum Ratings**

Characteristics	Symbol	Rating	Unit
Power supply range	V <sub>CC</sub>	-0.5~7.0	V
DC input voltage	V <sub>IN</sub>	-0.5~7.0	V
DC switch voltage	Vs	-0.5~7.0	V
Input diode current	I <sub>IK</sub>	-50	mA
Continuous channel current	IS	128	mA
Power dissipation	P <sub>D</sub>	200	mW
DC V <sub>CC</sub> /GND current	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA
Storage temperature	T <sub>stg</sub>	-65~150	°C

# **Recommended Operating Conditions**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	4.5~5.5	V
Input voltage	V <sub>IN</sub>	0~5.5	V
Switch voltage	Vs	0~5.5	V
Operating temperature	T <sub>opr</sub>	-40~85	°C
Input rise and fall time	dt/dv	0~10	ns/V

#### **Electrical Characteristics**

### DC Characteristics ( $Ta = -40 \sim 85$ °C)

Charac	teristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Typ. (Note 1)	Max	Unit
Input voltage	"H" level	V <sub>IH</sub>	_		4.5~5.5	2.0	_	_	V
input voitage	"L" level	VIL	_		4.5~5.5	_	_	0.8	V
High lovel outp	ut voltogo		IOH- 1A		4.75	2.3	2.8	3.2	
High-level outp	(Note 2)	Voн	IOH=-1µA		5.0	2.5	3.0	3.4	V
	(Note 2)		VIS - VCC	$V_{IS} = V_{CC}$		2.7	3.2	3.6	
Input leakage of	current	I <sub>IN</sub>	V <sub>IN</sub> = 0~5.5 V		4.5~5.5		_	±1.0	μΑ
Power off leaka	age current	loff	A, B, $\overline{OE}$ = 0~5.5 V		0	_	_	±1.0	μΑ
Off-STATE leaf (switch off)	kage current	I <sub>SZ</sub>	A, B = $0 \sim 5.5 \text{ V}$ , $\overline{\text{OE}} = \text{V}_{\text{CC}}$		4.5~5.5	_	_	±1.0	μΑ
			I <sub>IS</sub> = 64 mA	4.5	_	5	9		
			V <sub>IS</sub> = 0 V	IIS = 64 IIIA	4.75	_	5	8	
ON resistance		Ron	VIS = 0 V	I <sub>IS</sub> = 30 mA	4.5	_	5	9	Ω
	(Note 3)	KON		IIS – 30 IIIA	4.75	_	5	8	52
	V <sub>IS</sub> = 2.3 V, I <sub>IS</sub> = 15 mA		4.5	_	35	65			
			4.75	_	35	50			
Quiescent supp	oly current	ICC	VIN = VCC or GND,I <sub>OUT</sub> = 0		5.5		_	10	μΑ
Increase in I <sub>CC</sub>	per input	Δlcc	V <sub>IN</sub> = 3.4 V (one input)		5.5	_	_	2.5	mA

Note 1: Typical values are at  $V_{CC} = 5 \text{ V}$ ,  $Ta = 25^{\circ}\text{C}$ .

Note 2: It recommends that this device uses Pull-up resistance when adding and using resistance for an output terminal. Since it couses to drop a VOH voltage level when using Pull-down resistance for an output terminal.

Note 3: Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

### AC Characteristics ( $Ta = -40 \sim 85$ °C)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Propagation delay time (bus to bus)	t <sub>pLH</sub>	Figure 1, Figure 2 (Note 4)	4.5	ı	0.32	ns
Output enable time	t <sub>pZL</sub>	Figure 1, Figure 3	4.5		4.5	ns
Output disable time	t <sub>pLZ</sub> t <sub>pHZ</sub>	Figure 1, Figure 3	4.5	_	4.5	ns

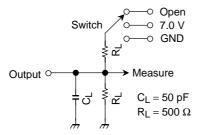
Note 4: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

### **Capacitive Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Control pin input capacitance	C <sub>IN</sub>	(Note 5)	5.0	3	pF
Switch terminal capacitance	C <sub>I/O</sub>	$\overline{OE} = V_{CC}$ (Note 5)	5.0	10	pF

Note 5: This parameter is guaranteed by design.

#### **AC Test Circuit**



Parameter	Switch
t <sub>pLH</sub> , t <sub>pHL</sub>	Open
t <sub>pLZ</sub> , t <sub>pZL</sub>	7.0 V
t <sub>pHZ</sub> , t <sub>pZH</sub>	GND

Figure 1

## **AC Waveform**

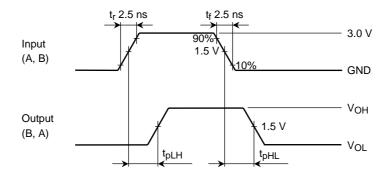


Figure 2 t<sub>pLH</sub>, t<sub>pHL</sub>

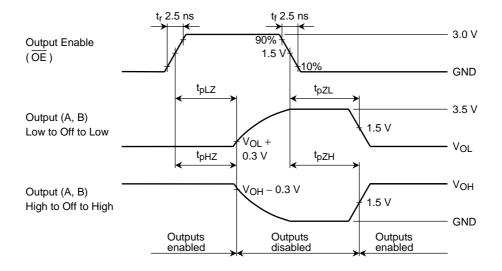
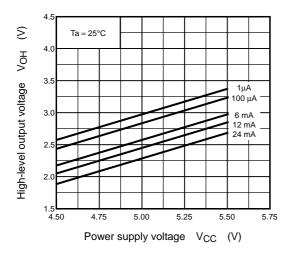
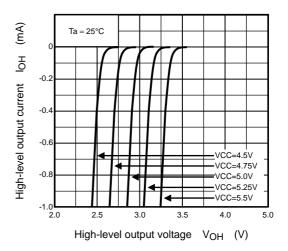
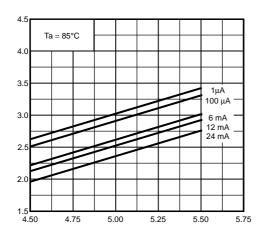


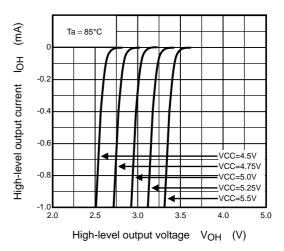
Figure 3  $t_{pLZ}$ ,  $t_{pHZ}$ ,  $t_{pZL}$ ,  $t_{pZH}$ 

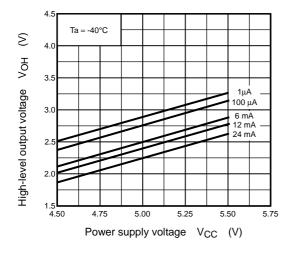
## V<sub>OH</sub> - V<sub>CC</sub> Characteristics (typ.)











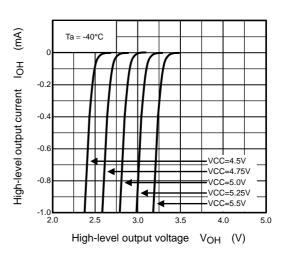
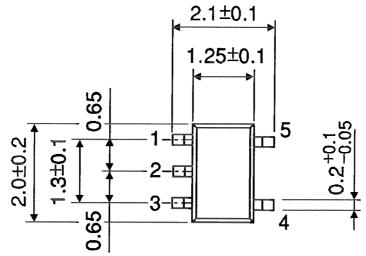


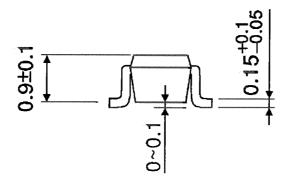
Figure 4

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## **Package Dimensions**

SSOP5-P-0.65A Unit: mm





Weight: 0.006 g (typ.)

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