TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SB385FU

Single Bus Switch

The TC7SB385FU provides single bit of high-speed TTL-compatible switching. The low on resistance of the switch allows connections to be made with minimal propagation delay.

The device is organized as just 1-bit low-impedance switch with output-enable (OE) input. When OE is high, the switch is on and data can flow from port A to port B, or vice versa. When OE is low, the switch is open and a high-impedance state exists between the two ports.

All inputs are equipped with protection circuits against static discharge.

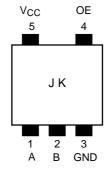
Features

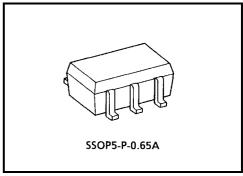
- Operating voltage: $V_{CC} = 4.5 \sim 5.5 \text{ V}$
- High speed operation: $t_{pd} = 0.25 \text{ ns (max)}$
- Low on resistance: $RON = 5 \Omega$ (typ.)
- ESD performance: Machine model > ±200 V

Human body model $> \pm 2000 \text{ V}$

- TTL level input (control input)
- · Package: USV

Pin Assignment (top view)



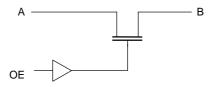


Weight: 0.006 g (typ.)

Truth Table

Input	Function
OE	Tunction
L	Disconnect
Н	A port = B port

System Diagram



Maximum Ratings

Characteristics	Symbol	Rating	Unit
Power supply range	V _{CC}	-0.5~7.0	V
DC input voltage	V _{IN}	-0.5~7.0	V
DC switch voltage	Vs	-0.5~7.0	V
Input diode current	I _{IK}	-50	mA
Continuous channel current	IS	128	mA
Power dissipation	PD	200	mW
DC V _{CC} /GND current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65~150	°C

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5~5.5	V
Input voltage	V _{IN}	0~5.5	V
Switch voltage	Vs	0~5.5	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~10	ns/V

Electrical Characteristics

DC Characteristics ($Ta = -40 \sim 85$ °C)

Characte	rietice	Symbol	Test Condition		Min	Тур.	Max	Unit	
Characte	Hotico	Symbol	rest c	ondition	V _{CC} (V)	IVIIII	(Note1)	IVIAX	Offic
Input voltage	"H" level	V _{IH}		_	4.5~5.5	2.0	_	_	V
input voltage	"L" level	V_{IL}		_	4.5~5.5	_		0.8	v
Input leakage cu	rrent	I _{IN}	V _{IN} = 0~5.5 V		4.5~5.5	_	_	±1.0	μА
Power off leakag	e current	I _{OFF}	A, B, OE = 0~5.5 V		0	_	_	±1.0	μΑ
Off-state leakage (switch off)	current	I _{SZ}	A, B = 0~5.5 V, OE = GND		4.5~5.5	_	_	±1.0	μА
ON registeres			I _{IS} = 30 mA		4.5	_	5	7	
ON resistance	(Note2)	R _{ON}	$V_{IS} = 0 V$	I _{IS} = 64 mA	4.5	_	5	7	Ω
			V _{IS} = 2.4 V, I _{IS} = 15 mA		4.5	_	10	15	
Quiescent supply	/ current	Icc	$V_{IN} = V_{CC}$ or GND, $I_{OUT} = 0$		5.5	_	_	10	μА
Quiescent suppry	- Current	Δlcc	V _{IN} = 3.4 V (one input)		5.5	_	_	2.5	mA

Note1: Typical values are at $V_{CC} = 5 \text{ V}$ and $Ta = 25^{\circ}\text{C}$.

Note2: Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

AC Characteristics ($Ta = -40 \sim 85$ °C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time (bus to bus)	t _{pLH}	Figure 1, Figure 2 (Note3)	4.5	_	0.25	ns
Output enable time	t _{pZL}	Figure 1, Figure 3	4.5		4.0	ns
Output disable time	t _{pLZ}	Figure 1, Figure 3	4.5	_	5.0	ns

Note3: This parameter is guaranteed by design but is not tested. The bus switch contributes no propagation delay other than the RC delay of the typical on resistance of the switch and the 50 pF load capacitance, when driven by an ideal voltage the source (zero output impedance).

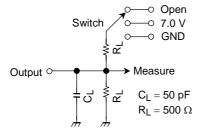
Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Тур.	Unit
Control pin input capacitance	C _{IN}		(Note4)	5.0	3	pF
Switch terminal capacitance	C _{I/O}	OE = GND	(Note4)	5.0	10	pF

3

Note4: This item is guaranteed by design.

AC Test Circuit



Parameter	Switch
t _{pLH} , t _{pHL}	Open
t _{pLZ} , t _{pZL}	7.0 V
t _{pHZ} , t _{pZH}	Open

Figure 1

AC Waveform

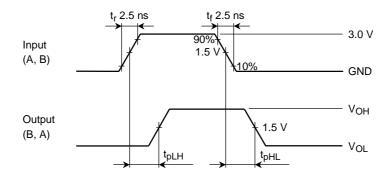


Figure 2 t_{pLH}, t_{pHL}

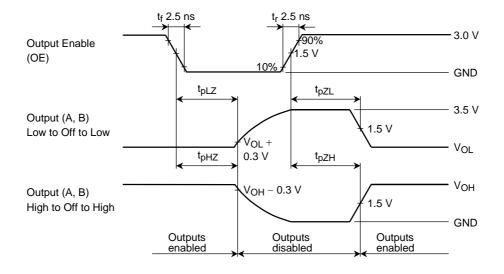
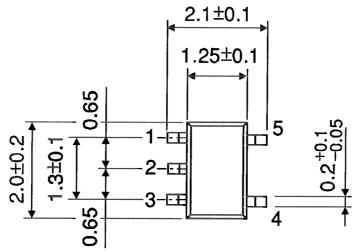
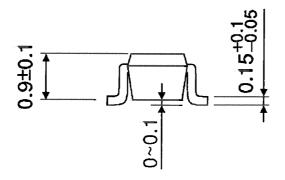


Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

Package Dimensions

SSOP5-P-0.65A Unit: mm





Weight: 0.006 g (typ.)

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