TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7MBD3245FK

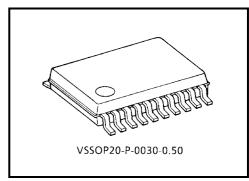
Octal Bus Switch

The TC7MBD3245FK provides eight bits of high-speed TTL-compatible bus switching in a standard '245 device pinout. The low on-state resistance of the switch allows connections to be made with minimal propagation delay.

The device is organized as one 8-bit switch. When output enable (\overline{OE}) is low, the switch is on and port A is connected to port B. When \overline{OE} is high, the switch is open and a high-impedance state exists between the two ports.

The internal diode which adds to power supply line is enable to realize the shift of signal level from $5\ V$ to $3.3\ V$. (Note 1)

All inputs are equipped with protection circuits against static discharge.



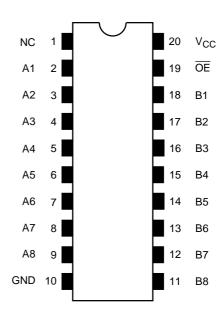
Weight: 0.03 g (typ.)

Features

- Operating voltage: V_{CC} = 4.5~5.5 V
- High speed: $t_{pd} = 0.25 \text{ ns (max)}$
- Low on resistance: $RON = 5 \Omega$ (typ.)
- ESD performance: Human body model > $\pm 2000 \text{ V}$ Machine model > $\pm 200 \text{ V}$
- Compatible with TTL outputs (control inputs)
- Package: VSSOP (US20)
- Pin compatible with the 74xx245 type.
- Functionally equivalent to (FST/CBT) 3245.

Note 1: In case that over-shoot noise is detected, this device should be used with clamp diode to prevent the next stage device from over-stress.

Pin Assignment (top view)

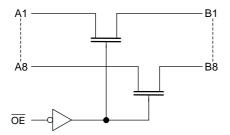


NC-No Internal Connection

Truth Table

Inputs	Function		
OE	1 diletion		
L	A port = B port		
Н	Disconnect		

System Diagram



Maximum Ratings

Characteristics	Symbol	Rating	Unit
Power supply range	V _{CC}	-0.5~7.0	V
DC input voltage	V _{IN}	-0.5~7.0	V
DC switch voltage	Vs	-0.5~7.0	V
Input diode current	I _{IK}	-50	mA
Continuous channel circuit	IS	128	mA
Power dissipation	P _D	180	mW
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65~150	°C

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5~5.5	V
Input voltage	V _{IN}	0~5.5	V
Switch voltage	V _S	0~5.5	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~10	ns/V

2



Electrical Characteristics

DC Characteristics ($Ta = -40 \sim 85$ °C)

Charac	teristics	Symbol	Test Condition		V _{CC} (V)	Min	Typ. (Note 2)	Max	Unit	
Input voltage	"H" level	V _{IH}	_	-	4.5~5.5	2.0	_	_	V	
Input voltage	"L" level	V _{IL}	_	-	4.5~5.5	_	_	0.8	V	
High-level outp	ut voltage	V _{OH}	Figure 4		_	_	_	_	_	
Input leakage of	current	I _{IN}	V _{IN} = 0~5.5 V		4.5~5.5	_	_	±1.0	μΑ	
Power off leaka	age current	loff	A, B, $\overline{OE} = 0 \sim 5.5 \text{ V}$		0	_	_	±1.0	μΑ	
Off-STATE leal (switch off)	kage current	I _{SZ}	A, B = 0 -5.5 V, $\overline{OE} = V_{CC}$		4.5~5.5	_	_	±1.0	μА	
ON resistance			V 0.V	I _{IS} = 64 mA	4.5	_	5	7		
ON resistance	(Note 3)	R _{ON}	R_{ON}	$V_{IS} = 0 V$	I _{IS} = 30 mA	4.5	_	5	7	Ω
	(Note 3)		V _{IS} = 2.4 V, I _{IS} = 15 m.	I _S = 2.4 V, I _{IS} = 15 mA		_	35	15		
Quiescent supply current I _{CC}		V _{IN} = V _{CC} or GND	Switch ON	5.5	_	_	1.5	mA		
Quiescent Supp	ory current	Icc	I _{OUT} = 0	Switch OFF	5.5	_	_	10	μА	
Increase in I _{CC}	; per input	Δlcc	V _{IN} = 3.4 V (one input)		5.5	_	_	2.5	mA	

Note 2: Typical values are at $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}\text{C}$.

Note 3: Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

AC Characteristics ($Ta = -40 \sim 85$ °C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time (bus to bus)	t _{pLH}	Figure 1, Figure 2 (Note 4)	4.5	_	0.25	ns
Output enable time	t _{pZL}	Figure 1, Figure 3	4.5	_	7.0	ns
Output disable time	t _{pLZ}	Figure 1, Figure 3	4.5	_	6.0	ns

Note 4: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

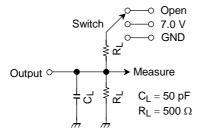
Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Control pin input capacitance	C _{IN}	(Note 5)	5.0	3	pF
Switch terminal capacitance	C _{I/O}	$\overline{OE} = V_{CC}$ (Note 5)	5.0	10	pF

3

Note 5: This parameter is guaranteed by design.

AC Test Circuit



Paramenter	Switch		
t _{pLH} , t _{pHL}	Open		
t_{pLZ}, t_{pZL}	7.0 V		
t _{pHZ} , t _{pZH}	Open		

Figure 1

AC Waveform

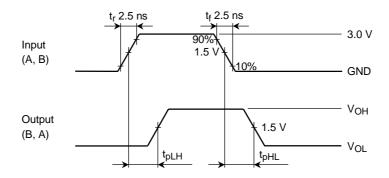


Figure 2 t_{pLH}, t_{pHL}

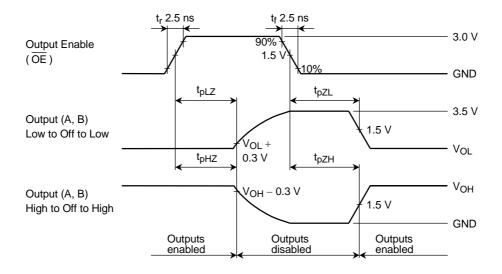
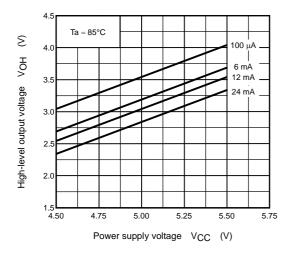
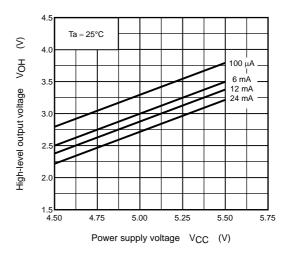


Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

V_{OH} – V_{CC} Characteristics (typ.)





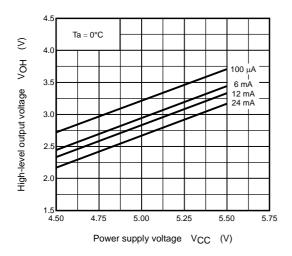
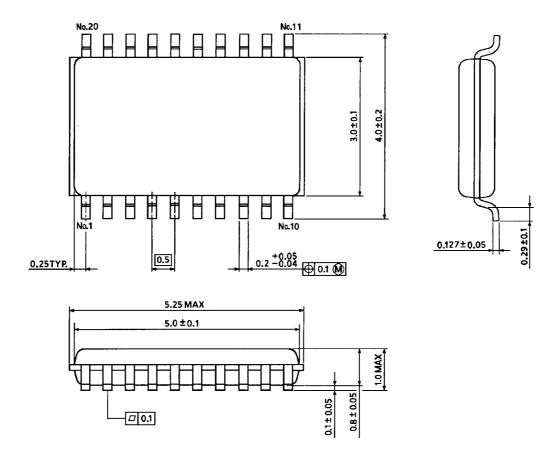


Figure 4

Package Dimensions



6

Weight: 0.03 g (typ.)

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