Toshiba CMOS Digital Integrated Circuit Silicon Monolithic

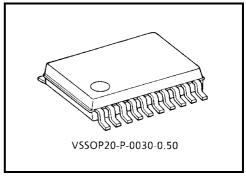
TC7MB3245FK

Octal Bus Switch

The TC7MB3245FK provides eight bits of high-speed TTL-compatible bus switching in a standard '245 device pinout. The low on-state resistance of the switch allows connections to be made with minimal propagation delay.

The device is organized as one 8-bit switch. When output enable (\overline{OE}) is low, the switch is on and port A is connected to port B. When \overline{OE} is high, the switch is open and a high-impedance state exists between the two ports.

All inputs are equipped with protection circuits against static discharge.

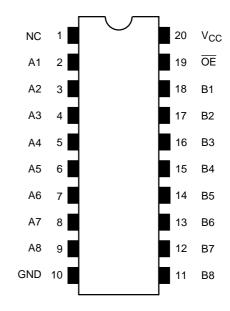


Weight: 0.03 g (typ.)

Features

- Operating voltage: $V_{CC} = 4.5 \sim 5.5 \text{ V}$
- High speed: $t_{pd} = 0.25 \text{ ns (max)}$
- Low on resistance: $RON = 5 \Omega$ (typ.)
- ESD performance: Human body model > $\pm 2000 \text{ V}$ Machine model > $\pm 200 \text{ V}$
- Compatible with TTL outputs (control inputs)
- Package: VSSOP (US20)
- Pin compatible with the 74xx245 type. Functionally equivalent to (FST/CBT) 3245.

Pin Assignment (top view)

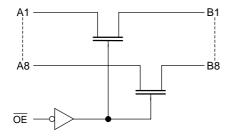


NC-No Internal Connection

Truth Table

Inputs	Function		
ŌE	Function		
L	A port = B port		
Н	Disconnect		

System Diagram



Maximum Ratings

Characteristics	Symbol	Rating	Unit
Power supply range	V _{CC}	-0.5~7.0	V
DC input voltage	V _{IN}	-0.5~7.0	V
DC switch voltage	Vs	-0.5~7.0	V
Input diode current	I _{IK}	-50	mA
Continuous channel circuit	IS	128	mA
Power dissipation	PD	180	mW
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65~150	°C

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5~5.5	V
Input voltage	V _{IN}	0~5.5	V
Switch voltage	V _S	0~5.5	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~10	ns/V

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Electrical Characteristics

DC Characteristics ($Ta = -40 \sim 85$ °C)

Charac	teristics	Symbol	Test Condition V _{CC} (V)		Min	Typ. (Note1)	Max	Unit	
Input voltage	"H" level	V_{IH}	_	-	4.5~5.5	2.0	_	_	V
iliput voltage	"L" level	V _{IL}	_	-	4.5~5.5	_	_	0.8	V
Input leakage of	current	I _{IN}	V _{IN} = 0~5.5 V		4.5~5.5	_	_	±1.0	μА
Power off leaka	age current	loff	A, B, $\overline{OE} = 0 \sim 5.5 \text{ V}$ 0		0	_	_	±1.0	μА
Off-STATE lea (switch off)	kage current	I _{SZ}	A, B = $0 \sim 5.5 \text{ V}$, $\overline{\text{OE}} = \text{V}_{\text{CC}}$		4.5~5.5	_	_	±1.0	μА
ON registance			V 0 V	I _{IS} = 64 mA	4.5	_	5	7	
ON resistance	(Note2)	R _{ON}	$V_{IS} = 0 V$	I _{IS} = 30 mA	4.5	_	5	7	Ω
	(140162)		V _{IS} = 2.4 V, I _{IS} = 15 mA		4.5	_	10	15	
Quiescent supp	oly current	Icc	$V_{IN} = V_{CC}$ or GND, $I_{OUT} = 0$ 5.5		5.5		_	10	μА
Increase in I _{CC}	per input	Δlcc	V _{IN} = 3.4 V (one input)		5.5	_	_	2.5	mA

Note 1: Typical values are at $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}\text{C}$.

Note 2: Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

AC Characteristics ($Ta = -40 \sim 85$ °C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time (bus to bus)	t _{pLH}	Figure 1, Figure 2 (Note3)	4.5	_	0.25	ns
Output enable time	t _{pZL}	Figure 1, Figure 3	4.5		5.9	ns
Output disable time	t _{pLZ}	Figure 1, Figure 3	4.5		5.9	ns

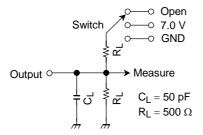
Note 3: This parameter is guaranteed by design but is not tested. The bus switch contributes no propagation delay other than the RC delay of the typical On resistance of the switch and the 50 pF load capacitance, when driven by an ideal voltage the source (zero output impedance).

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Control pin input capacitance	C _{IN}	(Note4)	5.0	3	pF
Switch terminal capacitance	C _{I/O}	$\overline{OE} = V_{CC}$ (Note4)	5.0	10	pF

Note 4: Parameter guaranteed by design.

AC Test Circuit



Parameter	Switch
t _{pLH} , t _{pHL}	Open
t _{pLZ} , t _{pZL}	7.0 V
t _{pHZ} , t _{pZH}	Open

Figure 1

AC Waveform

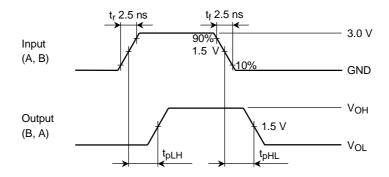


Figure 2 t_{pLH}, t_{pHL}

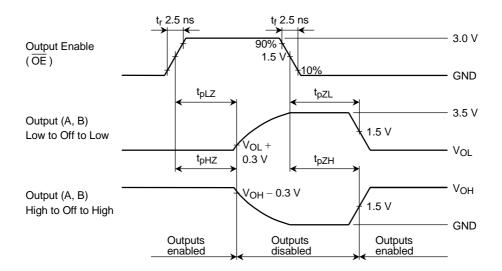
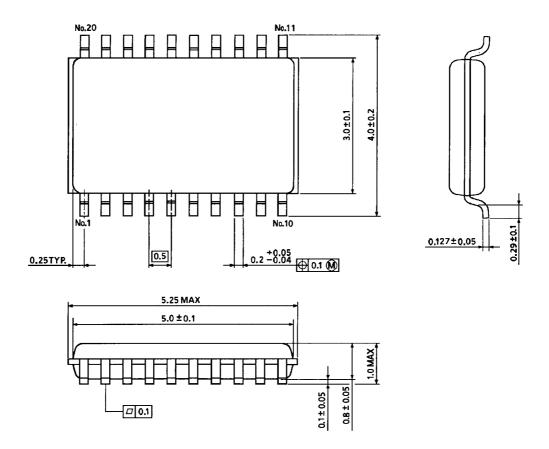


Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

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Package Dimensions



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Weight: 0.03 g (typ.)

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