<u>TOSHIBA</u>

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX125F,TC74LCX125FN,TC74LCX125FT

Low-Voltage Quad Bus Buffer with 5-V Tolerant Inputs and Outputs

The TC74LCX125F/FN/FT is a high-performance CMOS quad bus buffers. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

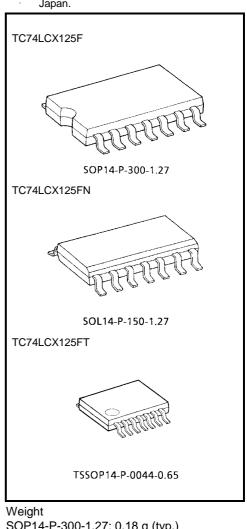
The device is designed for low-voltage $(3.3 \text{ V}) \text{ V}_{CC}$ applications, but it could be used to interface to 5-V supply environment for inputs.

This device requires the 3-state control input \overline{OE} to be set high to place the output into the high impedance state.

All inputs are equipped with protection circuits against static discharge.

Features

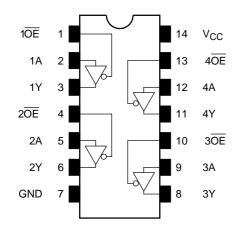
- Low-voltage operation: $V_{CC} = 2.0$ to 3.6 V
- High-speed operation: $t_{pd} = 6.0 \text{ ns} (max) (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Ouput current: $|I_{OH}|/I_{OL} = 24 \text{ mA} (\min) (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: ±500 mA
- Available in JEDEC SOP, JEITA SOP and TSSOP
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 125 type



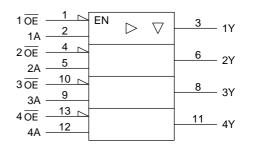
SOP14-P-300-1.27: 0.18 g (typ.) SOL14-P-150-1.27: 0.12 g (typ.) TSSOP14-P-0044-0.65: 0.06 g (typ.)

Note: xxxFN (JEDEC SOP) is not available in Japan.

Pin Assignment (top view)



IEC Logic Symbol



Truth Table

Inp	uts	Outputs
OE	А	Y
Н	Х	Z
L	L	L
L	Н	Н

X: Don't care

Z: High impedance

Maximum Ratings

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
		-0.5 to 7.0 (Note 1)	
DC output voltage	VOUT	-0.5 to $V_{CC} + 0.5$	V
		(Note 2)	
Input diode current	I _{IK}	-50	mA
Output diode current	I _{OK}	±50 (Note 3)	mA
DC output current	IOUT	±50	mA
Power dissipation	PD	180	mW
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65 to 150	

Note 1: Output in OFF state

Note 2: High or low state. IOUT absolute maximum rating must be observed.

Note 3: $V_{OUT} < GND, V_{OUT} > V_{CC}$

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit		
Power supply voltage	Vee	2.0 to 3.6	V		
Fower supply voltage	Vcc	1.5 to 3.6 (Note 4)			
Input voltage	V _{IN}	0 to 5.5	V		
Output voltage		0 to 5.5 (Note 5)	V		
Output voltage	Vout	0 to V _{CC} (Note 6)	v		
Output current	IOH/IOI	±24 (Note 7)	mA		
Output current	IOH/IOL	±12 (Note 8)	ША		
Operating temperature	T _{opr}	-40 to 85	°C		
Input rise and fall time	dt/dv	0 to 10 (Note 9)	ns/V		

Note 4: Data retention only

Note 5: Output in OFF state

Note 6: High or low state

Note 7: $V_{CC} = 3.0$ to 3.6 V

Note 8: $V_{CC} = 2.7$ to 3.0 V

Note 9: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Character	istics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit	
	H-level	Vih			2.7 to 3.6	2.0			
Input voltage	L-level	VIL			2.7 to 3.6		0.8	V	
				I _{OH} = -100 μA	2.7 to 3.6	V _{CC} - 0.2			
	H-level	V _{ОН}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -12 mA	2.7	2.2			
		0.1		I _{OH} = -18 mA	3.0	2.4			
Output voltage				I _{OH} = -24 mA	3.0	2.2		V	
				I _{OL} = 100 μA	2.7 to 3.6	_	0.2		
	L-level	Voi	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 12 mA	2.7		0.4		
	L-level	VOL	VIN = VIH OI VIL	VIN = VIH OI VIL	I _{OL} = 16 mA	3.0		0.4	
				I _{OL} = 24 mA	3.0		0.55		
Input leakage curre	ent	I _{IN}	V _{IN} = 0 to 5.5 V		2.7 to 3.6		±5.0	μA	
	atata aurrant	1	$V_{IN} = V_{IH} \text{ or } V_{IL}$		2.7 to 3.6		15.0		
3-State Output OFF	output OFF state current I _{OZ}		V _{OUT} = 0 to 5.5 V		2.7 10 3.6	_	±5.0	μA	
Power-off leakage	current	I _{OFF}	$V_{IN}/V_{OUT} = 5.5 V$		0		10.0	μA	
Quiescent supply current		laa	V _{IN} = V _{CC} or GND		2.7 to 3.6	_	10.0		
	unent	ICC	$V_{IN}/V_{OUT} = 3.6 \text{ to } 5.5 \text{ V}$		2.7 to 3.6		±10.0	μA	
Increase in Icc per	input	ΔI_{CC}	$V_{IH} = V_{CC} - 0.6 \text{ V}$		2.7 to 3.6		500		

AC Characteristics (Ta = -40 to 85°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{pLH}	Figure 1, Figure 2	2.7		6.5	ns
r topagation delay time	t _{pHL}		$\textbf{3.3}\pm\textbf{0.3}$	1.5	6.0	
	t _{pZL}	2.7		8.0		
Output enable time	t _{PZH}	Figure 1, Figure 3	$\textbf{3.3}\pm\textbf{0.3}$	1.5	7.0	ns
	t _{pLZ}		2.7	_	7.0	
Output disable time	t _{pHZ}	Figure 1, Figure 3	$\textbf{3.3}\pm\textbf{0.3}$	1.5	6.0	ns
Output to output skew	t _{osLH}	(Note 10)	2.7			ns
t _{osHL}		$\textbf{3.3}\pm\textbf{0.3}$	_	1.0	115	

Note 10: Parameter guaranteed by design.

 $(t_{osLH} = |t_{pLHm} - t_{pLHn}|, t_{osHL} = |t_{pHLm} - t_{pHLn}|)$

Dynamic Switching Characteristics ($T_2 = 25^{\circ}C$ input: $t = t_2 = 25$ ps. $C_1 = 50$ pF. B.

(Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500 \Omega$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	[V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}	—		3.3	7	pF
Output capacitance	C _{OUT}			3.3	8	pF
Power dissipation capacitance	C _{PD}	$f_{IN} = 10 \text{ MHz}$ ((Note 11)	3.3	25	pF

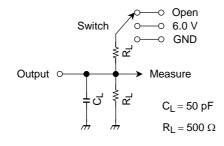
Note 11: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4$ (per gate)

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AC Test Circuit



Parameter	Switch
t _{pLH} , t _{pHL}	Open
t _{pLZ} , t _{pZL}	6.0 V
t _{pHZ} , t _{pZH}	GND



AC Waveform

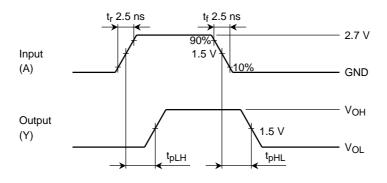
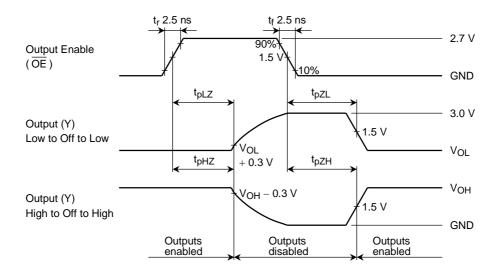
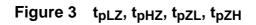
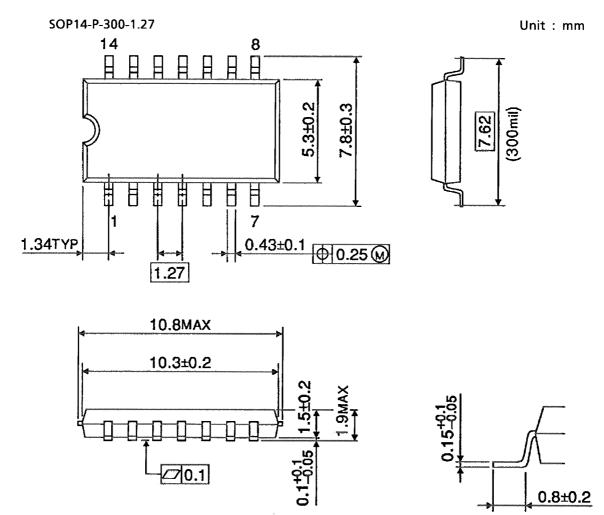


Figure 2 t_{pLH}, t_{pHL}



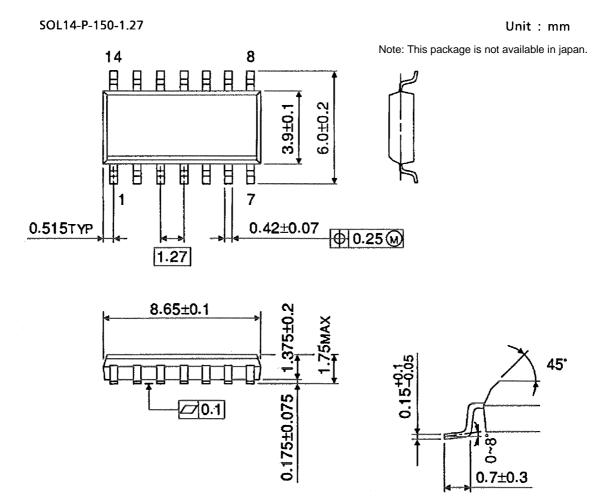


Package Dimensions



Weight: 0.18 g (typ.)

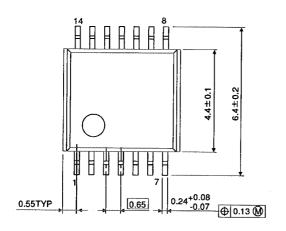
Package Dimensions



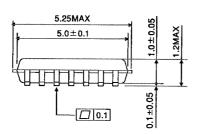
Weight: 0.12 g (typ.)

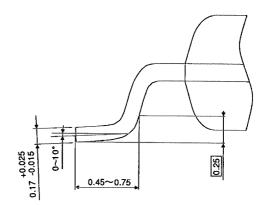
Package Dimensions

TSSOP14-P-0044-0.65









Weight: 0.06 g (typ.)

Unit : mm

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Handbook" etc..

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