TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TA8081P

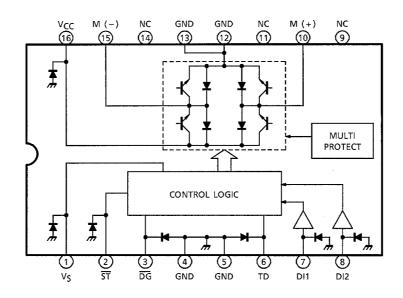
0.7A MOTOR DRIVER WITH DIAGNOSIS

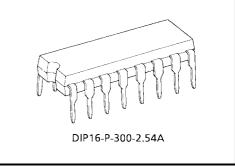
The TA8081P is a 0.7A motor driver which directly drivers a bidirectional DC motor. Inputs DI1 and DI2 are combined to select one of forward, reverse, stop, and brake modes. Since the inputs are TTL-compatible, this IC can be controlled directly from a CPU or other control system. The IC also has various protective, self-diagnostic, and standby functions.

FEATURES

- Bidirectional DC motor driver.
- Current capacity: 0.7A (max)
- Low standby current: 100µA (max)
- Four operation modes: Forward, reverse, stop, and brake.
- Recommended operating supply voltage range: $V_{CC} = 6 V$ to 16 V
- Protective functions: Thermal shutdown, short-circuit protection, and over-voltage shutdown.
- Built-in counter electromotive force absorption diodes.
- Self-diagnostic output: On short-circuit detection.
- Plastic DIP-16pin.

BLOCK DIAGRAM AND PIN LAYOUT





Weight: 1.0 g (typ.)

PIN DESCRIPTION

PIN No.	SYMBOL	DESCRIPTION			
1	VS	Control power supply pin. This pin is completely isolated from V_{CC} .			
2	ST	When this pin is opened or grounded, the output turns off, thus reducing the current consumption to 100 μ A or less. If standby mode is not needed, the pin is connected to V _{CC} .			
3	DG	Self-diagnostic output pin. When the output current becomes 1.5A or more, this pin outputs a protection switching waveform similar to the waveform which would be supplied from M (+) / M (-) pin. If a capacitor is connected to the TD pin, the signal from this pin will become low after a specific delay. The output from the pin is an open-collector output. The delay time is calculated approximately by the following : TD = $50 \times C_T$ (ms) C_T (µF)The permissible range of C_T is from 0.01μ F to 2μ F.			
6	TD	Provides a delay for the \overline{DG} output. A capacitor is connected between this pin and GND. When the pin is opened, the \overline{DG} pin supplies a switching output.			
78	DI1 DI2	Output status control pin.Connects to a PNP-type voltage comparator.			
10	M (+)	Connects to the DC motor. Both the sink and the source have a current capacity of 0.7A. Diodes for absorbing counter electromotive force are contained on the V_{CC} and GND sides.			
15	M (-)	Connects to the DC motor together with pin 10 and has the same function as pin 10. This pin is controlled by the inputs from pins 7 and 8.			
16	V _{CC}	Output section power supply pin. This pin has a function to turn off the output when the applied voltage exceeds 30.0V, thus protecting the IC and the load.			
4, 5, 12, 13	GND	Grounded.			
9, 11, 14	NC	Not connected. (Electrically, this pin is completely open.)			

TRUTH TABLE

INPUT			OUT	PUT	OUTPUT MODE	
DI1	DI2	ST	M (+)	M (-)	OUT OT MODE	
н	Н	Н	L	L	Brake	
L	Н	Н	L	Н	Reverse (CCW)	
Н	L	Н	Н	L	Forward (CW)	
L	L	Н	OFF (high impedance)		Stop	
H/L	H/L	L	OFF (high impedance)		Standby (stop)	

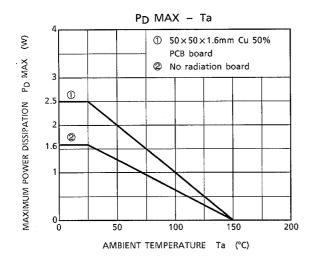
MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Power Supply Voltage	V _{CC}	33	V	
Power Supply Voltage	V _{CC}	60 (1s)		
Input Voltage	V _{IN}	-0.3~V _{CC}	V	
Output Current	I _{O AVE}	0.7	А	
Power Dissipation	PD	1.6	W	
Operating Temperature	T _{opr}	-40~110	°C	
Storage Temperature	T _{stg}	-55~150	°C	
Lead Temperature-time	T _{sol}	260 (10s)	°C	

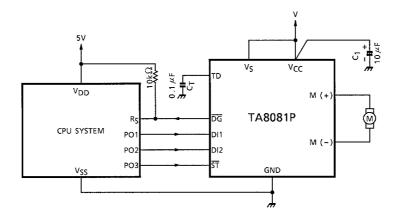
ELECTRICAL CHARACTERISTICS (V_{CC} = 6~16V, Ta = -40~110°C)

CHARACTERISTIC	SYMBOL	PIN	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT	
	I _{S1}	Vs	-	Stop	-	5	10	mA	
Current Consumption 1	I _{S2}		_	Forward / Reverse	-	10	20		
	I _{S3}		_	Brake	-	10	20		
	I _{CC1}	V _{CC}		Stop	_	1.0	2.0	mA	
Current Consumption 2	I _{CC2}			Forward / Reverse	-	15	30		
	I _{CC3}		_	Brake	-	1.0	2.0		
	V _{IL}	DI1 / DI2	-	—	-	-	0.8	v	
Input) (altaga	VIH			—	2.0	_	_		
Input Voltage	V _{IL}	ST	_	—	_	_	0.5		
	V _{IH}		_	—	2.0	_	_		
	١ _{١L}	DI1 / DI2	_	V _{IN} = 0.4V	-10	_	10	μA	
lanut Current	IIH			V _{IN} = V _{CC}	-10	_	10		
Input Current	١ _{١L}	ST		V _{IN} = 0.4V	_	_	20		
	IIH		_	V _{IN} = VCC	_	_	2.0	mA	
Output Saturation Voltage		_{sat} (total) M (+) / M (−)	_	I _O = 0.5A, Tc = 25°C	_	1.7	2.4	V	
Output Saturation voltage	v _{sat} (ioiai)		_	I _O = 0.5A, Tc = 110°C	-	1.6	2.3		
Output Leakage Current	I _{LEAK} .U	- M (+) / M (-)	_	V _{OUT} = 0V	_	_	-10	μA	
Output Leakage Current	ILEAK·L		_	$V_{OUT} = V_{CC}$	-	_	10		
Output Voltage	V _{OL}	DG		I _{OL} = 3mA	_	_	0.5	V	
Output Leakage Current	I _{LEAK}	DG	_	V _{OUT} = V _{CC}	-	-	10	μA	
	V _{F·U}	— M (+) / M (–)	_	I _F = 0.5A	_	1.5	_	v	
Diode Forward Voltage	V _{F·L}				_	1.5	_		
Over-current Detection	I _{SD}	_	_	_	1.0	1.5	2.0	Α	
Shutdown Temperature	T _{SD}	_	—	—	—	150	—	°C	
Over-voltage Detection	V _{SD}	—	—	—	27	30	33	V	
Standby Current	۱ _S	V _{CC} +V _S	—	ST = 0V	—	—	100	μA	
Transfer Dalay Time	t _{pLH}	—	—	_	—	1	10	μs	
Transfer Delay Time	t _{pHL}	_	_	_	_	1	10		

Note: The parameter values above are guaranteed in the operating voltage range of 6V to 16V. If the guaranteed range is exceeded in practical use, make sure that the IC operates normally in application.



EXAMPLE OF APPLICATION CIRCUIT

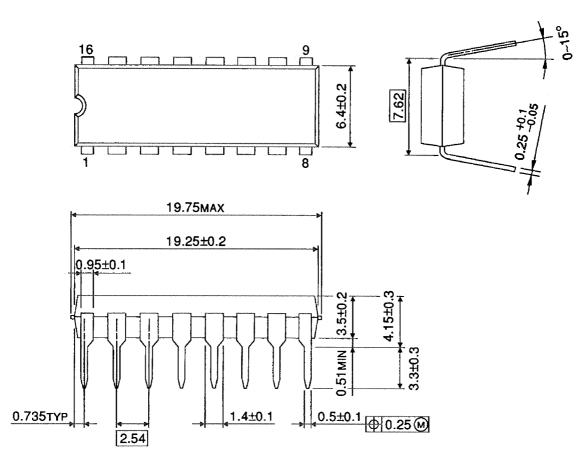


Cautions for Wiring : C1 is for absorbing disturbance, noise, etc. Connect is as close to the IC as possible.

Package Dimensions

DIP16-P-300-2.54A

Unit : mm



Weight: 1.0 g (Typ.)

RESTRICTIONS ON PRODUCT USE

Handbook" etc..

000707EAA_S

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
 In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.