TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA7376P

Audio Power Amplifier

The TA7376P is dual audio power amplifier for portable products.

Features

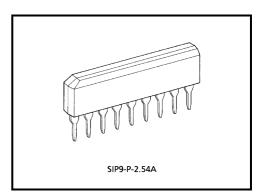
- Low operating supply voltage: $V_{CC} = 1.8 \sim 6V \text{ (Ta} = 25 \text{°C)}$
- Low quiescent current: ICCQ = 5.3mA

$$(V_{CC} = 4.5V)$$

• Including ripple filter circuit: RR = −42dB

$$(C_{RIP} = 10 \mu F, f_r = 100 Hz)$$

- Voltage gain: Gy = 39.5dB (typ.)
- Very few external parts and small package. (SIP-9pin)

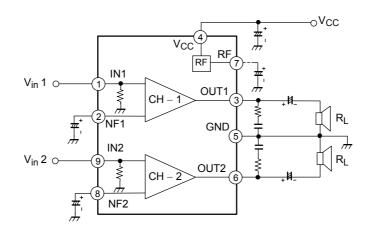


Weight: 0.92g (typ.)

Output Power Table (THD = 10%, f = 1kHz, Stereo, Typ. value)

V _{CC} Load	R _L = 32Ω	R _L = 16Ω	R _L = 8Ω	$R_L = 4\Omega$
3V	21mW	38mW	65mW	100mW
4.5V	56mW	100mW	180mW	300mW
6V	120mW	230mW	400mW	_

Block Diagram



Application Note

1. Input stage

The input stage of power amplifier (equivalent circuit) is comprised of a PNP differential pair (Q_2 and Q_3) preceded by a PNP emitter follower (Q_1) which allows DC referencing of the source signal to ground.

This eliminates the need for an input coupling condenser.

However, in case the brush noise of volume becomes a problem, provide serially a coupling condenser to the input side.

2. Adjustment of voltage gain

The voltage gain is fixed at GV = 40 dB by the resistors (R₁ and R₂) in IC, however, its reduction is possible through adding R_f as shown in Fig.2. In this case, the voltage gain is obtained by the following equation.

$$G_{V} = 20 \lambda og \frac{R_1 + R_2 + R_f}{R_1 + R_f}$$

It is recommended to use this IC with the voltage gain of $GV \stackrel{.}{=} 30 dB$ or over.

3. Ripple rejection ratio (RR)

If the TA7376P does not have the ripple filter condenser (CRIP), the ripple rejection ratio is as follow.

$$\begin{split} RR &= -25 dB \; (typ.) \\ &(CNF = 22 \mu F, \, f_{\mathbf{r}} = 100 Hz) \\ RR &= -34 dB \; (typ.) \\ &(CNF = 100 \mu F, \, f_{\mathbf{r}} = 100 Hz) \end{split}$$

If the ripple filter condenser is connected to the pin(7), the ripple rejection ratio is improved as following the DATA (RR- f_r).

4. Pop sound

It must be connected the condenser (CRIP) from pin(7) to GND, if the "Pop" sound is harshness.

In this case, the value is 10µF something.

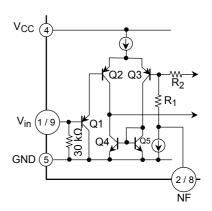


Fig.1

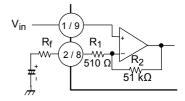


Fig.2

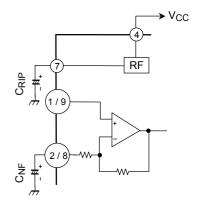


Fig.3

[Unit: V]



5. Phase-compensation

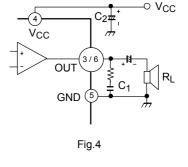
The purpose of condenser C₁ is to prevent oscillation.

These condenser need to be small temperature coefficient and excellent frequency characteristic. So ceramic condenser is unsuitable.

Condenser C_2 is rather large value than $10\mu F$ and GND line is better to short and wide lay–out so that the some common impedance are decreased.

Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Supply voltage	V _{CC}	8	V	
Power dissipation	P _D (Note)	950	mW	
Operation temperature	T _{opr}	-25~75	°C	
Storage temperature	T _{stg}	-55~150	°C	



(Note) Derated above Ta = 25° C in the proportion of 7.6mW / $^{\circ}$ C.

Electrical Characteristics

(unless otherwise specified, V_{CC} = 4.5V, f = 1kHz, R_g = 600 Ω , R_L = 4 Ω , Ta = 25°C)

Characteristic	Symbol	Test Cir– cuit	Test Condition	Min.	Тур.	Max.	Unit	
Quiescent current	Iccq	_	V _{in} = 0, V _{CC} = 3V	_	4.9	8.0	mA	
			V _{in} = 0	_	5.3	10.0		
			V _{in} = 0, V _{CC} = 6V	_	5.7	14.0		
Output power	P _{out}	_	V_{CC} = 3V, R_L = 4 Ω , THD = 10%	84	100	_	mW	
			V_{CC} = 3V, R_L = 32 Ω , THD = 10%	_	21	_		
			V_{CC} = 4.5V, R_L = 4 Ω , THD = 10%	250	300	_		
			V_{CC} = 4.5V, R_L = 8 Ω , THD = 10%	_	180	_		
			V _{CC} = 6V, R _L = 8Ω, THD = 10%	_	400	_		
Total harmonic distortion	THD	_	P _{out} = 100mW	_	0.11	1.0	%	
Voltage gain	G _V	_	V _{out} = 0.775V _{rms}	37.5	39.5	41.5	dB	
Output noise voltage	V _{no}	_	$R_g = 10\Omega$, BPF = 20Hz~20kHz	_	0.21	0.7	mV _{rms}	
Ripple rejection ratio	RR	_	C_{RIP} = 10 μ F, C_{NF} = 22 μ F f_r = 100Hz, V_r = 0.38 V_{rms}	_	-42	-30	- dB	
			C_{RIP} = OPEN, C_{NF} = 100 μ F f_r = 100Hz, V_r = 0.38 V_{rms}	_	-34	_		
Cross talk	СТ	_	V _{out} = 0.775 V _{rms}	_	-60	-40	dB	
Input resistance	R _{IN}	_	_	_	30	_	kΩ	

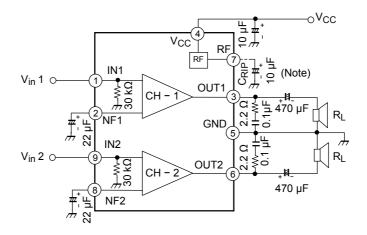
Quiescent Terminal DC Voltage (V_{CC} = 4.5V, Ta = 25°C, typ. value)

Terminal 3 5 7 8 9 0.003 0.003 Voltage (V) 0.59 1.98 4.5 0 1.98 1.28 0.59

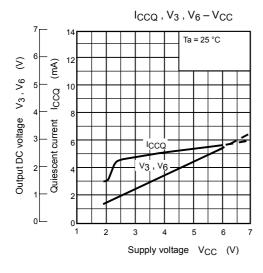
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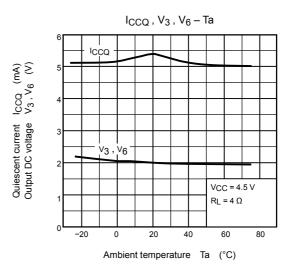
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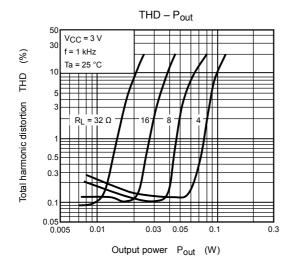
Test circuit

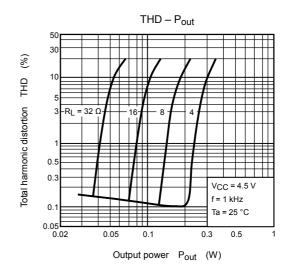


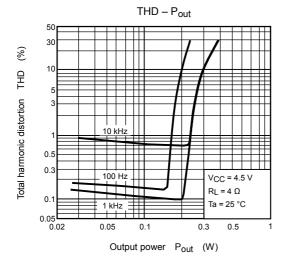
(Note) C_{RIP} is shown in item 3 and 4 of Application Note.

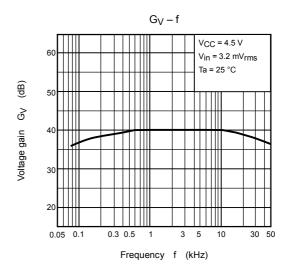


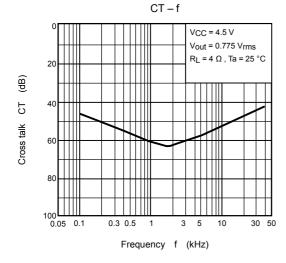


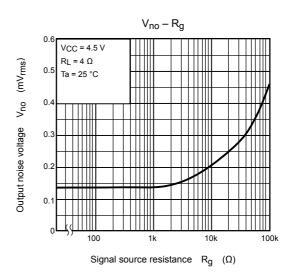


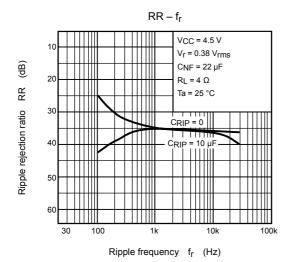


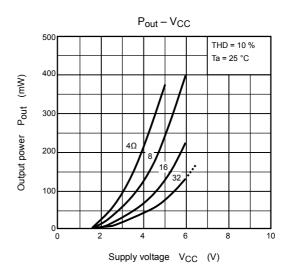


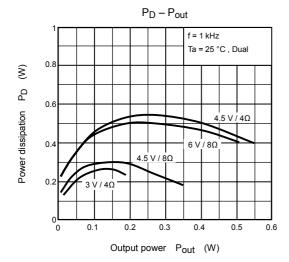


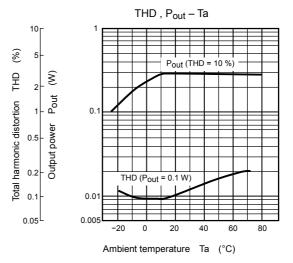


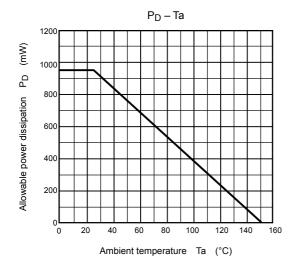








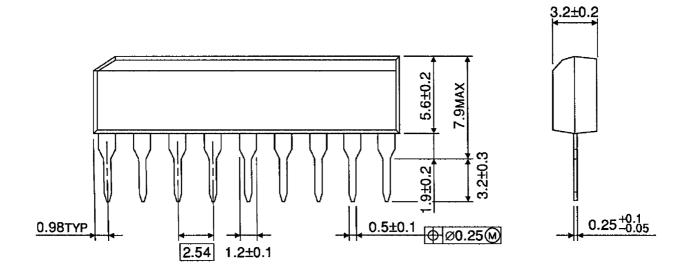


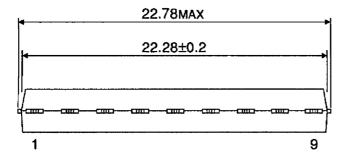


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Package Dimensions

SIP9-P-2.54A Unit: mm





Weight: 0.92g (typ.)

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