## RC4558, RM4558 DUAL GENERAL-PURPOSE OPERATIONAL AMPLIFIERS

SLOS073A - MARCH 1976 - REVISED JUNE 1999

- Continuous-Short-Circuit Protection
- Wide Common-Mode and Differential Voltage Ranges
- No Frequency Compensation Required
- Low Power Consumption
- No Latch-Up
- Unity-Gain Bandwidth . . . 3 MHz Typ
- Gain and Phase Match Between Amplifiers
- Low Noise ... 8 nV $\sqrt{Hz}$  Typ at 1 kHz
- Designed To Be Interchangeable With Raytheon RC4558 and RM4558 Devices

#### description

The RC4558 and RM4558 devices are dual general-purpose operational amplifiers with each half electrically similar to the  $\mu$ A741 except that offset null capability is not provided.

The high common-mode input voltage range and the absence of latch-up make these amplifiers ideal for voltage-follower applications. The devices are short-circuit protected and the internal frequency compensation ensures stability without external components.

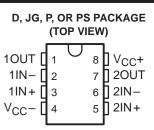
The RC4558 is characterized for operation from  $0^{\circ}$ C to  $70^{\circ}$ C, and the RM4558 is characterized for operation over the full military temperature range of  $-55^{\circ}$ C to  $125^{\circ}$ C.

	V <sub>IO</sub> MAX AT 25°C	PACKAGED DEVICES							
TA		SMALL OUTLINE (D)	SSOP (DBR)	CERAMIC DIP (JG)	PLASTIC DIP (P)	SOP (PSR)			
0°C to 70°C	6 mV	RC4558D	RC4558DBR	_	RC4558P	RC4558PSR			
−55 °C to 125°C	6 mV	_	_	RM4558JG	_	—			

#### AVAILABLE OPTIONS

The D package is available taped and reeled. Add the suffix R to the device type (e.g., RC4558DR).

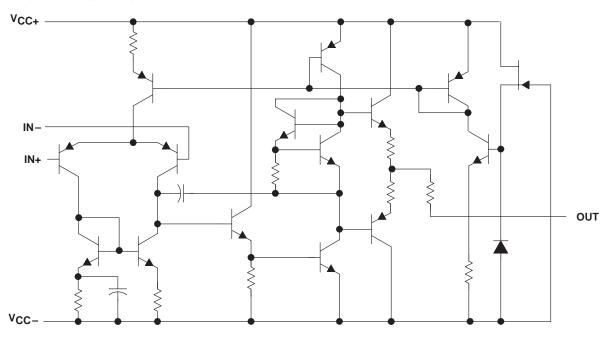




# RC4558, RM4558 DUAL GENERAL-PURPOSE OPERATIONAL AMPLIFIERS

SLOS073A - MARCH 1976 - REVISED JUNE 1999

### schematic (each amplifier)



### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

		RC4558	RC4558 RM4558		
Supply veltage (eee Nete 1)	V <sub>CC+</sub>	18	22	V	
Supply voltage (see Note 1)	V <sub>CC</sub> -	-18	-22		
Differential input voltage (see Note 2)		±30	±30	V	
Input voltage (any input, see Notes 1 and 3)		±15	±15	V	
Duration of output short circuit to ground, one amplifier at a time (see Note 4)		unlimited			
	D package	19	97		
Package thermal impedance, $\theta_{JA}$ (see Note 5)	P package	104		°C/W	
	PS package	16	63		
Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds: JG package			°C		
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds: D or P package		260		°C	
Storage temperature range, T <sub>Stg</sub>		-65 to 150	-65 to 150	°C	

NOTES: 1. All voltage values, unless otherwise noted, are with respect to the midpoint between V<sub>CC+</sub> and V<sub>CC-</sub>.

2. Differential voltages are at IN+ with respect to IN-.

3. The magnitude of the input voltage must never exceed the magnitude of the supply voltage or 15 V, whichever is less.

4. Temperature and/or supply voltages must be limited to ensure that the dissipation rating is not exceeded.

5. The package thermal impedance is calculated in accordance with JESD 51, except for through-hole packages, which use a trace length of zero.

### recommended operating conditions

	MIN	MAX	UNIT	
Supply voltage	V <sub>CC+</sub>	5	15	v
Supply voltage	V <sub>CC</sub> -	-5	-15	
Operating free air temperature Te	RC4558	0	70	°C
Operating free-air temperature, T <sub>A</sub>	RM4558	-55	125	C



# RC4558, RM4558 DUAL GENERAL-PURPOSE OPERATIONAL AMPLIFIERS

SLOS073A - MARCH 1976 - REVISED JUNE 1999

	DADAMETED				RC4558			RM4558			
	PARAMETER	TEST CONDITIONS <sup>†</sup>		MIN TY	TYP	MAX	MIN	TYP	MAX	UNIT	
				25°C		0.5	6		0.5	5	
VIO	Input offset voltage		V <sub>O</sub> = 0	Full range			7.5			6	mV
				25°C		5	200		5	200	
IIO	Input offset current		V <sub>O</sub> = 0	Full range			300			500	nA
				25°C		150	500		140	500	
IB	B Input bias current		V <sub>O</sub> = 0	Full range			800			1500	nA
VICR	Common-mode input voltage range	е		25°C	±12	±14		±12	±14		V
		$R_L = 10 \ k\Omega$	25°C	±12	±14		±12	±14		V	
VOM	Maximum output voltage swing	$R_L = 2 k\Omega$	25°C	±10	±13		±10	±13			
vОм	Maximum output voltage swing		$R_L \ge 2 \ k\Omega$	Full range	±10			±10			
				25°C	20	300		50	350		
AVD	Large-signal differential voltage amplification		$R_L \ge 2 k\Omega,$ $V_O = \pm 10 V$	Full range	15			25			V/mV
B <sub>1</sub>	Unity-gain bandwith			25°C		3		2	3.5		MHz
ri	Input resistance			25°C	0.3	5		0.3	5		MΩ
CMRR	Common-mode rejection ratio			25°C	70	90		70	90		dB
k <sub>SVS</sub>	Supply-voltage sensitivity ( $\Delta V_{IO}/\Delta$	V <sub>CC</sub> )	$V_{CC} = \pm 15 V$ to $\pm 9 V$	25°C		30	150		30	150	μV/V
V <sub>n</sub>	Equivalent input noise voltage (clo	sed loop)	$A_{VD} = 100, R_S = 100 \Omega, f = 1 \text{ kHz}, BW = 1 \text{ Hz}$	25°C		8			8		nV√Hz
			V <sub>O</sub> = 0, No load	25°C		2.5	5.6		2.5	5.6	
ICC	Supply current (both amplifiers)	T <sub>A(min)</sub>			3	6.6		3	6.6	mA	
		T <sub>A(max)</sub>			2.3	5		2	5		
	Total newer dissinction			25°C		75	170		75	170	
PD	Total power dissipation (both amplifiers)		V <sub>O</sub> = 0, No load	T <sub>A(min)</sub>		90	200		90	200	mW
				T <sub>A(max)</sub>		70	150		60	150	
V01/V02	Crosstalk attenuation	pen loop	$R_S = 1 k\Omega$ ,	25°C		85			85		dB
· 01/ • 02	Avg = 100		f = 10 kHz	200		105			105		

### electrical characteristics at specified free-air temperature, $V_{CC+} = 15 V$ , $V_{CC-} = -15 V$

<sup>†</sup> All characteristics are measured under open-loop conditions with zero common-mode input voltage unless otherwise specified. Full range is  $0^{\circ}$ C to  $70^{\circ}$ C for RC4558 and  $-55^{\circ}$ C to  $125^{\circ}$ C for RM4558. T<sub>A(min)</sub> is  $0^{\circ}$ C for RC4558 and  $-55^{\circ}$ C for RM4558. T<sub>A(max)</sub> is  $70^{\circ}$ C for RC4558 and  $125^{\circ}$ C for RM4558. T<sub>A(max)</sub> is  $70^{\circ}$ C for RC4558 and  $125^{\circ}$ C for RM4558.

### operating characteristics, V<sub>CC+</sub> = 15 V, V<sub>CC-</sub> = -15 V, T<sub>A</sub> = $25^{\circ}$ C

	PARAMETER		MIN	TYP	MAX	UNIT		
tr	Rise time	Vı = 20 mV.				0.13		ns
	Overshoot	v] = 20 mv,	$R_L = 2 k\Omega$ ,	2 kΩ, CL = 100 pF		5%		
SR	Slew rate at unity gain	V <sub>I</sub> = 10 V,	RL = 2 kΩ,	CL = 100 pF	1.1	1.7		V/µs



#### **IMPORTANT NOTICE**

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.

Copyright © 1999, Texas Instruments Incorporated