# POWER INTEGRATED CIRCUIT <br> Switching Regulator 5 Amp Positive and Negative Power Output Stages 

## FEATURES

- Designed and characterized for switching regulator applications
- Cost saving design reduces size, improves efficiency, reduces noise and RFI
- High operating frequency (to $>100 \mathrm{kHz}$ ) results in smaller inductor-capacitor filter and improved power supply response time
- High operating efficiency: Typical 2A circuit performance -

Rise and Fall time <75ns
Efficiency $>85 \%$

- No reverse recovery spike generated by commutating diode (See note
- Electrically isolated, 4-Pin, TO-66 hermetic case


## DESCRIPTION

ESP Switching Regulator is a unique hybrid transistor circuit, specifically designed, constructed and specified for use in high current switching regulator applications. The designer is thus relieved of one of the most time consuming, tedious and critical aspects of switching regulator design: choosing the appropriate switching transistors and commutating diode, and empirically determining the optimum drive and bias conditions:

The PIC600 series switching regulators are designed and characterized to be driven with standard integrated circuit voltage regulators. They are completely characterized over their entire operating range of $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$. The devices are enclosed in a special 4-pin TO-66 package, hermetically sealed for high reliability. The hybrid circuit construction utilizes thick film resistors on a beryllia substrate for maximum thermal conductivity and resultant low thermal impedance. All of the active elements in the hybrid are fully passivated.

Application Notes U-68 and U-76 provide a detailed description of the hybrid circuit and design guidance for specific circuit applications.


MECHANICAL SPECIFICATIONS


NJ Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However, NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.

## absolute maximum ratings

|  | PICE00 | PIC601 | PIC602 | PIC610 | PICE11 | PICs12 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Input Voltage, $\mathrm{V}_{4.2}$ | 60 V | 80 V | 100 V . | $-60 \mathrm{~V}$ | -80V | -100V |
| Output Voltage, $\mathrm{V}_{1-2}$ | 60 V | 80 V | 100 V | -60V | -80V | -100V |
| Drive-Input Reverse Voltage, $\mathrm{V}_{3.4}$ | 5 V . | 5 V | 5 V | -5V | -5V | -5V |
| Output Current, I, ..................... | 15A | 5A. | 5A. | -5A | -5A | -5A |
| Drive Current, $I_{3} \ldots . . . . . . . . . . . . . .$. | -0.2A. | -0.2A | -0.2A.. | .. 0.2A | 0.2 A | .... 0.2A |
| Thermal Resistance |  |  |  |  |  |  |
| Junction to Case, $\theta_{\text {J.C }}$ |  |  |  |  |  |  |
| Power Switch |  |  |  | $4.0^{\circ} \mathrm{C} / \mathrm{W}$ |  |  |
| Commutating Diode |  |  |  | $4.0^{\circ} \mathrm{C} / \mathrm{W}$ |  |  |
| Case to Ambient, $\theta_{C_{-A}}$ |  |  |  | $60.0{ }^{\circ} \mathrm{C} / \mathrm{W}$ |  |  |
| Operating Temperature Range, $\mathrm{T}_{\mathrm{C}}$ |  |  |  | $5^{\circ} \mathrm{C}$ to + |  |  |
| Maximum Junction Temperature, |  |  |  | $+150^{\circ} \mathrm{C}$ |  |  |
| Storage Temperature Range |  |  |  | $5^{\circ} \mathrm{C}$ to + |  |  |

ELECTRICAL SPECIFICATIONS (at $25^{\circ} \mathrm{C}$ unless noted)

| Current Delay Time | $\mathrm{t}_{\text {di }}$ | - | 20 | 40 | - | 20 | 40 | ns | $\mathrm{V}_{1 \mathrm{n}}=25 \mathrm{~V}(-25 \mathrm{~V})$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Current Rise Time | $t_{\text {ri }}$ | - | 50 | $75^{\circ}$ | - | 50 | 75 | ns | $V_{\text {out }}=5 \mathrm{~V}(-5 \mathrm{~V})$ |
| Voltage Rise Time | $t_{\text {cv }}$ | - | 30 | 50 | - | 30 | 50 | ns | $\mathrm{I}_{\text {out }}=2 \mathrm{~A}(-2 \mathrm{~A})$ |
| Voltage Storage Time | $t_{s v}$ | - | 700 | - | - | 700 | - | ns | $\mathrm{I}_{3}=-20 \mathrm{~mA}(20 \mathrm{~mA})$ |
| Voltage Fall Time | $t_{\text {fv }}$ | - | 50 | 75 | - | 50 | 75 | ns | See Figure 2. |
| Current Fall Time | $t_{\text {fi }}$ | - | 70 | 150 | - | 70 | 150 | ns | See notes 1., 2., 4. |
| Efficiency (Notes 2. \& 4.) | $\eta$ | - | 85 | - | - | 85 | - | \% |  |
| On-State Voltage (Note 3.) | $V_{\text {4-1(on) }}$ | $\cdots$ | 1.0 | 1.5 | - | -1.0 | -1.5 | V | $\mathrm{I}_{4}=2 \mathrm{~A}(-2 \mathrm{~A}), \mathrm{I}_{3}=-.02 \mathrm{~A}(.02 \mathrm{~A})$ |
| On-State Voltage (Note 3.) | $V_{4+1(0)}$ | - | 2.5 | 3.5 | - | -2.5 | -3.5 | V | $\mathrm{I}_{4}=5 \mathrm{~A}(-5 \mathrm{~A}), \mathrm{I}_{3}=-.02 \mathrm{~A}(.02 \mathrm{~A})$ |
| Diode Forward Voltage (Note 3.) | $V_{\text {2-1(an) }}$ | - | . 8 | 1.0 | - | -. 8 | $-1.0$ | V | $\mathrm{I}_{3}=2 \mathrm{~A}(-2 \mathrm{~A})$ |
| Diode Forward Voltage (Note 3.) | $V_{2-1(0 n)}$ | - | 1.0 | 1.5 | - | -1.0 | -1.5 | V | $\mathrm{I}_{2}=5 \mathrm{~A}(-5 \mathrm{~A})$ |
| Off-State Current | $I_{4-1}$ | - | 0.1 | 10 | - | -0.1 | $-10$ | ${ }_{\mu} \mathrm{A}$ | $\mathrm{V}_{4}=$ Rated input voltage |
| Off-State Current | $\mathrm{I}_{+1}$ | - | 10 | - | - | -10 | - | $\mu \mathrm{A}$ | $V_{4}=$ Rated input voltage, $\mathrm{T}_{4}=100^{\circ} \mathrm{C}$ |
| Diode Reverse Current | $\mathrm{I}_{1.2}$ | - | 1.0 | 10 | - | - 1.0 | $-10$ | $\mu \mathrm{A}$ | $\mathrm{V}_{1}=$ Rated output voltage |
| Diode Reverse Current | $\mathrm{I}_{1-2}$ | - | 500 | - | - | - 500 | - | $\mu \mathrm{A}$ | $\mathrm{V}_{1}=$ Rated output voltage, $\mathrm{T}_{\mathrm{A}}=100^{\circ} \mathrm{C}$. |

Notes:

1. In switching an inductive load, the current will lead the voltage on turn on and lag the voltage on turn-off (see Figura 2 ). Therefore, Voltage Delay Time $\left(t_{\text {ov }}\right) \cong t_{d i}+t_{t i}$ and Current Storage Time $\left(t_{1}\right) \cong t_{1 v}+t_{t,}$,
2. The efficiency is a measure of internal power tosses and is equal to Output Power divided by Input Power. The switching speed circuit of Figure 1, in which the efficiency is measured, is representative of typical operating conditions for the PIC600 series switching regulators.
3. Pulse test: Duration $=300 \mathrm{~ms}$, Duty Cycle $\leqslant 2 \%$.
4. As can be seen from the switching waveforms shown in Figure 2, no reverse or forward recovery spike is generated by the commutating diade during switching! This reduces self-generated noise, since no current spike is fed through the switching regulator. It also improves efficiency and reliability, since the power switch only carries current during turn-on.
