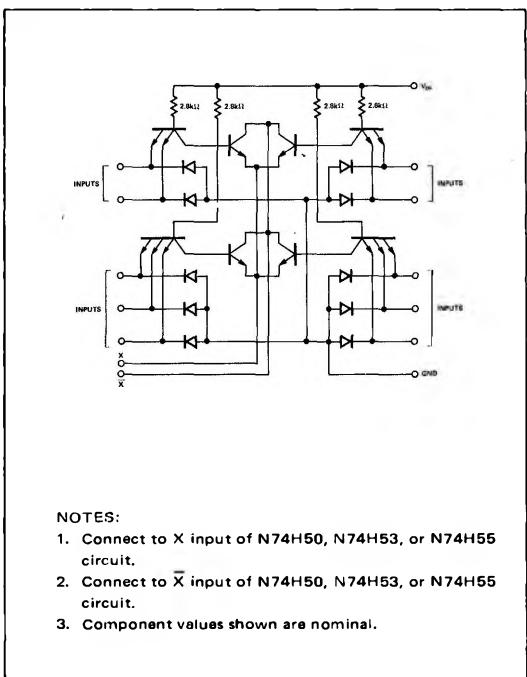


# Signetics 3-2-2-3-INPUT AND-OR EXPANDER (FOR USE WITH N74H50, N74H53, N74H55 CIRCUITS) | N74H62

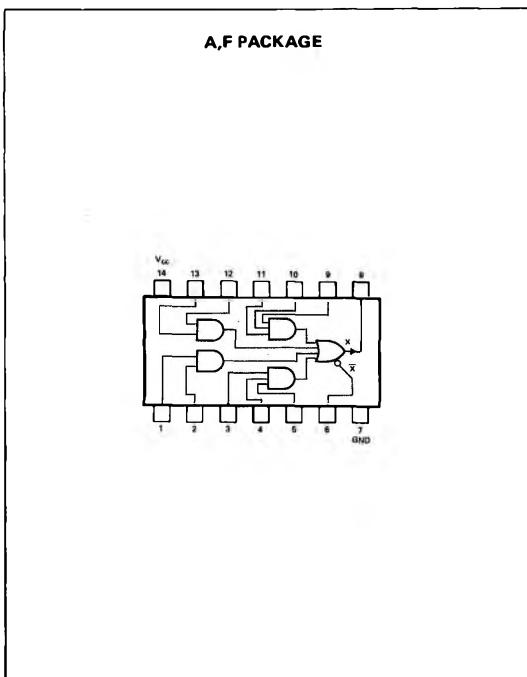
N74H62-A,F

DIGITAL 54/74 TTL SERIES

## SCHEMATIC (each gate)



## PIN CONFIGURATIONS



## RECOMMENDED OPERATING CONDITIONS

Supply Voltage $V_{CC}$	4.75V to 5.25V
Maximum number of expanders that may be fanned-in to one N74H50, N74H53, or N74H55 circuit	1

## ELECTRICAL CHARACTERISTICS (unless otherwise noted $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ )

PARAMETER	TEST CONDITIONS	MIN	TYP <sup>†</sup>	MAX	UNIT
$V_{in(1)}$	$V_{CC} = 4.75\text{V}$		2		V
$V_{in(0)}$	$V_{CC} = 4.75\text{V}$		0.8		V
$V_{on}$	$V_{CC} = 4.75\text{V}, I_{on} = 6.3\text{mA}, V_{CC} = 5.25\text{V}, I_{on} = 7.4\text{mA},$	$V_{in} = 2\text{V}, T_A = 0^\circ\text{C}$	$V_1 = 1\text{V}, V_1 = 0.8\text{V}, T_A = 70^\circ\text{C}$	0.4	V
$I_{off}$	$V_{CC} = 4.75\text{V}, R = 675\Omega,$	$V_{in} = 0.8\text{V}, T_A = 0^\circ\text{C}$	$V_1 = 4.5\text{V},$	570	$\mu\text{A}$
$I_{on}$	$V_{CC} = 4.75\text{V}, T_A = 0^\circ\text{C}$	$V_{in} = 2\text{V},$	$V_1 = 1\text{V}, -600$		$\mu\text{A}$

## ELECTRICAL CHARACTERISTICS (Cont'd)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
$I_{in(0)}$	Logical 0 level input current (each input) $V_{CC} = 5.25V, V_{in} = 0.4V$			-2	mA
$I_{in(1)}$	Logical 1 level input current (each input) $V_{CC} = 5.25V, V_{in} = 2.4V$ $V_{CC} = 5.25V, V_{in} = 5.5V$			50	$\mu A$
$I_{CC(on)}$	On-state supply current $V_{CC} = 5.25V, V_{in} = 4.5V, V_1 = 0.85V$		3.8	7	mA
$I_{CC(off)}$	Off-state supply current $V_{CC} = 5.25V, V_{in} = 0, V_1 = 0.85V$		6	9	mA

OUTPUT CAPACITANCE  $V_{CC}$  and GND terminals open,  $T_A = 25^\circ C$ 

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
$C_x$	Effective capacitance of output transistor $Q_1$ $f = 1 \text{ MHz}$		1.3		pF

<sup>†</sup>All typical values are at  $V_{CC} = 5V, T_A = 25^\circ C$ .