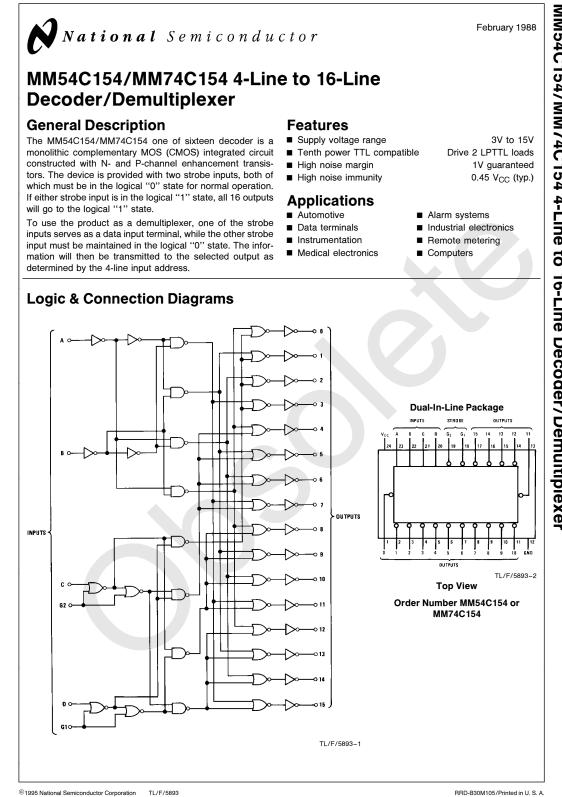
MM54C154,MM74C154

MM54C154 MM74C154 4-Line to 16-Line Decoder/Demultiplexer



Literature Number: SNOS320A



MM54C154/MM74C154 4-Line to 16-Line Decoder/Demultiplexer

please c	Aerospace specified devic ontact the National Semic stributors for availability and	onductor Sales	Storage Tempe Maximum V _{CC} ¹ Power Dissipat Dual-In-Line	Voltage		65°C to -	18V
•	Temperature Range)			700 mW 500 mW	
MM54C	154	-55°C to +125°C	Range			/ to 15V	
MM74C	154	-40°C to +85°C	Lead Temperat (Soldering, 10				260°C
	ectrical Characteris						
Symbol	Parameter	Conditior	IS	Min	Тур	Max	Units
MOS TO CI		N 5 0V		0.5			
V _{IN(1)}	Logical "1" Input Voltage	$V_{CC} = 5.0V$ $V_{CC} = 10V$		3.5 8.0			V V
V _{IN(0)}	Logical "0" Input Voltage	$V_{CC} = 5.0V$ $V_{CC} = 10V$				1.5 2.0	V V
V _{OUT(1)}	Logical "1" Output Voltage	$V_{CC} = 5.0V, I_0 = -1$ $V_{CC} = 10V, I_0 = -10$		4.5 9.0			v v
V _{OUT(0)}	Logical "0" Output Voltage	$V_{CC} = 5.0V, I_{O} = 10\mu$ $V_{CC} = 10V, I_{O} = 10\mu$	ιA			0.5 1.0	v v
I _{IN(1)}	Logical "1" Input Current	$V_{\rm CC} = 15V, V_{\rm IN} = 15$			0.005	1.0	μΑ
I _{IN(0)}	Logical "0" Input Current	$V_{CC} = 15V, V_{IN} = 0V$		-1.0	-0.005		μΑ
	Supply Current	$V_{CC} = 15V$			0.05	300	μΑ
		100 111					
V _{IN(1)}	Logical "1" Input Voltage	54C $V_{CC} = 4.5V$		V _{CC} - 1.5			V
		74C $V_{CC} = 4.75V$		V _{CC} – 1.5			V
V _{IN(0)}	Logical "0" Input Voltage	$\begin{array}{ccc} 54C & V_{CC} = 4.5V \\ 74C & V_{CC} = 4.75V \end{array}$				0.8 0.8	V V
V _{OUT(1)}	Logical "1" Output Voltage	54C $V_{CC} = 4.5V, I_{O}$ 74C $V_{CC} = 4.75V, I_{C}$		2.4 2.4			V V
V _{OUT(0)}	Logical "0" Output Voltage	54C $V_{CC} = 4.5V, I_O$ 74C $V_{CC} = 4.75V, I_O$	•			0.4 0.4	V V
UTPUT DR	IVE (See 54C/74C Family Cha			Current)			
ISOURCE	Output Source Current	$V_{CC} = 5.0V, V_{IN(0)} = T_A = 25^{\circ}C, V_{OUT} = 0$		-1.75			mA
ISOURCE	Output Source Current	$V_{CC} = 10V, V_{IN(0)} = 0$ $T_A = 25^{\circ}C, V_{OUT} = 0$	V	-8.0			mA
ISINK	Output Sink Current	$V_{CC} = 5.0V, V_{IN(1)} = T_A = 25^{\circ}C, V_{OUT} = V_A$	5.0V	1.75			mA
ISINK	Output Sink Current	$V_{CC} = 10V, V_{IN(1)} = T_A = 25^{\circ}C, V_{OUT} = V_A$	10V	8.0			mA
	olute Maximum Ratings" are those valu meant to imply that the devices should						

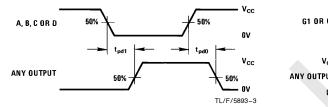
Symbol	Parameter	Conditions	Min	Тур	Мах	Units
t _{pd0}	Propagation Delay to a Logical "0" from Any Input to Any Output	$\begin{array}{l} V_{CC}=5.0V\\ V_{CC}=10V \end{array}$		275 100	400 200	ns ns
t _{pd0}	Propagation Delay to a Logical "0" from G1 or G2 to Any Output	$V_{CC} = 5.0V$ $V_{CC} = 10V$		275 100	400 200	ns ns
t _{pd0}	Propagation Delay to a Logical "0" from Any Input to Any Output	$V_{CC} = 5.0V$ $V_{CC} = 10V$		265 100	400 200	ns ns
t _{pd1}	Propagation Delay to a Logical "1" from G1 or G2 to Any Output	$V_{CC} = 5.0V$ $V_{CC} = 10V$		265 100	400 200	ns ns
C _{IN}	Input Capacitance	(Note 2)		5.0		pF
C _{PD}	Power Dissipation Capacitance	(Note 3)		60		pF

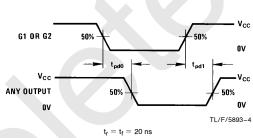
*AC Parameters are guaranteed by DC correlated testing.

Note 2: Capacitance is guaranteed by periodic testing.

Note 3: C_{PD} determines the no load AC power consumption of any CMOS device. For complete explanation see 54C/74C Family Characteristics Application Note AN-90.

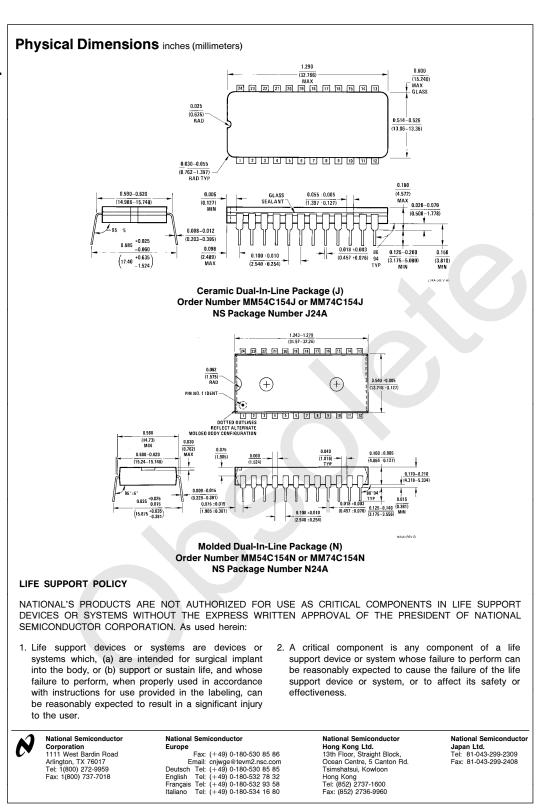
Switching Time Waveforms





Truth Table

		Inpu	ts								Outputs										
G1	G2	D	с	в	Α	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
L	L	L	L	L	L	L	н	н	н	Н	Н	Н	н	н	н	Н	Н	н	Н	Н	Н
L	L	L	L	L	Н	н	L	н	H	Н	н	н	н	н	Н	Н	н	н	Н	н	Н
L	L	L	L	Н	L	н	Н	L	Н	н	Н	н	Н	Н	Н	Н	н	н	Н	Н	Н
L	L	L	L	н	Н	н	Н	Н	L	H	н	Н	Н	Н	Н	Н	Н	н	Н	Н	Н
L	L	L	Н	L	L	н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
L	L	L	Н	L	н	н	Н	н	н	н	L	Н	Н	Н	Н	Н	Н	н	Н	Н	Н
L	L	L	Н	Н	L	Н	н	н	н	н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н
L	L	L	Н	Н	н	н	Н	Н	Н	н	Н	Н	L	Н	Н	Н	Н	н	Н	Н	Н
L	L	н	L	L	L	н	н	H	Н	н	Н	Н	Н	L	Н	Н	Н	н	Н	Н	Н
L	L	н	L	L	Н	н	Н	Н	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н
L	L	н	L	Н	L	н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н
L	L	н	L	Н	н	н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н
L	L	н	н	L	L	н	Н	Н	Н	н	Н	Н	Н	Н	Н	Н	Н	L	Н	Н	Н
L	L	н	Н	L	Н	н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	Н	Н
L	L	н	Н	Н	L	н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	Н
L	L	н	Н	Н	Н	н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L
L	Н	X	Х	Х	Х	H	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	H
Н	L	X	Х	Х	Х	H	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Н	Н	Х	Х	Х	Х	н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	н	Н	Н	Н



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