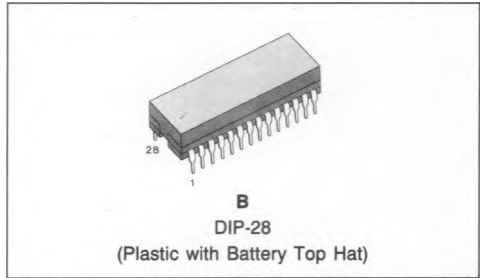


32K X 8 ZEROPOWER RAM

ADVANCED DATA

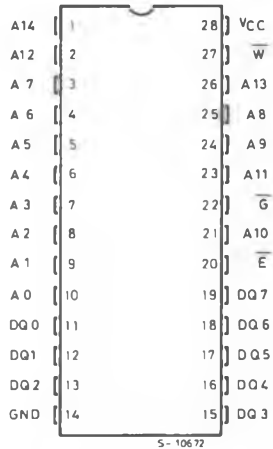
- DATA RETENTION IN THE ABSENCE OF V_{CC}
- DATA IS AUTOMATICALLY PROTECTED DURING POWER LOSS
- DIRECTLY REPLACES 32Kx8 VOLATILE STATIC RAM OR EEPROM
- UNLIMITED WRITE CYCLES
- CMOS - LOW POWER OPERATION
- STANDARD 28-PIN JEDEC PINOUT
- READ CYCLE TIME EQUALS WRITE CYCLE TIME
- FULL 10% OPERATING RANGE
- LITHIUM ENERGY SOURCE IS ELECTRICAL-
LY DISCONNECTED TO RETAIN FRESHNESS
UNTIL POWER IS APPLIED THE FIRST TIME



TRUTH TABLE MK48Z30

V_{CC}	\bar{E}	\bar{G}	\bar{W}	MODE	DQ	POWER
$< V_{CC}$ (max)	V_{IH}	X	X	Deselect	High-Z	Standby
	V_{IL}	X	V_{IL}	Write	D_{IN}	Active
V_{CC} (min)	V_{IL}	V_{IL}	V_{IH}	Read	D_{OUT}	Active
	V_{IL}	V_{IH}	V_{IH}	Read	High-Z	Active
$< V_{PFD}$ (min)	X	X	X	Deselect	High-Z	CMOS Standby
$> V_{SO}$						
$\leq V_{SO}$	X	X	X	Deselect	High-Z	Battery Back- up

PIN CONNECTIONS



Constantly monitors V_{CC} for an out of tolerance condition. When such a condition occurs, the lithium energy source is automatically switched on and write protection is unconditionally enabled to prevent garbled data. The nonvolatile static RAM can be used in place of 32K X 8 static RAM directly conforming to the popular BYTEWIDE 28 pin DIP standard. There is no limit on the number of write cycles which can be executed and no additional support circuitry is required for microprocessor interface.

DESCRIPTION

The MK48Z30 is a 262,144-bit, fully static, nonvolatile static RAM organized as 32,768 words by 8 bits. The nonvolatile memory has a self-contained lithium energy source and control circuitry which