Products to be Announced

MOSTEK

MK 4096-6

FEATURES:

- ☐ Access Time: 200 nsec☐ Cycle Time: 350 nsec
- Pin-for-pin compatibility with the 16-pin

MK 4096

- All inputs including clocks are TTL compatible with low capacitance of 8 picofarads
- ☐ Voltage pins are loacted on the corners to simplify PCB layout
- Output is TTL compatible, three-state
 Readily available automatic insertion
 equipment can be used in board assembly

DESCRIPTION:

The MK 4096 is a 4096x1 bit dynamic random access memory circuit. System oriented features include direct TTL compatibility and low capacitance at all inputs, three-state TTL compatible latched output that remains valid into the next cycle, and a chip select decoding time that does not have to add to the access time. A unique multiplexing and latching operation for the address inputs permits this circuit to be packaged in a standard 16-pin DIP on .3 inch centers.

