

MIC6211

IttyBitty[™] Operational Amplifier

Final Information

General Description

The MIC6211 IttyBitty[™] op amp is a general-purpose, highperformance, single- or split-supply, operational amplifier in a space-saving, surface-mount package.

The MIC6211 operates from 4V to 32V, single or differential (split) supply. The input common-mode range includes ground. The device features a 2.5MHz unity gain bandwidth, $6V/\mu s$ slew rate, and is internally unity-gain compensated.

Inputs are protected against reverse polarity (input voltage less than V–) and ESD (electrostatic discharge). Output is current limited for both sourcing and sinking. Output short circuits of unlimited duration are allowed, provided the power dissipation specification is not exceeded.

The MIC6211 is available in the tiny, 5-lead SOT-23-5 surface-mount package.

Features

- 4V to 32V operation
- Small footprint package
- Unity gain stable
- 2.5 MHz unity gain bandwidth
- 6V/µs typical slew rate
- Short circuit protected

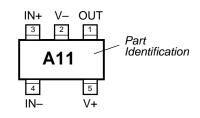
Applications

- Analog blocks
- Active filtering

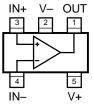
Ordering Information

Part Number	Temperature Range	Package
MIC6211BM5	-40°C to +85°C	SOT-23-5

Pin Configuration



Functional Configuration



SOT-23-5 (M5)

Pin Description

Pin Number	Pin Name	Pin Function
1	OUT	Amplifier Output
2	V–	Negative Supply: Negative supply for split supply application or ground for single supply application.
3	IN+	Noninverting Input
4	IN–	Inverting Input
5	V+	Positive Supply

Micrel, Inc. • 1849 Fortune Drive • San Jose, CA 95131 • USA • tel + 1 (408) 944-0800 • fax + 1 (408) 944-0970 • http://www.micrel.com

Absolute Maximum Ratings

Supply Voltage $(V_{V+} - V_{V-})$. 36V or ±18V
Differential Input Voltage (V _{IN+} – V _{IN-})	±36V
Input Voltage (V _{IN+} , V _{IN}) (V _V -	- 0.3V) to V _{V+}
Output Short Circuit Current Duration	∞

Operating Ratings

Micre		

Supply Voltage	4V to 32V
Ambient Temperature Range	. −40°C to +85°C
SOT-23-5 Thermal Resistance (θ_{JA})	220°C/W
(mounted to printed circuit board)	

Electrical Characteristics (Differential Supply)

 $V+=+15V, V-=-15V, V_{CM}=0V; R_{L}=2k\Omega; T_{A}=25^{\circ}C, \text{ bold } \text{values indicate } -40^{\circ}C \leq T_{A} \leq +85^{\circ}C, T_{A}=T_{J}; \text{ unless noted } T_{A}=1000 \text{ s}^{-1}$

Symbol	Parameter	Condition	Min	Тур	Max	Units
V _{OS}	Input Offset Voltage			2	7	mV
TCV _{OS}	Average Input Offset Drift	Note 1		7		μV/°C
I _B	Input Bias Current			50	250	nA
I _{os}	Input Offset Current			8	30	nA
V _{CM}	Input Voltage Range		+13.5 -15.0	+13.8 -15.3		V V
CMRR	Common Mode Rejection Ratio	V _{CM} = +13.5V, -15.0V	65	100		dB
PSRR	Power Supply Rejection Ratio	$V_{\rm S} = \pm 2.5 V$ to $\pm 15 V$	65	110		dB
A _{VOL}	Large Signal Voltage Gain	$V_0 = \pm 10V$	25	180		V/mV
V _{OUT}	Maximum Output Voltage Swing		±12.5	±14		V
B _W	Bandwidth			2.5		MHz
S _R	Slew Rate			6		V/µs
I _{SC}	Output Short Circuit Current	Sourcing or sinking	30	50		mA
I _S	Supply Current			1.3	2.0	mA

Electrical Characteristics (Single Supply)

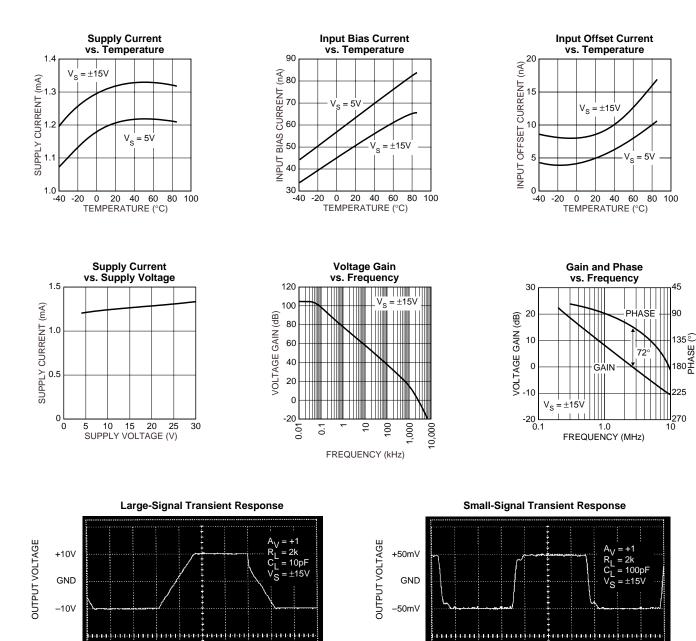
V+ = +5V, V- = 0V, V_{CM} = 0.1V; T_A = 25°C, **bold** values indicate $-40°C \le T_A \le +85°C$, $T_A = T_J$; unless noted

Symbol	Parameter	Condition	Min	Тур	Max	Units
V _{OS}	Input Offset Voltage			2	7	mV
TCV _{OS}	Average Input Offset Drift	Note 1		7		μV/°C
I _B	Input Bias Current			65	250	nA
I _{OS}	Input Offset Current			8	30	nA
V _{CM}	Input Voltage Range		+3.5 0	+3.7 -0.3		V V
CMRR	Common Mode Rejection Ratio	$V_{CM} = 0V$ to 3.5V	45	70		dB
PSRR	Power Supply Rejection Ratio	$V_{S} = \pm 2.5 V$ to $\pm 15 V$	65	105		dB
A _{VOL}	Large Signal Voltage Gain	$V_0 = 1.5V$ to 3.5V, $R_L = 2k$	15	170		V/mV
V _{OUT}	Maximum Output Voltage Swing	$R_{L} = 10k \text{ to GND}$ $R_{L} = 10k \text{ to +5V}$	+3.8	+4.0 +1.0	+1.2	V V
I _{SC}	Output Short Circuit Current	Sourcing or sinking	20	40		mA
I _S	Supply Current			1.2	1.8	mA

General Note: Devices are ESD protected; however, handling precautions are recommended.

Note 1: Not production tested.

Typical Characteristics



+10V

GND

-10V

INPUT VOLTAGE

500ns/DIV

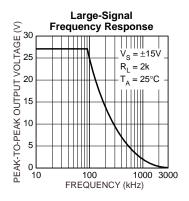
INPUT VOLTAGE

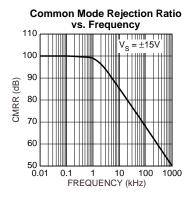
2µs/DIV

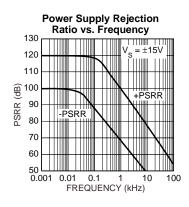
+50mV

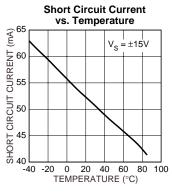
GND

–50mV

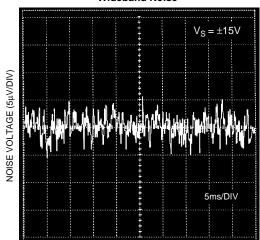












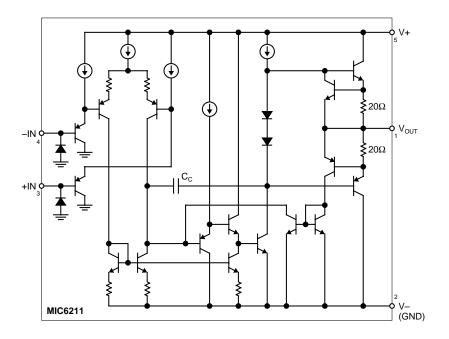
 $\nabla_{S} = \pm 15 \vee$

0.1Hz to 10Hz Noise

Wideband Noise

NOISE VOLTAGE (4μV/DIV)

Functional Diagram



Common-Mode Range and Output Voltage

The input common-mode range of the MIC6211 is from the *negative supply voltage* to *1.2V below the positive supply voltage*. The output voltage swings within 1V of the positive and negative supply voltage.

Voltage Buffer

Figure 1 shows a standard voltage follower/buffer. The output voltage equals the input voltage. This circuit is used to buffer a high impedance signal source. This circuit works equally well with single or split supplies.

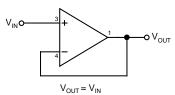


Figure 1. Voltage Buffer

Inverting Amplifier

Figure 2 shows an inverting amplifier with its gain set by the ratio of two resistors. This circuit works best with split supplies, but will perform with single supply systems if the non-inverting input (+ input) is biased up above ground.

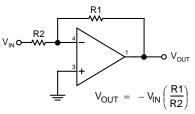


Figure 2. Inverting Amplifer

Voltage Controlled Current Sink

Figure 3 is a voltage controlled current sink. A buffer transistor forces current through a programming resistor until the feedback loop is satisfied. Current flow is V_{IN}/R . This circuit works with single or split supplies.

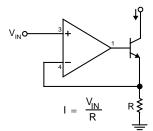


Figure 3. Voltage Controlled Current Sink

High-Pass Filter

Figure 4 is an active filter with 20dB ($10\times$) gain and a low-frequency cutoff of 10Hz. The high gain-bandwidth of the MIC6211 allows operation beyond 100kHz. This filter configuration is designed for split supplies.

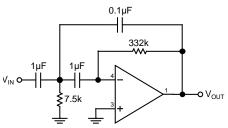


Figure 4a. High-Pass Filter

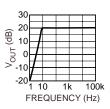


Figure 4b. High-Pass Filter Response

Summing Amplifier

Figure 5 is a single supply summing amplifier. In this configuration, the output voltage is the sum of V1 and V2, minus the sum of V3 and V4. By adding more resistors to either the inverting or non-inverting input, more voltages may be summed. This single supply version has one important restriction: the sum of V1 and V2 must exceed the sum of V3 and V4, since the output voltage cannot pull below zero with only a single supply.

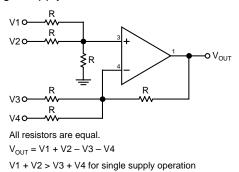
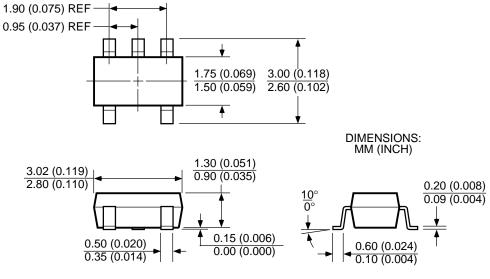


Figure 5. Summing Amplifier

Package Information



SOT-23-5 (M5)

MICREL INC. 1849 FORTUNE DRIVE SAN JOSE, CA 95131 USA

TEL + 1 (408) 944-0800 FAX + 1 (408) 944-0970 WEB http://www.micrel.com

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