Advance Information

Integrated Relay/Solenoid Driver

- Optimized to Switch 3 V to 5 V Relays from a 5 V Rail
- Compatible with "TX" and "TQ" Series Telecom Relays Rated up to 625 mW at 3 V to 5 V
- Features Low Input Drive Current
- Internal Zener Clamp Routes Induced Current to Ground Rather Than Back to Supply
- Guaranteed Off State with No Input Connection
- Supports Large Systems with Minimal Off-State Leakage
- ESD Resistant in Accordance with the 2000 V Human Body Model
- Provides a Robust Driver Interface Between Relay Coil and Sensitive Logic Circuits

Applications include:

- Telecom Line Cards and Telephony
- Industrial Controls
- Security Systems
- Appliances and White Goods
- Automated Test Equipment
- Automotive Controls

This device is intended to replace an array of three to six discrete components with an integrated part. It can be used to switch other 3 to 5 Vdc Inductive Loads such as solenoids and small DC motors.

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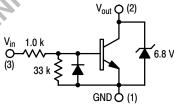
http://onsemi.com

RELAY/SOLENOID DRIVER SILICON MONOLITHIC CIRCUIT BLOCK



CASE 29-11, STYLE 14 TO-92

INTERNAL CIRCUIT DIAGRAM



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Power Supply Voltage	V _{CC}	6.0	Vdc
Recommended Operating Supply Voltage	V _{CC}	2.0-5.5	Vdc
Input Voltage	V _{in(fwd)}	6.0	Vdc
Reverse Input Voltage	V _{in(rev)}	-0.5	Vdc
Output Sink Current — Continuous	I _O	300	mA
Junction Temperature	TJ	150	°C
Operating Ambient Temperature Range	T _A	-40 to +85	°C
Storage Temperature Range	T _{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation ⁽¹⁾ Derate above 25°C	P _D	625	mW
Thermal Resistance Junction to Ambient	$R_{ heta JA}$	200	°C/W

^{1.} FR-5 PCB of 1" \times 0.75" \times 0.062", $T_A = 25$ °C

This document contains information on a new product. Specifications and information herein are subject to change without notice.

$\textbf{ELECTRICAL CHARACTERISTICS} \ (T_{A} = 25^{\circ}C \ unless \ otherwise \ noted)$

V 7	•				
Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	·				
Output Zener Breakdown Voltage (@ IT = 10 mA Pulse)	V _(BRout) V _(-BRout)	6.4 —	6.8 -0.7	7.2 —	V
Output Leakage Current @ 0 Input Voltage (V _{out} = 5.5 Vdc, V _{in} = O.C., T _A = 25°C) (V _{out} = 5.5 Vdc, V _{in} = O.C., T _A = 85°C)	loo			5.0 30	μА
ON CHARACTERISTICS	<u>.</u>				
Input Bias Current @ V_{in} = 4.0 Vdc (I_O = 250 mA, V_{out} = 0.4 Vdc, T_A = -40°C) (correlated to a measurement @ 25°C)	l _{in}	_	2.5	_	mAdc
Output Saturation Voltage (I_O = 250 mA, V_{in} = 4.0 Vdc, T_A = -40°C) (correlated to a measurement @ 25°C)			0.2	0.4	Vdc
Output Sink Current — Continuous (T _A = -40°C, V _{CE} = 0.4 Vdc, V _{in} = 4.0 Vdc) (correlated to a measurement @ 25°C)	I _{C(on)}	250	_	OB	mA

TYPICAL APPLICATION-DEPENDENT SWITCHING PERFORMANCE

SWITCHING CHARACTERISTICS

Characteristic	Symbol	V _{CC}	Min	Тур	Max	Units
Propagation Delay Times:		, O',				ns
High to Low Propagation Delay; Figures 1, 2 (5.0 V 74HC04)	t _{PHL}	5.5		55	_	
Low to High Propagation Delay; Figures 1, 2 (5.0 V 74HC04)	t _{PLH}	5.5	O	430	_	
High to Low Propagation Delay; Figures 1, 3 (3.0 V 74HC04)	CtPHL	5.5	_	85	_	
Low to High Propagation Delay; Figures 1, 3 (3.0 V 74HC04)	t _{PLH}	5.5	_	315	_	
High to Low Propagation Delay; Figures 1, 4 (5.0 V 74LS04)	t _{PHL}	5.5	_	55	_	
Low to High Propagation Delay; Figures 1, 4 (5.0 V 74LS04)	tplh	5.5	_	2385	_	
Transition Times:	.().					ns
Fall Time; Figures 1, 2 (5.0 V 74HC04)	t _f	5.5	_	45	_	
Rise Time; Figures 1, 2 (5.0 V 74HC04)	t _r	5.5		160	_	
Fall Time; Figures 1, 3 (3.0 V 74HC04)	t _f	5.5	_	70	_	
Rise Time; Figures 1, 3 (3.0 V 74HC04)	t _r	5.5	_	195	_	
Fall Time; Figures 1, 4 (5.0 V 74LS04)	t _f	5.5	_	45	_	
Rise Time; Figures 1, 4 (5.0 V 74LS04)	t _r	5.5	_	2400	_	
Input Slew Rate ⁽¹⁾	ΔV/Δt in	5.5	TBD	—	—	V/ms

^{1.} Minimum input slew rate must be followed to avoid overdissipating the device.

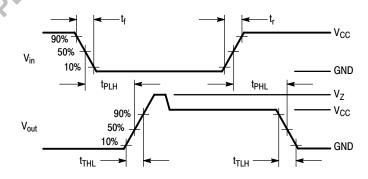


Figure 1. Switching Waveforms

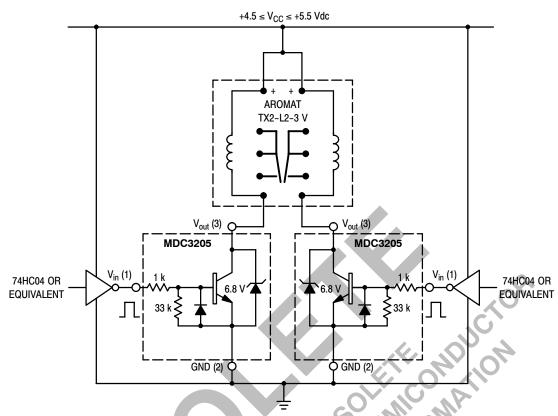


Figure 2. A 3.0-V, 200-mW Dual Coil Latching Relay Application with 5.0 V-HCMOS Interface

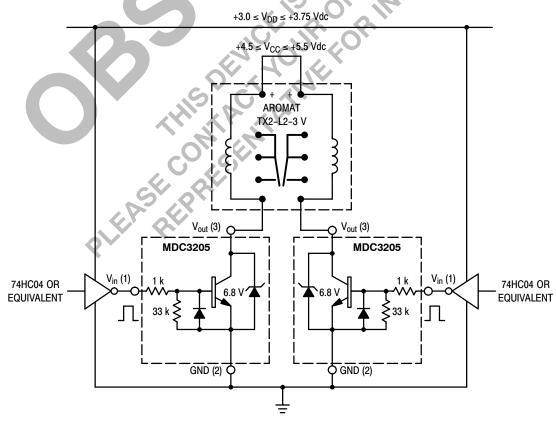


Figure 3. A 3.0-V, 200-mW Dual Coil Latching Relay Application with 3.0 V-HCMOS Interface

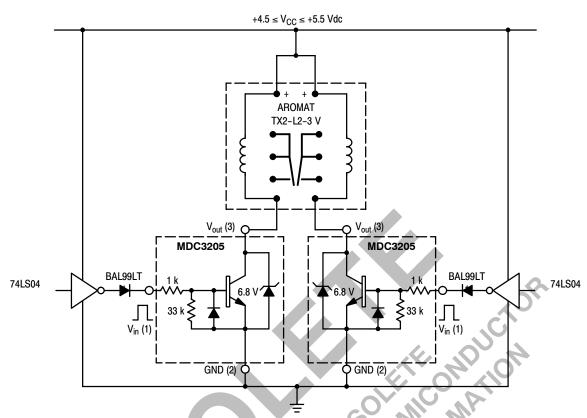


Figure 4. A 3.0-V, 200-mW Dual Coil Latching Relay Application with TTL Interface

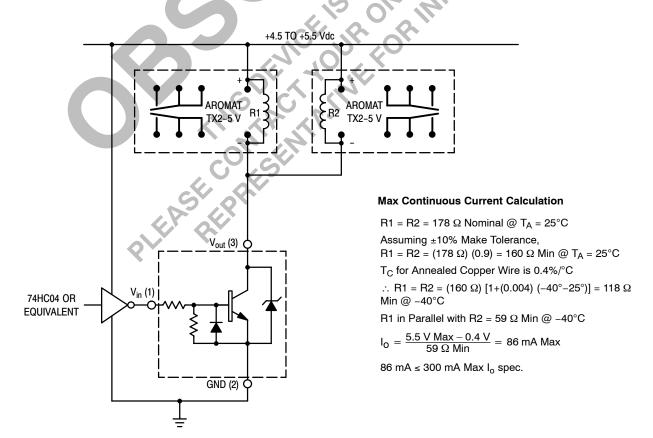
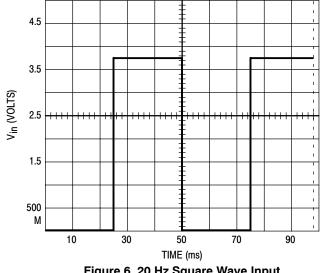


Figure 5. Typical 5.0 V, 140 mW Coil Dual Relay Application

TYPICAL OPERATING WAVEFORMS

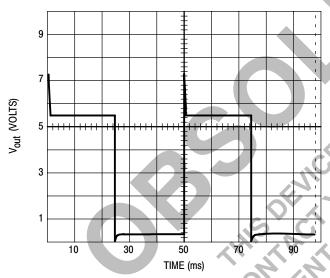
(Circuit of Figure 5)



225 175 I_C (mA) 125 75 25 10 70 30 50 90

Figure 6. 20 Hz Square Wave Input

TIME (ms) Figure 7. 20 Hz Square Wave Response



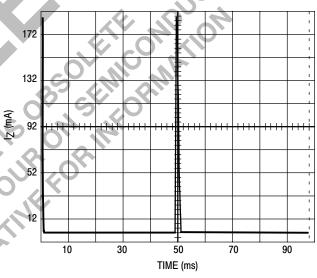
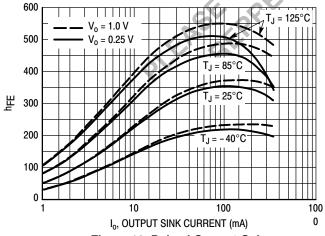


Figure 8. 20 Hz Square Wave Response

Figure 9. 20 Hz Square Wave Response



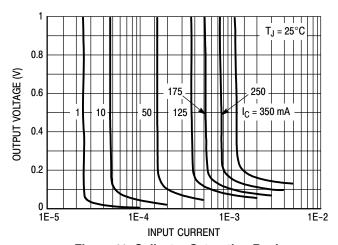


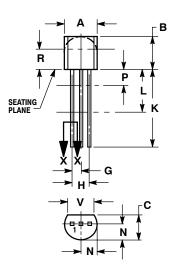
Figure 10. Pulsed Current Gain

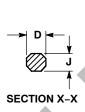
Figure 11. Collector Saturation Region

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUF AI

STYLE 14:
PIN 1. EMITTER
2. COLLECTOR
3. BASE





NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.
- CONTOUR OF PACKAGE BEYOND DIMENSION R
 IS UNCONTROLLED.
- LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	ETERS
MI	MIN	MAX	MIN	MAX
A	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	_4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1,15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020 4	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080_	0.105	2.04	2.66
Р	4	0.100		2.54
R	0.115		2.93	
٧_	0.135		3.43	

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