SLLS119A - FEBRUARY 1986 - REVISED FEBRUARY 1993

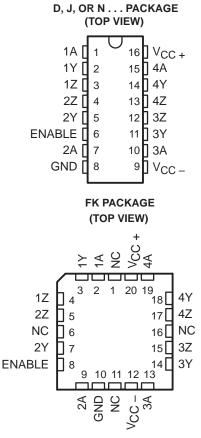
- Similar to a Dual Version of SN55/75110A Line Driver
- Improved Stability Over Supply Voltage and Temperature Ranges
- Constant-Current Outputs
- High Output Impedance
- High Common-Mode Output Voltage Range -3 V to 10 V
- Glitch-Free Power-Up/Power-Down
 Operation
- TTL-Input Compatibility
- Common-Enable Circuit
- Designed to Be Interchangeable With Motorola MC3453 and Military-Temperature-Range Version of MC3553

description

The MC3453 and MC3553 feature four line drivers with a common-enable input. When the ENABLE input is high, a constant output current is switched between each pair of output terminals in response to the logic level at that channel input. When the ENABLE is low, all channel outputs are nonconductive (transistors biased to cutoff). This minimizes loading in party-line systems where a large number of drivers share the same line.

The driver outputs have a common-mode voltage range of -3 V to 10 V, allowing common-mode voltages on the line without affecting driver performance.

All outputs should be maintained within the recommended common-mode output voltage range to ensure that the channels do not interact with each other. To minimize power dissipation, all unused outputs should be grounded.



NC-No internal connection

FU	NCTI	ON	TAR	IF.
			171	

LOGIC INPUT	ENABLE INPUT	OUTPUT CURRENT Z Y	
Н	Н	On	Off
L	Н	Off	On
Н	L	Off	Off
L	L	Off	Off

L = low logic level, H = high logic level

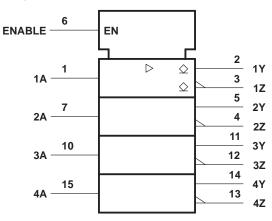
All inputs are diode clamped and are designed to satisfy TTL-system requirements. The inputs are tested at 2 V for high-logic-level input conditions and 0.8 V for low-logic-level input conditions. These tests ensure 400 mV of noise margin when interfaced with Series 54/74 TTL.

The MC3453 is characterized for operation from 0° C to 70° C. The MC3553 is characterized for operation over the full military temperature range of -55° C to 125° C.



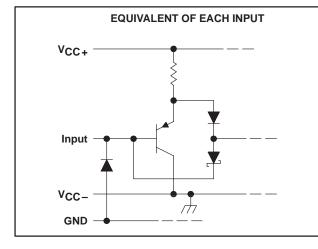
SLLS119A - FEBRUARY 1986 - REVISED FEBRUARY 1993

logic symbol[†]

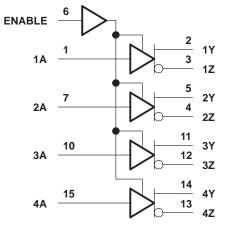


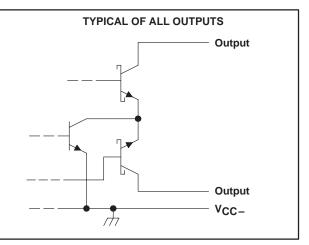
[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

schematics of inputs and outputs



logic diagram (positive logic)





absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC+} (see Note 1) Supply voltage, V _{CC -}	
Input voltage (any input)	5.5 V
Output voltage range (any output)	
Continuous total dissipation	See Dissipation Rating Table
Operating free-air temperature range: MC3453	0°C to 70°C
MC3553	–55°C to 125°C
Storage temperature range	–65°C to 150°C
Case temperature for 60 seconds: FK package	260°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds: D or N packa	ıge 260°C
Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds: J package .	300°C

NOTE 1: All voltage values are with respect to network ground terminal.



SLLS119A - FEBRUARY 1986 - REVISED FEBRUARY 1993

DISSIPATION RATING TABLE							
PACKAGE	T _A ≤ 25°C POWER RATING	OPERATING FACTOR ABOVE T _A = 25°C	T _A = 70°C POWER RATING	T _A = 125°C POWER RATING			
D	950 mW	7.6 mW/°C	608 mW	_			
FK	1375 mW	11.0 mW/°C	880 mW	275 mW			
J	1375 mW	11.0 mW/°C	880 mW	275 mW			
Ν	1150 mW	9.2 mW/°C	736 mW	—			

recommended operating conditions

		MIN	TYP	MAX	UNIT
Supply voltage Vee	$T_A \ge 25^{\circ}C$	4.5	5	5.5	V
Supply voltage, V _{CC+}	T _A < 25°C	4.75	5	5.5	v
Supply voltage Vee	$T_A \ge 25^{\circ}C$	-4.5	-5	-5.5	v
Supply voltage, V _{CC} _	T _A < 25°C	-4.75	-5	-5.5	V
High-level input voltage, VIH		2		5.5	V
Low-level input voltage, VIL		0		0.8	V
Common-mode output voltage range	VOCR +	0		10	V
Common-mode output voltage range	VOCR-	0		-3	V
Operating free-air temperature, T_{Δ}	MC3453	0		70	°C
Operating nee-an temperature, TA	MC3553	-55		125	0

electrical characteristics over recommended operating free-air temperature range, $V_{\mbox{CC}\pm}$ = MAX (unless otherwise noted)

	PARAMETER	TEST	TEST CONDITIONS [†]		TYP‡	MAX	UNIT
VIK	Input clamp voltage	$I_{I} = -12 \text{ mA}$			-0.9	-1.5	V
	On-state output current	$V_{CC+} = MAX,$	V _{CC} -= MAX		11	15	mA
IO(on)	On-state output current	$V_{CC+} = MIN, V_{CC-} = MIN$		6.5	11		mA
I _{O(off)}	Off-state output current	$V_{CC+} = MIN,$	$V_{CC-} = MIN, V_O = 10 V$			100	μA
	High-level input current	$V_{I} = 2.4 V$				40	μA
ΊН	nigh-level input current	$V_{I} = V_{CC+} max$				1	mA
۱ _{IL}	Low-level input current	V _I = 0.4 V	$V_{I} = 0.4 V$			-1.6	mA
	Supply current from V _{CC+}	A inputs at 0.4 V	ENABLE at 2 V		33	50	mA
ICC+	Supply current norm VCC+	A inputs at 0.4 V	ENABLE at 0.4 V		33	50	IIIA
	Supply current from Vee		ENABLE at 2 V		-68	-90	mA
ICC-	Supply current from V _{CC} -	A inputs at 0.4 V	ENABLE at 0.4 V		-31	-40	mA

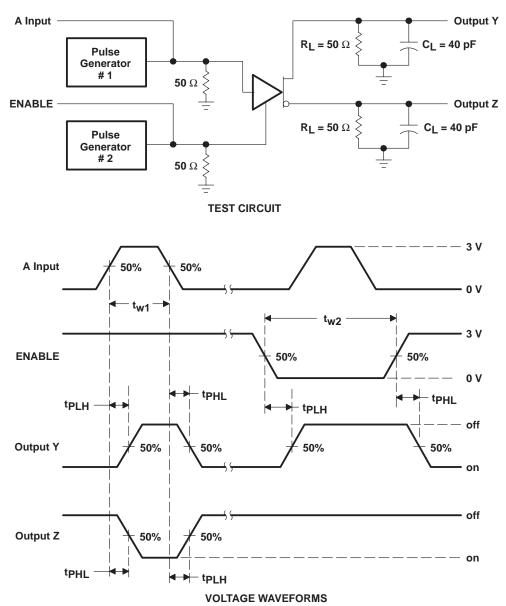
[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. [‡] All typical values are at $V_{CC+} = 5 \text{ V}$, $V_{CC-} = -5 \text{ V}$, and $T_A = 25^{\circ}\text{C}$.

switching characteristics, V_{CC+} = 5 V, V_{CC-} = –5 V, R_L = 50 Ω , C_L = 40 pF, T_A = 25°C

	PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	МАХ	UNIT
t _{PLH}	Propagation delay time, low-to-high level output	А	Y or Z			9	15	ns
^t PHL	Propagation delay time, high-to-low level output	А	Y or Z	See Figure 1		7	15	ns
^t PLH	Propagation delay time, low-to-high level output	ENABLE	Y or Z	See Figure 1		14	25	ns
^t PHL	Propagation delay time, high-to-low level output	ENABLE	Y or Z			15	25	ns



SLLS119A - FEBRUARY 1986 - REVISED FEBRUARY 1993



PARAMETER MEASUREMENT INFORMATION

NOTES: A. The pulse generators have the following characteristics: $Z_O = 50 \ \Omega$, $t_r = t_f = 10 \pm 5 \ ns$, $t_{W1} = 200 \ ns$, PRR $\leq 1 \ MHz$, $t_{W2} = 1 \ \mu s$, PRR $\leq 500 \ kHz$.

B. C_I includes probe and jig capacitance.





PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
MC3453D	OBSOLETE	SOIC	D	14	TBD	Call TI	Call TI

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details. TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

Important Information and Disclaimer:The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

Products		Applications	
Amplifiers	amplifier.ti.com	Audio	www.ti.com/audio
Data Converters	dataconverter.ti.com	Automotive	www.ti.com/automotive
DSP	dsp.ti.com	Broadband	www.ti.com/broadband
Interface	interface.ti.com	Digital Control	www.ti.com/digitalcontrol
Logic	logic.ti.com	Military	www.ti.com/military
Power Mgmt	power.ti.com	Optical Networking	www.ti.com/opticalnetwork
Microcontrollers	microcontroller.ti.com	Security	www.ti.com/security
		Telephony	www.ti.com/telephony
		Video & Imaging	www.ti.com/video
		Wireless	www.ti.com/wireless

Mailing Address:

Texas Instruments

Post Office Box 655303 Dallas, Texas 75265

Copyright © 2005, Texas Instruments Incorporated