V_{CC1} = Pin 1, 15
V_{CC2} = Pin 16
  V_{EE} = Pin 8

P_D = 160 mW typ/pkg (No load)
t_{pd} = 1.5 ns typ (All Output Loaded)

High Speed Dual 3-input 3-output OR Gate

The MC10210 is designed to drive up to six transmission lines simultaneously. The multiple outputs of this device also allow the wire "OR"-ing of several levels of gating for minimization of gate and package count.

The ability to control three parallel lines with minimum propagation delay from a single point makes the MC10210 particularly useful in clock distribution applications where minimum clock skew is desired.

The MC10210 is a higher speed version of the MC10110. It is a pin-for-pin replacement for the device. Three V_{CC} pins are provided and each one should be used.