



CLOCKED TRUTH TABLE

R	C	Q	Qn+1
L	L	∅	Qn
L	H	L	L
L	H	H	H
H	L	∅	L

∅ Don't Care

* A clock H is a clock transition from a low to a high state

 V_{CC} = Pin 16 V_{EE} = Pin 8 P_D = 460 mW typ/pkg (No Load) f_{toggle} 150 MHz (typ)

Hex "D" Master-Slave Flip-Flop/With Reset

The MC10186 contains six high-speed, master slave type "D" flip-flops. Clocking is common to all six flip-flops. Data is entered into the master when the clock is low. Master to slave data transfer takes place on the positive-going Clock transition. Thus, outputs may change only on a positive-going Clock transition. A change in the information present at the data (D) input will not affect the output information any other time due to the master-slave construction of this device. A common Reset is included in this circuit. Reset only functions when clock is low.