Hex D Master-Slave Flip-Flop

The MC10176 contains six high-speed, master slave type "D" flip-flops. Clocking is common to all six flip-flops. Data is entered into the master when the clock is low. Master to slave data transfer takes place on the positive-going Clock transition. Thus, outputs may change only on a positive-going Clock transition. A change in the information present at the data (D) input will not affect the output information any other time due to the master-slave construction of this device.

CLOCKED TRUTH TABLE

C	D	Q_{n+1}
	0	Q_n
	1	1

(Don't Care)

V_{CC1} = Pin 1
V_{CC2} = Pin 16
V_{EE} = Pin 8

P_D = 460 mW typ/pkg (No Load)
f_{toggle} 150 MHz (typ)

MC10176   **FLIP-FLOPS**