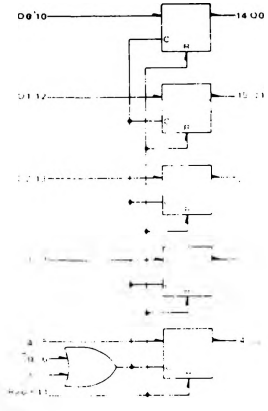
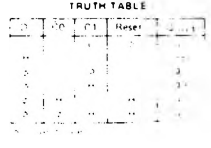


TRUTH TABLE



V_{CC1} = Pin 1
V_{CC2} = Pin 16
V_{EE} = Pin 8

P_D = 400 mW typ/pkg (No Load)
t_{pd} = 2.5 ns typ (Data to output)

Quint Latch

The MC10175 is a high speed, low power quint latch. It features five D type latches with common reset and a common two-input clock. Data is transferred on the negative edge of the clock and latched on the positive edge. The two clock inputs are "OR" ed together. Propagation delays are typically 2.5 nanoseconds from each data input to the output.

Any change on the data input will be reflected at the outputs while the clock is low. The outputs are latched on the positive transition of the clock. While the clock is in the high state, a change in the information present at the data inputs will not affect the output information. The reset input is enabled only when the clock is in the high state.

The MC10175 allows storage of five bits of information, and it is useful in temporary storage applications in high speed central processors, accumulators, register files, digital communication systems, instrumentation, and test equipment.