V_{CC1} = Pin 1
V_{CC2} = Pin 16
V_{EE} = Pin 8

P_D = 315 ns typ/pkg (No Load)
t_{pd} = 4.0 ns typ

Binary to 1-8 Decoder (High)

The MC10162 is designed to convert three lines of input data to a one-of-eight output. The selected output will be high while all other outputs are low. The enable inputs, when either or both are high, force all outputs low.

TRUTH TABLE

INPUTS					OUTPUTS							
$\overline{E0}$	$\overline{E1}$	C	B	A	Q0	Q1	Q2	Q3	Q4	Q5	Q6	Q7
L	L	L	L	L	H	L	L	L	L	L	L	L
L	L	L	L	H	L	H	L	L	L	L	L	L
L	L	L	H	L	L	L	H	L	L	L	L	L
L	L	L	H	H	L	L	L	H	L	L	L	L
L	L	H	L	L	L	L	L	L	H	L	L	L
L	L	H	L	H	L	L	L	L	L	H	L	L
L	L	H	H	L	L	L	L	L	L	L	H	L
L	L	H	H	H	L	L	L	L	L	L	L	H
H	◊	◊	◊	◊	L	L	L	L	L	L	L	L
◊	H	◊	◊	◊	L	L	L	L	L	L	L	L

◊ Don't Care

MC10162

DECODERS