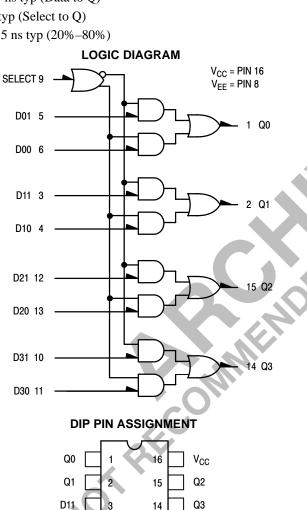
# **Quad 2-Input Multiplexer** (Non-Inverting)

The MC10158 is a quad two channel multiplexer. A common select input determines which data inputs are enabled. A high (H) level enables data inputs D00, D10, D20, and D30 and a low (L) level enables data inputs D01, D11, D21, and D31.

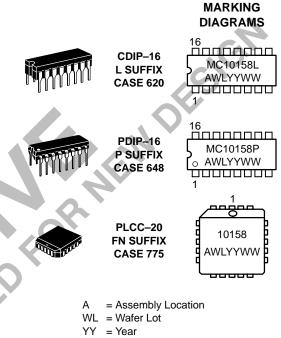
- P<sub>D</sub>=197 mW typ/pkg (No Load)
- t<sub>pd</sub>=2.5 ns typ (Data to Q)
- 3.2 ns typ (Select to Q)
- $t_r, t_f=2.5 \text{ ns typ} (20\%-80\%)$





## **ON Semiconductor**

http://onsemi.com



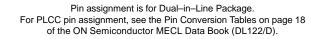
WW = Work Week

#### **TRUTH TABLE**

Select	D0	D1	Q
L	Х	L	L
L	Х	Н	н
Н	L	Х	L
Н	Н	Х	Н

### **ORDERING INFORMATION**

Device	Package	Shipping
MC10158L	CDIP-16	25 Units / Rail
MC10158P	PDIP-16	25 Units / Rail
MC10158FN	PLCC-20	46 Units / Rail



D20

D21

D30

D31

SELECT

13

12

11

10

9

D10

D01

D00

NC

 $V_{EE}$ 

4

5

6

7

8

## MC10158

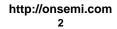
#### **ELECTRICAL CHARACTERISTICS**

			Test Limits							
		Pin Under		–30°C +25°C		+85°C				
Characteristic	Symbol	Test	Min	Max	Min	Тур	Max	Min	Max	Unit
Power Supply Drain Current	Ι <sub>Ε</sub>	8		53		38	48		53	mAdc
Input Current	l <sub>inH</sub>	9 5		360 400			225 250		225 250	μAdc
	I <sub>inL</sub>	5	0.5		0.5			0.3		μAdc
Output Voltage Logic 1	V <sub>OH</sub>	1	-1.060	-0.890	-0.960		-0.810	-0.890	-0.700	Vdc
Output Voltage Logic 0	V <sub>OL</sub>	1	-1.890	-1.675	-1.850		-1.650	-1.825	-1.615	Vdc
Threshold Voltage Logic 1	V <sub>OHA</sub>	1	-1.080		-0.980			-0.910		Vdc
Threshold Voltage Logic 0	V <sub>OLA</sub>	1		-1.655			-1.630		-1.595	Vdc
Switching Times (50 $\Omega$ Load)									C	ns
Propagation Data Input Delay Select Input	t <sub>5–1–</sub> t <sub>9+1+</sub>	1 1	1.3 2.5	3.1 4.8	1.2 2.4	2.5 3.2	3.0 4.5	1.3 2.5	3.2 4.8	
Rise Time (20 to 80%)	t <sub>1+</sub>	1	1.6	3.4	1.5	2.5	3.3	1.6	3.4	
Fall Time (20 to 80%)	t <sub>1-</sub>	1	1.6	3.4	1.5	2.5	3.3	1.6	3.4	

#### ELECTRICAL CHARACTERISTICS (continued)

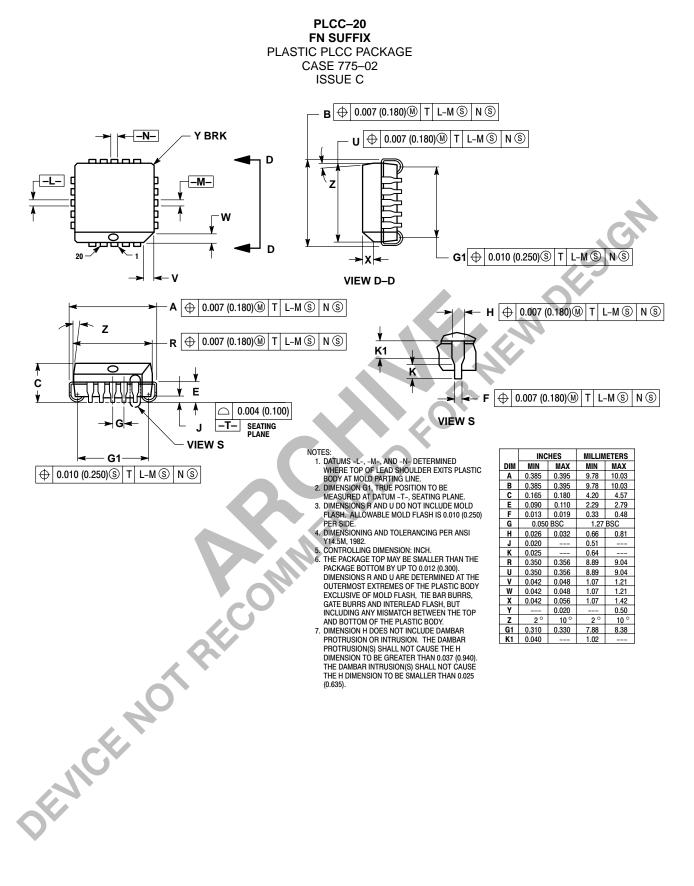
				TEST VOLTAGE VALUES (Volts)					
		@ Test Te	mperature	V <sub>IHmax</sub>	V <sub>ILmin</sub>	VIHAmin	VILAmax	V <sub>EE</sub>	]
			–30°C	-0.890	-1.890	-1.205	-1.500	-5.2	
			+25°C	-0.810	-1.850	-1.105	-1.475	-5.2	
+85°C			-0.700	-1.825	-1.035	-1.440	-5.2		
			Pin	TEST V	VOLTAGE APPLIED TO PINS LISTED BELOW				
Characteristic		Symbol	Under Test	V <sub>IHmax</sub>	V <sub>ILmin</sub>	V <sub>IHAmin</sub>	V <sub>ILAmax</sub>	V <sub>EE</sub>	(V <sub>CC</sub> ) Gnd
Power Supply Drain (	Current	Ξ	8	8			8	16	
Input Current	1	linH	9 5	9 5				8 8	16 16
		l <sub>inL</sub>	5		5			8	16
Output Voltage	Logic 1	V <sub>OH</sub>	1	5				8	16
Output Voltage	Logic 0	V <sub>OL</sub>	1					8	16
Threshold Voltage	Logic 1	VOHA	1			5		8	16
Threshold Voltage	Logic 0	V <sub>OLA</sub>	1				5	8	16
Switching Times	(50 $\Omega$ Load)			+1.11V	+0.31V	Pulse In	Pulse Out	–3.2 V	+2.0 V
Propagation Delay	Data Input Select Input	t <sub>5–1–</sub> t <sub>9+1+</sub>	1 1	6		5 9	1 1	8 8	16 16
Rise Time	(20 to 80%)	t <sub>1+</sub>	1			5	1	8	16
Fall Time	(20 to 80%)	t <sub>1-</sub>	1			5	1	8	16

Each MECL 10,000 series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 linear fpm is maintained. Outputs are terminated through a 50-ohm resistor to -2.0 volts. Test procedures are shown for only one gate. The other gates are tested in the same manner.

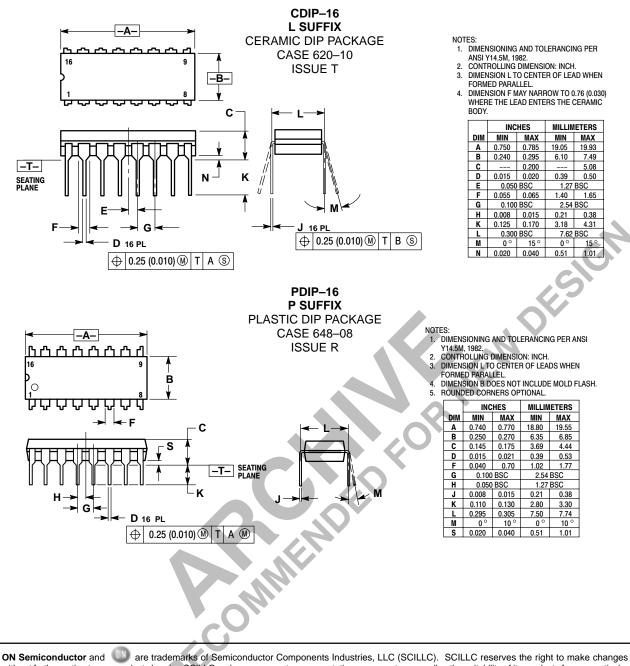


## MC10158

#### PACKAGE DIMENSIONS



## MC10158



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