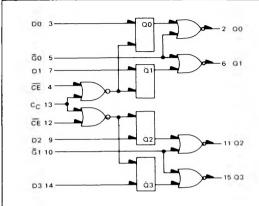
MC10153 LATCHES



## TRUTH TABLE

1	G	С	۵	Q <sub>n+1</sub>
	Н	Φ	Φ	L
	L	н	Φ	Q <sub>n</sub>
	L	L	L	L
	L	L	н	н
φ = Don't Care				
C = CC + CE				

 $\begin{array}{l} V_{CC1} = Pin \quad 1 \\ V_{CC2} = Pin \quad 16 \\ V_{EE} = Pin \quad 8 \end{array}$ 

 $P_D = 310 \text{ mW typ/pkg (No Load)}$ 

 $t_{pd} = 4.0 \text{ ns typ}$ 

## Quad Latch

The MC10153 is a high speed, low power, MECL quad latch consisting of four bistable latch circuits with D type inputs and gated Q outputs. Open emitters allow a large number of outputs to be wire-O Red together. Latch outputs are gated, allowing direct wiring to a bus. When the clock is low, outputs will follow D inputs. Information is latched on positive going transition of the clock. The MC10153 provides the same logic function as the MC10133, except for inversion of the clock.