$$2 = (4 + 5) \cdot (6 + 7 + 9)$$
$$3 = \overline{(4 + 5) \cdot (6 + 7 + 9)}$$

V_{CC1} = Pin 1
V_{CC2} = Pin 16
V_{EE} = Pin 8

P_D = 100 mW typ/pkg (No Load)
t_{pd} = 2.3 ns typ

Dual 2-Wide 2-3-input OR-AND/OR-AND-INVERT Gate

The MC10117 is a general purpose logic element designed for use in data control, such as digital multiplexing or data distribution. Pin 9 is common to both gates.

MC10117   **GATES**