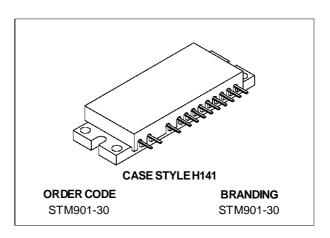
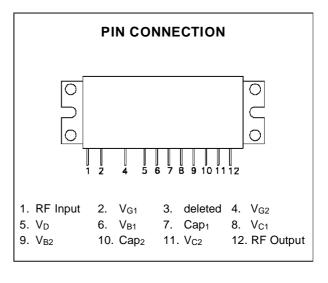


# REPOWER MODULE

- LINEAR POWER AMPLIFIER
- 860 900 MHz
- 26 VOLTS
- INPUT/OUTPUT 50 OHMS
- Pout = +44.7 dBm PEP
- GAIN = 35 dB MIN.





# DESCRIPTION

The STM901-30 module is designed for digital cellular radio base station applications in the 860-900 MHz frequency range operating at 26V.

The STM901-30 is designed to meet the low distortion, high linearity requirements of modern digital cellular base station equipment.

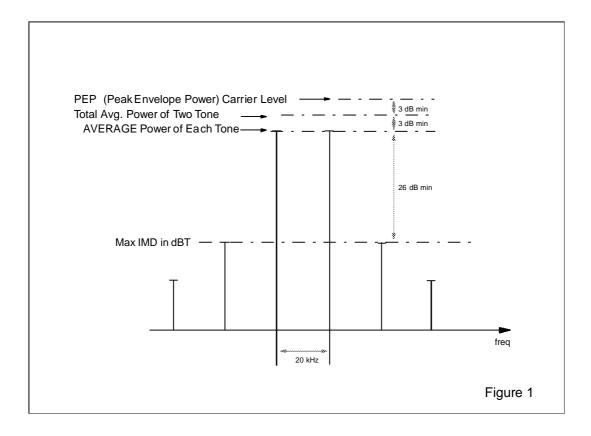
### ABSOLUTE MAXIMUM RATINGS (Tcase = 25°C)

Symbol	Parameter	Value	Unit	
V, V <sub>D</sub> , V <sub>C1</sub> , V <sub>C2</sub>	DC Supply Voltage	28	Vdc	
lq1	Bias Current @ V = 26V, 1st Stage	0.40	Adc	
I <sub>Q2</sub>	Bias Current @ V = 26V, 2nd Stage	0.40	Adc	
I <sub>Q3</sub>	Bias Current @ V = 26V, 3rd Stage	0.54	Adc	
IQ4	Bias Current @ V = 26V, 4th Stage	1.62	Adc	
PIN	RF Input Power (P <sub>OUT</sub> < 44.7 dBm PEP)	14	dBm PEP	
Pout	RF Output Power (V = 26V)	48	dBm PEP	
T <sub>STG</sub>	Storage Temperature	- 30 to +100	°C	
T <sub>OPER</sub>	Operating Temperature	- 30 to +100	°C	

Symbol	Parameter	Test Conditions	Value			Unit
Symbol			Min.	Тур.	Max.	Unit
BW	Frequency Range		860	—	900	MHz
GP	Power Gain	P <sub>OUT</sub> = +44.7 dBm PEP	35	38	_	dB
η <sub>dt</sub> *	Double-Tone Efficiency	$P_{OUT} = +44.7 \text{ dBm PEP}$	27	30		%
	Input VSWR	P <sub>OUT</sub> = +44.7 dBm PEP	_	1.5:1	3:1	
IMD*	Intermodulation Distortion	P <sub>OUT</sub> = +44.7 dBm PEP	_	-33	-26	dBT**
_	Load Mismatch	VSWR = 5:1 V = 26Vdc P <sub>OUT</sub> = +44.7 dBm PEP	No Degradation in Output Power			

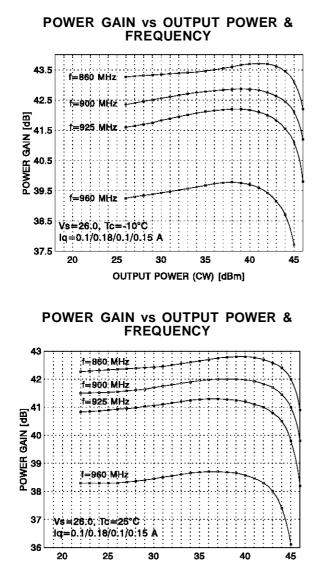
# ELECTRICAL SPECIFICATIONS ( $T_{case} = 25^{\circ}C$ , $V_D$ , $V_{C1}$ , $V_{C2} = 26V$ ) ( $I_{DQ1} = 100$ mA, $I_{DQ2} = 180$ mA, $I_{CQ1} = 50$ mA, $I_{CQ2} = 150$ mA)

Note: \* Two-Tone test; 20 KHz separation \*\* dBT - in dB, referenced to tone level (See Figure 1 below)





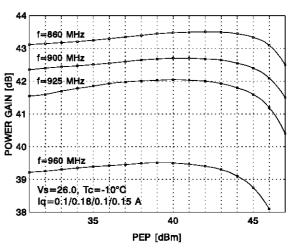
#### **TYPICAL PERFORMANCE**



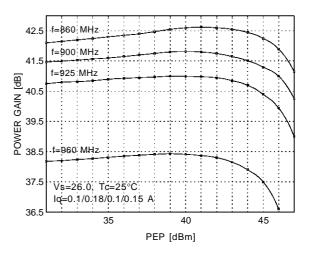
**POWER GAIN vs OUTPUT POWER &** FREQUENCY 40.3 f=860 MHz 39.3 =900 MHz =925 MHz 1=960 MHz 35.3 Vs=26.0, Tc=+85°C Iq=0.1/0.18/0.1/0.15 A 34.3 20 25 30 35 40 45 OUTPUT POWER (CW) [dBm]

OUTPUT POWER (CW) [dBm]

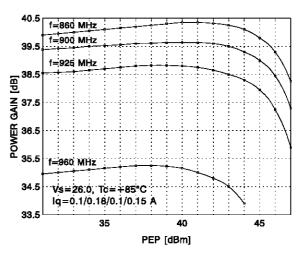




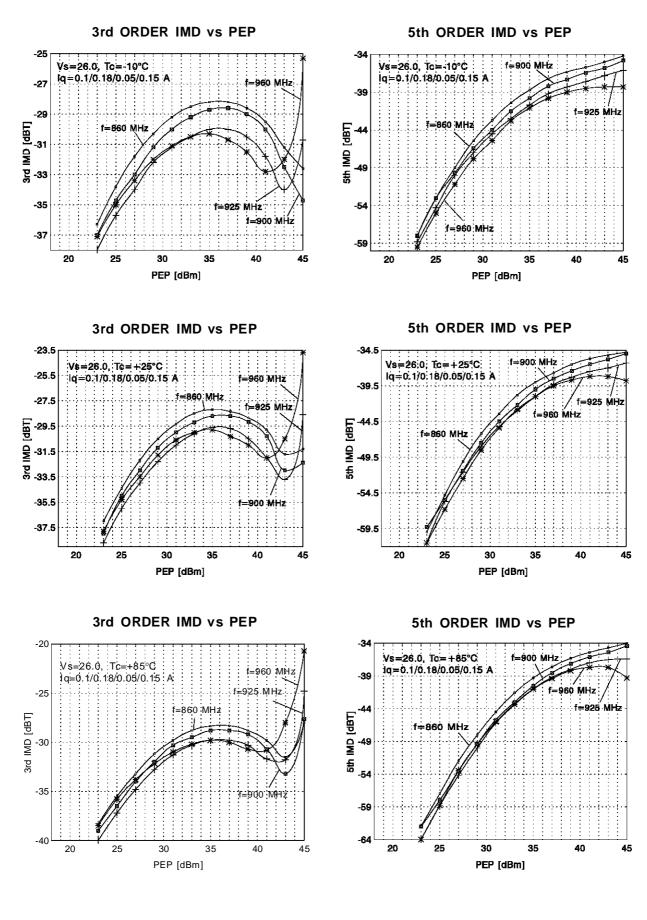
**POWER GAIN vs PEP & FREQUENCY** 



**POWER GAIN vs PEP & FREQUENCY** 



# TYPICAL PERFORMANCE

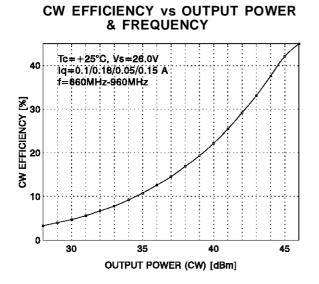


SGS-THOMSON

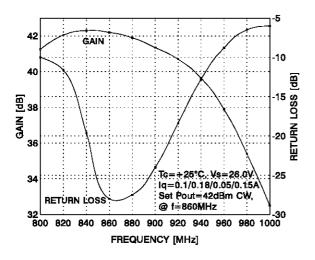
POELECTRON CS

MC

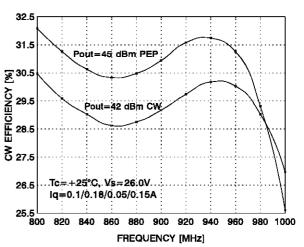
## **TYPICAL PERFORMANCE**

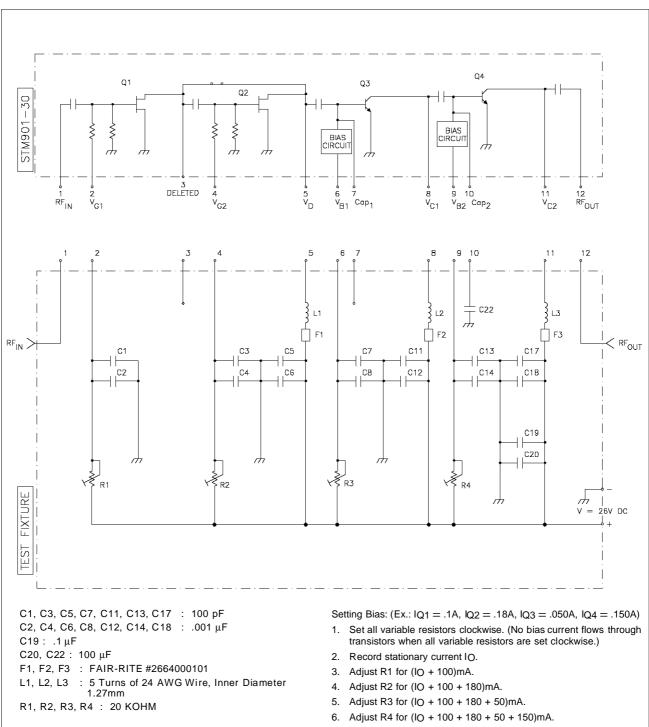


GAIN & RETURN LOSS vs FREQUENCY



**EFFICIENCY vs FREQUENCY** 







### APPLICATIONS RECOMMENDATIONS

#### **OPERATION LIMITS**

The STM901-30 power module should never be operated under any condition which exceeds the Absolute Maximum Ratings presented on this data sheet. Nor should the module be operated continuously at any of the specified maximum ratings. If the module is to be operated under any condition such that it may be subjected to one or more of the maximum rating conditions, care must be taken to monitor other parameters which may be affected.

#### DECOUPLING

Failure to properly decouple any of the voltage supply pins will result in oscillations at certain operating frequencies. Therefore, it is recommended that these pins be bypassed as indicated in the Module DC and Test Fixture Configuration drawing of this data sheet.

#### MODULE MOUNTING

To insure adequate thermal transfer from the module to the heatsink, it is recommended that a satisfactory thermal compound such as Dow Corning 340, Wakefield 120-2 or equivalent be applied between the module flange and the heatsink.

The heatsink mounting surface under the module should be flat to within +/- 0.05 mm (+/- 0.002 inch). The module should be mounted to the heatsink using 3 mm (or 4-40) or equivalent screws torqued to 5-6 kg-cm (4-6 in-lb).

The module leads are attached to the equipment PC board using 180°C solder applied to the leads with a properly grounded soldering iron tip, not to exceed 195°C, applied a minimum of 2 mm (0.080 inch) from the body of the module for a duration not to exceed 15 seconds per lead. It is imperative that no other portion of the module, other than the leads, be subjected to temperatures in excess of 100°C (maximum storage temperature), for any period of time, as the plastic moulded cover, internal components and sealing adhesives may be adversely affected by such conditions.

Due to the construction techniques and the materials used within the module, reflow soldering of the flange heatsink or the leads, is not recommended.

#### THERMAL CONSIDERATIONS

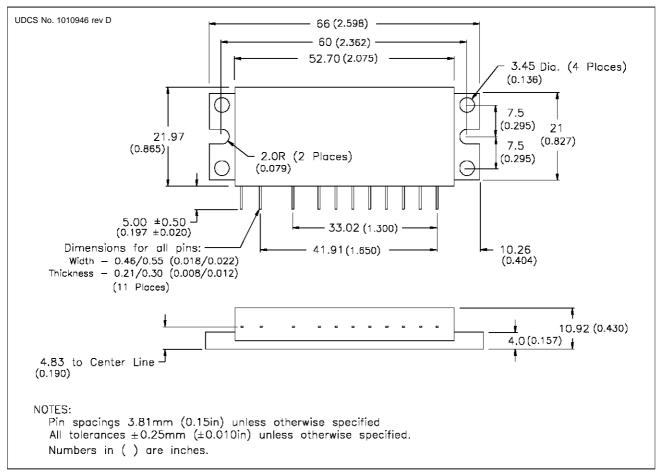
It will be necessary to provide a suitable heatsink in order to maintain the module flange temperature at or below the maximum case operating temperature. In a case where the module output power will be limited to +44.7 dBm (30W PEP) and designing for the worst case double-tone efficiency of 25%, the power dissipated by the module will be 48 watts. The heatsink must be designed such that the thermal rise will be less than the difference between the maximum ambient temperature at which the module will operate and the maximum operating case temperature of the module while dissipating 48 watts.

At  $T_{case} = +85^{\circ}C$ , V = 26v,  $I_{Q1} = 0.1A$ ,  $I_{Q2} = 0.18A$ ,  $I_{Q3} = 0.05A$ ,  $I_{Q4} = 0.2A$ ,  $Z_L = 50$  ohms and  $P_{OUT} = +44.7dBm$  PEP, maximum junction temperatures for the individual transistors should be below the following values:

Q1 = 115°C Q2 = 130°C Q3 = 125°C Q4 = 145°C



# PACKAGE MECHANICAL DATA



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