

M48T39Y

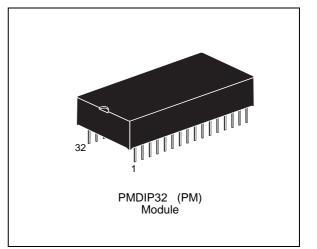
256Kb (32K x 8) TIMEKEEPER[®] SRAM

PRELIMINARY DATA

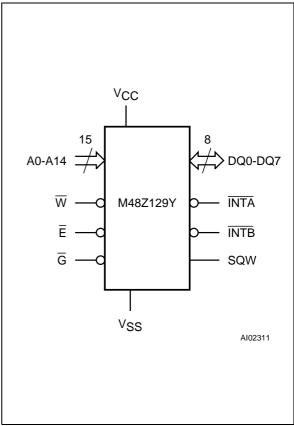
- INTEGRATED ULTRA LOW POWER SRAM, REAL TIME CLOCK, POWER-FAIL CONTROL CIRCUIT, BATTERY and CRYSTAL
- BCD CODED YEAR, MONTH, DAY, DATE, HOURS, MINUTES and SECONDS
- AUTOMATIC POWER-FAIL CHIP DESELECT and WRITE PROTECTION
- WRITE PROTECT VOLTAGE (V_{PFD} = Power-fail Deselect Voltage):
 M48T39Y: 4.20V ≤ V_{PFD} ≤ 4.50V
- PROGRAMMABLE INTERRUPTS and SQUARE WAVE OUTPUT
- WATCHDOG TIMER RESTARTS on OUT-OF-CONTROL PROCESSOR
- CLOCK ACCURACY IS BETTER THAN ± 1 MINUTE per MONTH at 25 °C
- 10 YEARS of DATA RETENTION and CLOCK OPERATION in the ABSENCE of POWER
- SELF-CONTAINED BATTERY and CRYSTAL in DIP PACKAGE
- PROGRAMMABLE ALARM OUTPUT ACTIVE in the BATTERY BACK-UP MODE
- PIN and FUNCTION COMPATIBLE with DS1386

DESCRIPTION

The M48T39Y TIMEKEEPER[®] RAM is a non-volatile 262,144 bit static RAM and real time clock organized as 32,768 words by 8 bits. System integration features include Programmable Alarms, Watchdog Timer and Interval Timer. The special 32-pin DIP package provides a highly integrated battery back-up memory and real time clock solution.



Logic Diagram



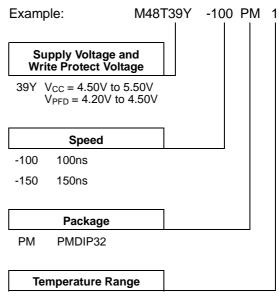
January 1998

This is preliminary information on a new product now in development or undergoing evaluation. Details are subject to change without notice.

DIP Pin Connections

A2 [A1 [A0 [1 2 3 4 5 6 7 8 9 10 11 12	M48T39Y	 31 30 29 28 27 26 25 24 23 22 21) VCC) SQW) VCC) W] A13] A8] A9] A11] G] A10] Ē] DQ7
	-			F
-	-			E Contraction of the second se
_				Г
A4 [8	M48T39Y	25	D A11
A3 [9		24]G
A2 [10		23] A10
A1 [11		22	ΔĒ
A0 [12		21] DQ7
DQ0 [13		20] DQ6
DQ1 [14		19] DQ5
DQ2 [15		18] DQ4
vss [16		17] DQ3
-		Al	02312	-

Ordering Information Scheme For a list of available options or for further information on any aspect of this device, please contact the SGS-THOMSON Sales Office nearest to you.



0 to 70 °C 1

Signal Names

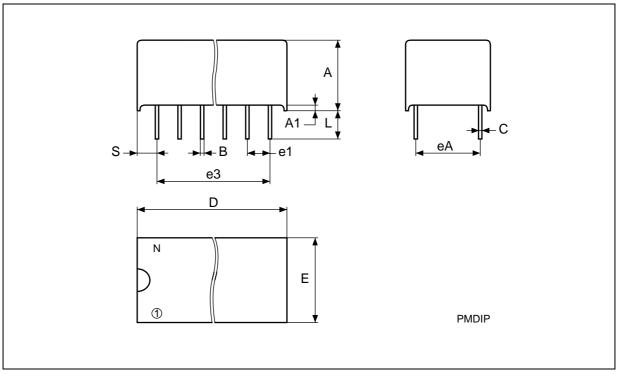
A0-A14	Address Inputs		
DQ0-DQ7	Data Inputs / Outputs		
Ē	Chip Enable Input		
G	Output Enable Input		
W	Write Enable Input		
INTA	Interrupt Output A (Open Drain)		
INTB	B Interrupt Output B (Open Drain)		
SQW	Square Wave Output		
V _{CC}	Supply Voltage		
V _{SS}	Ground		



Symb		mm			inches			
	Тур	Min	Max	Тур	Min	Мах		
А		9.27	9.52		0.365	0.375		
A1		0.38	_		0.015	_		
В		0.43	0.59		0.017	0.023		
С		0.20	0.33		0.008	0.013		
D		42.42	43.18		1.670	1.700		
Е		18.03	18.80		0.710	0.740		
e1		2.30	2.81		0.090	0.110		
e3		34.43	42.08		1.355	1.656		
eA		14.99	16.00		0.590	0.630		
L		3.05	3.81		0.120	0.150		
S		1.91	2.79		0.075	0.110		
Ν		32			32			

PMDIP32 - 32 pin Plastic DIP Module

PMDIP32



Drawing is not to scale.

Information furnished is believed to be accurate and reliable. However, SGS-THOMSON Microelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of SGS-THOMSON Microelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. SGS-THOMSON Microelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of SGS-THOMSON Microelectronics.

© 1998 SGS-THOMSON Microelectronics - All Rights Reserved

IMEKEEPER is a registered trademark of SGS-THOMSON Microelectronics ™ BYTEWIDE is a trademark of SGS-THOMSON Microelectronics

SGS-THOMSON Microelectronics GROUP OF COMPANIES Australia - Brazil - China - France - Germany - Italy - Japan - Korea - Malaysia - Malta - Morocco - The Netherlands -Singapore - Spain - Sweden - Switzerland - Taiwan - Thailand - United Kingdom - U.S.A.

