

CMOS 128 X 128 DIGITAL SWITCHING MATRIX

ADVANCE DATA

- HARDWARE (pin-to-pin) AND SOFTWARE COMPATIBLE WITH M044
- 128 INPUT AND 128 OUTPUT CHANNEL DIGITAL SWITCHING MATRIX (non blocking)
- TYPICAL APPLICATION IN PABX
- EUROPEAN AND U.S. STANDARD COMPATIBLE (32/24 serial channels per frame)
- PCM INPUTS AND OUTPUTS MATUALLY COMPATIBLE
- ACTUAL INPUT-OUTPUT CHANNEL CONNECTIONS STORED AND MODIFIED VIA AN ON-CHIP 8-BIT PARALLEL MICROPROCESSOR INTERFACE
- 5 MAIN "FUNCTIONS" OR "INSTRUCTIONS" AVAILABLE
- TYPICAL BIT RATE : 2Mbit/s (lower allowed)
- TYPICAL SYNCHRONIZATION RATE : 8KHz (time frame is 125µs)
- 5 VOLT POWER SUPPLY WITH INTERNALLY GENERATED BIAS VOLTAGE
- TYPICAL CURRENT CONSUMPTION IS 22mA
- MOS & TTL INPUT/OUTPUT LEVELS COMPATIBLE
- DIFFUSED WITH HIGH DENSITY ADVANCED 1.2µs CMOS PROCESS HCMOS3

Main instruction controlled by the micro-processor interface

- CHANNEL CONNECTION/DISCONNECTION
- CHANNEL DISCONNECTION
- INSERTION OF A BYTE ON A PCM OUTPUT CHANNEL
- TRANSFER TO THE MICROPROCESSOR OF A SINGLE PCM OUTPUT CHANNEL SAMPLE
- TRANSFER TO THE MICROPROCESSOR OF A SINGLE OUTPUT CONTROL WORD

