



Intel® Core™ i3-2100 Processor

3M Cache, 3.10 GHz

Essentials

- Product Collection [Legacy Intel® Core™ Processors](#)
- Code Name [Products formerly Sandy Bridge](#)
- Vertical Segment Desktop
- Processor Number i3-2100
- Status Discontinued
- Launch Date Q1'11
- Expected Discontinuance Q3'2013
- Lithography 32 nm
- Recommended Customer Price \$120.00

Performance

- # of Cores 2
- # of Threads 4
- Processor Base Frequency 3.10 GHz
- Cache 3 MB SmartCache
- Bus Speed 5 GT/s DMI
- TDP 65 W

Supplemental Information

- Embedded Options Available No
- Datasheet [View now](#)

Memory Specifications

- Max Memory Size (dependent on memory type) 32 GB
- Memory Types DDR3 1066/1333
- Max # of Memory Channels 2
- Max Memory Bandwidth 21 GB/s

Processor Graphics

- Processor Graphics † Intel® HD Graphics 2000
- Graphics Base Frequency 850 MHz
- Graphics Max Dynamic Frequency 1.10 GHz
- Intel® Quick Sync Video Yes
- Intel® InTru™ 3D Technology Yes
- Intel® Flexible Display Interface (Intel® FDI) Yes
- Intel® Clear Video HD Technology Yes

- # of Displays Supported † 2
- Device ID 0x102

Expansion Options

- PCI Express Revision 2.0
- Max # of PCI Express Lanes 16

Package Specifications

- Sockets Supported FCLGA1155
- Max CPU Configuration 1
- T_{CASE} 69.1°C
- Package Size 37.5mm x 37.5mm

Advanced Technologies

- Intel® Turbo Boost Technology † No
- Intel® vPro™ Platform Eligibility † No
- Intel® Hyper-Threading Technology † Yes
- Intel® Virtualization Technology (VT-x) † Yes
- Intel® Virtualization Technology for Directed I/O (VT-d) † No
- Intel® VT-x with Extended Page Tables (EPT) † Yes
- Intel® 64 † Yes
- Instruction Set 64-bit
- Instruction Set Extensions Intel® SSE4.1, Intel® SSE4.2, Intel® AVX
- Idle States Yes
- Enhanced Intel SpeedStep® Technology Yes
- Thermal Monitoring Technologies Yes
- Intel® Fast Memory Access Yes
- Intel® Flex Memory Access Yes
- Intel® Identity Protection Technology † Yes

Security & Reliability

- Intel® AES New Instructions No
- Intel® Trusted Execution Technology † No
- Execute Disable Bit † Yes