# 5-channel BTL driver for CD and MD Players

# **BA6996FP**

The BA6996FP, an IC designed for CD and MD players, has a 5-channel BTL power driver and a standard operational amplifier. The spindle and tray driver share a single output buffer, and are specified with a control pin. In addition, the internal level shift circuit reduces the number of attached components.

## Applications

CD players, CD-ROM drives, MD players and other optical disc devices

#### Features

- 1) A 5-channel BTL driver in a HSOP 28-pin package, allowing for application miniaturization.
- 2) Five drivers are turned on and off according to the combination of control pin logic.
- The tray driver operates even when the servo power supply drops.
- 4) Internal thermal shutdown circuit.
- 5) Gain is adjustable with an attached resistor.
- 6) Internal standard operational amplifier.
- 7) Supports low-voltage servos.

# ● Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Power supply voltage	Vcc	18	V
Power dissipation	Pd	1.7*	w
Operating temperature	Topr	<b>−35~+85</b>	°C
Storage temperature	Tstg	<b>−55~+150</b>	°C

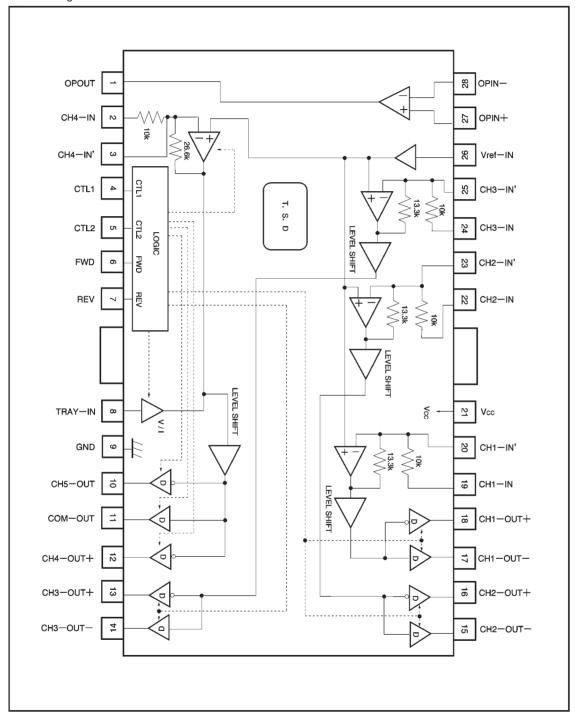
<sup>\*</sup> Reduced by 13.6 mW for each increase in Ta of 1℃ over 25℃. When mounted on a 50 × 50 × 1mm paper phenol PCB board.

# ■Recommended operating conditions (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit
Power supply voltage	Vcc	4.8	_	12	V



# Block diagram

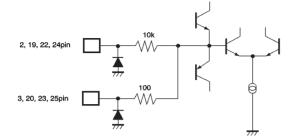


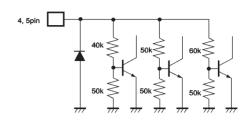
# Pin descriptions

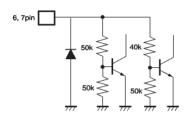
Pin No.	Pin name	Function			
1	OPOUT	Operational amplifier output			
2	CH4-IN	Channel 4 input			
3	CH4-IN'	Channel 4 gain adjustment input			
4	CTL1	Control 1 input			
5	CTL2	Control 2 input			
6	FWD	Tray forward input			
7	REV	Tray reverse input			
8	TRAY-IN	Tray input			
9	GND	Substrate ground			
10	CH5-OUT-	Tray negative output			
11	COM-OUT	Tray positive output/channel 4 negative output			
12	CH4-OUT+	Channel 4 positive output			
13	CH3-OUT+	Channel 3 positive output			
14	CH3-OUT-	Channel 3 negative output			
15	CH2-OUT-	Channel 2 negative output			
16	CH2-OUT+	Channel 2 positive output			
17	CH1-OUT-	Channel 1 negative output			
18	CH1-OUT+	Channel 1 positive output			
19	CH1-IN	Channel 1 input			
20	CH1-IN'	Channel 1 gain adjustment input			
21	Vcc	Vcc			
22	CH2-IN	Channel 2 input			
23	CH2-IN'	Channel 2 gain adjustment input			
24	CH3-IN	Channel 3 input			
25	CH3-IN'	Channel 3 gain adjustment input			
26	Vref-IN	Bias amplifier input			
27	OPIN+	Operational amplifier non-inverted input			
28	OPIN-	Operational amplifier inverted input			

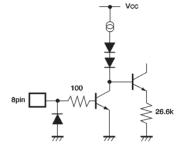
<sup>\*</sup> Positive and negative output is relative to the polarity of the input pins. (For example, pin 18 output is HIGH when pin 19 output is LOW.)

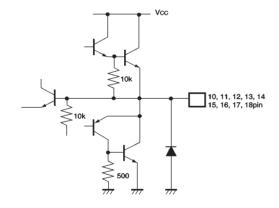
# Input/output circuits

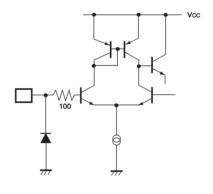


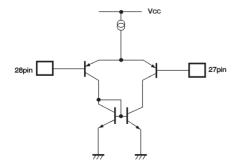












●Electrical characteristics (unless otherwise noted, Ta = 25°C, Vcc = 8V, f = 1kHz, RL = 8Ω)

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Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Quiescent current dissipation	lcc	8.5	11.5	14.5	mA	No load
Output voltage 1, offset	Voo	-40	_	40	mV	Channel 1 to 3
Output voltage 2, offset	Voo	-100	_	100	mV	Channel 4
Maximum output pin voltage 1	V <sub>OHD1</sub>	3.7	4.3	_	V	V <sub>IN</sub> =V <sub>CC</sub>
Maximum output pin voltage 2	V <sub>OHD2</sub>	_	-4.3	-3.7	V	V <sub>IN</sub> =GND
Closed loop voltage gain 1	Gvc1	6.5	8.0	9.5	dB	V <sub>IN</sub> =0.1V <sub>rms</sub> , 1kHz (excluding channel4)
Closed loop voltage gain 2	Gvc2	11.5	14.0	16.5	dB	V <sub>IN</sub> =0.1V <sub>rms</sub> , 1kHz (channel4)
Ripple rejection	RR	_	60	_	dB	V <sub>IN</sub> =0.1V <sub>rms</sub> , 100Hz
Slew rate	SR	_	2.0	_	V/μs	100 Hz square wave, 3 VP-P output
⟨Tray driver⟩						
Output voltage F	Vof	2.5	3.0	3.5	٧	Pin 8 voltage=3 V
Output voltage R	Vor	-3.5	-3.0	-2.5	٧	Pin 8 voltage=3 V
Output voltage range F	Vome	3.7	4.3	_	٧	Pin 8 voltage=5 V
Output voltage range R	Vomr	_	-4.3	-3.7	٧	Pin 8 voltage=5 V
Load requiation F	△VFI	_	250	500	mV	I∟=100-400 mA, pin 8 voltage = 2.5 V
Load requiation R	△VRI	_	250	500	mV	I∟=100-400 mA, pin 8 voltage = 2.5 V
Line requiation F	△V <sub>FL</sub>	_	300	600	mV	Vcc=5V~12V
Line requiation R	△VRL	_	300	600	mV	Vcc=5V~12V
Output voltage, offset	Vool	-50	_	50	mV	Braking, output voltage
(Logic: CT1, CTL2, FWD and I	REV					
Input high level voltage	ViH	2.0	_	8.0	V	Maximum: Vcc
Input low level voltage	VIL	-0.3	_	0.5	V	
Input high level current	Іін	_	_	500	μΑ	
Input low level current	lı∟	_	_	500	μΑ	
(Operational amplifier)						
Offset voltage	Vofop	<b>-5</b>	0	5	mV	
Input bias current	IBIAS	_	_	300	nA	
Output high level voltage	Vонор	6.0	_	_	V	
Output low level voltage	Volop	_	_	1.1	٧	
Output drive current (source)	loso	10	40	_	mA	GND at 50 Ω
Output drive current (sink)	losi	10	50	_	mA	Vcc at 50 Ω
Open loop voltage gain	Gvo	_	78	_	dB	V <sub>IN</sub> =-75dBV, 1kHz
Slew rate	SRop	_	1	_	V/μs	100 Hz square wave, 4 V <sub>P-P</sub> output
Ripple rejection	RRop	_	65	_	dB	V <sub>IN</sub> =0.1V <sub>rms</sub> , 100Hz
Synchronous signal rejection	CMRR		84		dB	VIN=0.1Vms, 1kHz

O Not designed for radiation resistance.

## Measurement circuit

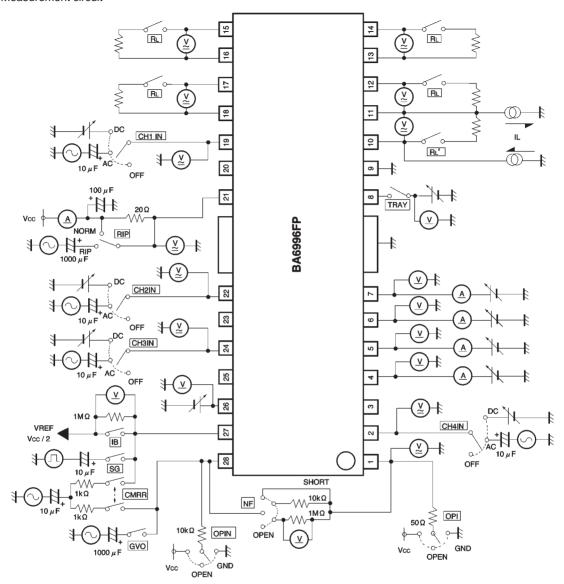


Fig.1

# Application example

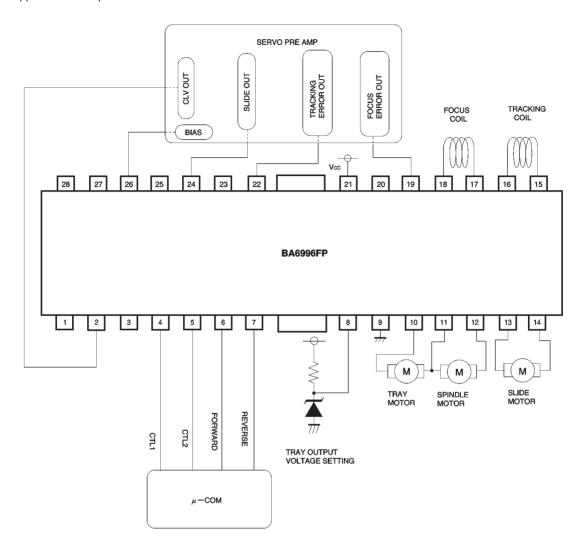
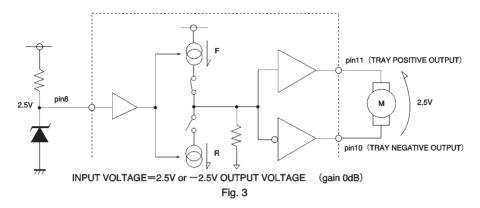


Fig.2

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## Operation notes

(1) Setting the tray motor driver voltage (forward mode)



Note: The tray driver will not exceed the power supply's maximum output voltage, even if set above this maximum voltage (refer to the following page). The example above applies only when setting below the maximum output voltage. Maximum output voltage for the power supply can be output by pulling up the tray input pin (pin 8) or by connecting it to Vcc.

#### (2) Mute functions

Mute function	Muted channels		
Thermal shutdown	All channels		
Muting during supply voltage drop	All channels		
Muting during bias voltage drop	channel1 to 4		

«Thermal shutdown»

The output current is muted when the chip temperature exceeds 175°C (typically).

«Supply voltage drop muting»

The internal circuits turn off when the supply voltage drops below 4.3V (typically), and turn on again when it rises above 4.5V (typically).

- (3) Muting occurs during thermal shutdown and when the supply voltage or bias pin voltage drops. In each case, only the driver is muted. The output pin voltage during muting is the internal bias voltage, roughly  $V_{\text{CC}}$ –VF/2.
- (4) Be sure to connect the IC to a  $0.1\mu F$  bypass capacitor to the power supply, at the base of the IC.
- (5) Connect the radiating fin to an external ground.

#### Electrical characteristic curves

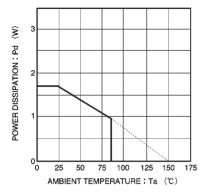


Fig.4 Thermal derating curve

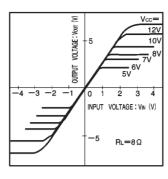


Fig. 5 Driver CH1-3 I / O characteristics (when changing power supply voltage)

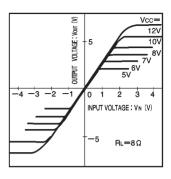


Fig. 6 Driver CH4 I / O characteristics (when changing power supply voltage)

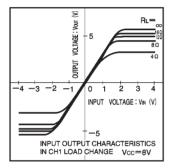


Fig. 7 Driver CH1-3 I / O characteristics (when load changes)

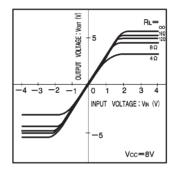


Fig. 8 Driver CH4 I / O characteristics (when load changes)

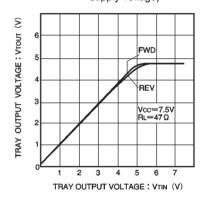


Fig. 9 Tray driver I / O characteristics

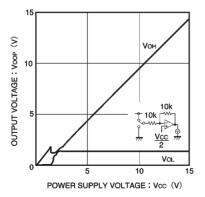


Fig. 10 Power supply voltage vs. high / low output voltage

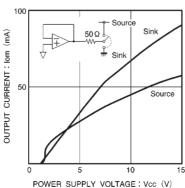
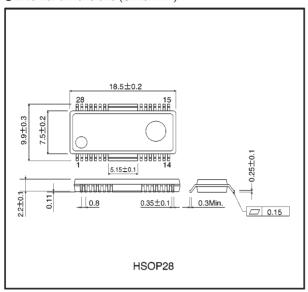


Fig. 11 Power supply voltage vs. operational amplifier output operating current

# ●External dimensions (Units: mm)



# **MEMO**