82542 4-BIT QUAD EXCLUSIVE-NOR

ADVANCED INFORMATION DIGITAL 8000 SERIES SCHOTTKY TTL/MSI

DESCRIPTION

The 82S42 contains four independent Exclusive-NOR gates which may be used to implement digital comparison functions. The 82S42 outputs are bare collector to facilitate implementation of multiple-bit comparisons; a 4-bit comparison is made by connecting the outputs of the four independent gates together.

FEATURES

- SCHOTTKY-CLAMPED TTL STRUCTURE
- PNP INPUTS

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• OPEN COLLECTOR OUTPUTS

LOGIC DIAGRAM

ELECTRICAL CHARACTERISTICS

Propagation Delay (Typ)	
A _n , B _n to f _n	9ns (82S42)
Input Load Current (Max)	
l _{In} "0"	800µA
l _{In"1"}	20µA
Output Current (Min)	
lout"O"	30mA @ 0.5V (82S42)

