# 54LS30,DM54LS30,DM74LS30

54LS30 DM54LS30 DM74LS30 8-Input NAND Gate



Literature Number: SNOS302A



# 54LS30/DM54LS30/DM74LS30 8-Input NAND Gate

#### **General Description**

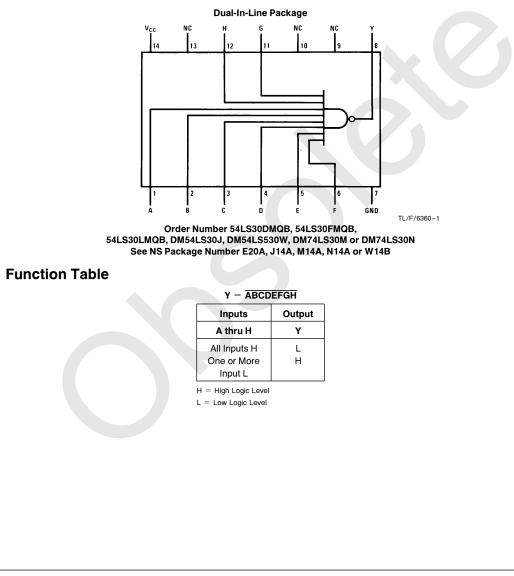
#### Features

This device contains a single gate which performs the logic NAND function.

 Alternate Military/Aerospace device (54LS30) is available. Contact a National Semiconductor Sales Office/ Distributor for specifications. 54LS30/DM54LS30/DM74LS30 8-Input NAND Gate

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# **Connection Diagram**



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#### Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	
DM54LS and 54LS	-55°C to +125°C
DM74LS	0°C to +70°C
Storage Temperature Range	-65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

#### **Recommended Operating Conditions**

Symbol	Parameter	DM54LS30			DM74LS30			Units
Cymbol		Min	Nom	Max	Min	Nom	Max	Onito
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V <sub>IH</sub>	High Level Input Voltage	2			2			V
VIL	Low Level Input Voltage			0.7			0.8	V
I <sub>OH</sub>	High Level Output Current			-0.4			-0.4	mA
I <sub>OL</sub>	Low Level Output Current			4			8	mA
T <sub>A</sub>	Free Air Operating Temperature	-55		125	0		70	°C

### Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units	
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$				-1.5	V	
	High Level Output	$V_{CC} = Min, I_{OH} = Max$	DM54	2.5	3.4		v	
	Voltage	V <sub>IL</sub> = Max	DM74	2.7	3.4			
V <sub>OL</sub> Low Level Output Voltage	V <sub>OL</sub>	Low Level Output	$V_{CC} = Min, I_{OL} = Max$	DM54		0.25	0.4	
	Voltage	$V_{IH} = Min$	DM74		0.35	0.5	v	
		$I_{OL} = 4 \text{ mA}, V_{CC} = \text{Min}$	DM74		0.25	0.4		
l <sub>l</sub>	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$				0.1	mA	
I <sub>IH</sub>	High Level Input Current	$V_{CC} = Max, V_I = 2.7V$				20	μΑ	
IIL	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$	$V_{CC} = Max, V_I = 0.4V$			-0.4	mA	
l <sub>OS</sub> S	s Short Circuit Output Current	V <sub>CC</sub> = Max	DM54	-20		-100	mA	
		Output Current (Note 2) DM7	DM74	-20		-100	IIIA	
ICCH	Supply Current with Outputs High	V <sub>CC</sub> = Max			0.35	0.5	mA	
ICCL	Supply Current with Outputs Low	V <sub>CC</sub> = Max			0.6	1.1	mA	

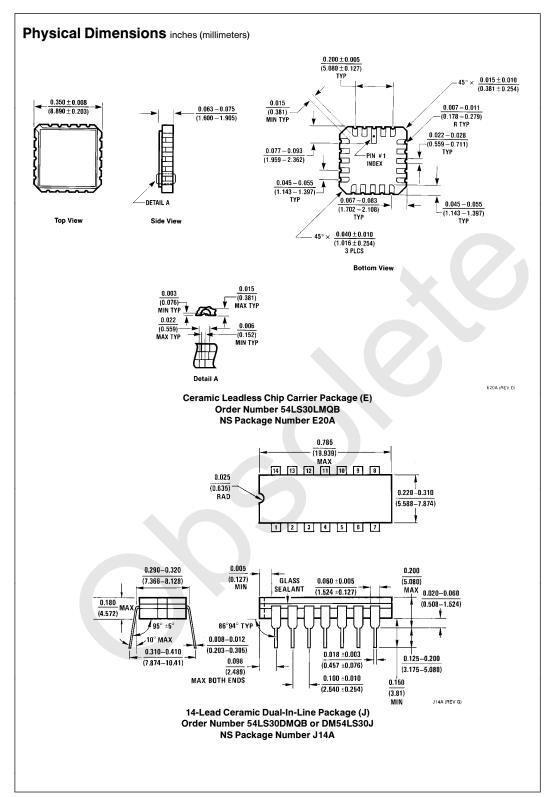
### Switching Characteristics at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$ (See Section 1 for Test Waveforms and Output Load)

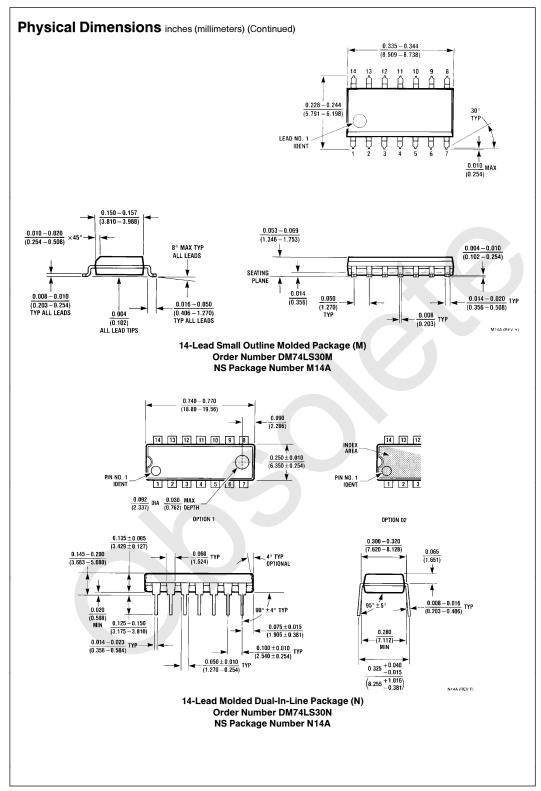
Symbol	Parameter	C <sub>L</sub> = 15 pF		C <sub>L</sub> =	Units	
		Min	Max	Min	Мах	
t <sub>PLH</sub>	Propagation Delay Time Low to High Level Output	4	12	5	18	ns
t <sub>PHL</sub>	Propagation Delay Time High to Low Level Output	4	15	5	20	ns
Note 1: All typicals	are at $V_{CC} = 5V$ . $T_A = 25^{\circ}C$ .					

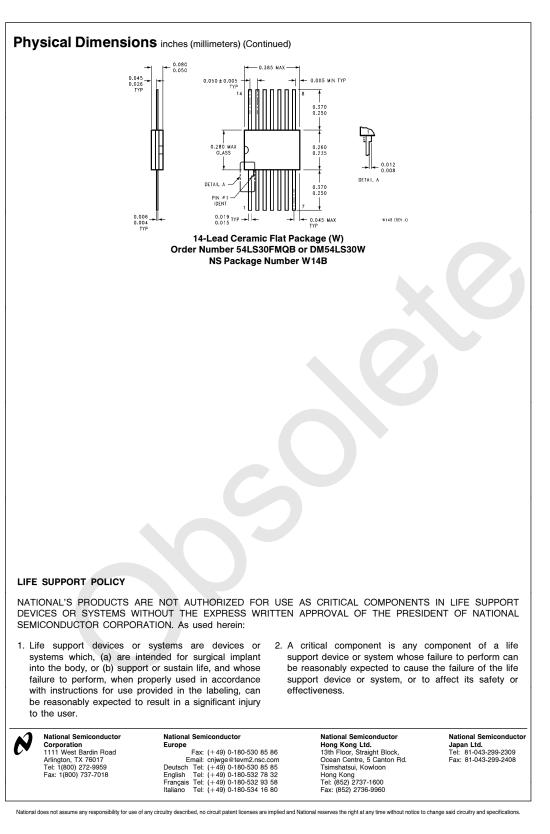
**Note 1:** All typicals are at  $V_{CC} = 5V$ ,  $T_A = 25^{\circ}C$ .

Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.









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