54LS20,DM54LS20,DM74LS20

54LS20 DM54LS20 DM74LS20 Dual 4-Input NAND Gates



Literature Number: SNOS291A

54LS20/DM54LS20/DM74LS20 Dual 4-Input NAND Gates

General Description

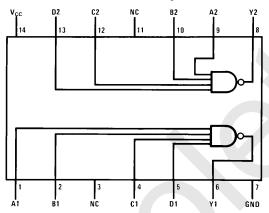
Features

This device contains two independent gates each of which performs the logic NAND function.

 Alternate Military/Aerospace device (54LS20) is available. Contact a National Semiconductor Sales Office/ Distributor for specifications

Connection Diagram

Dual-In-Line Package



TL/F/6355-1

Order Number 54LS20DMQB, 54LS20FMQB, 54LS20LMQB, DM54LS20J, DM54LS20W, DM74LS20M or DM74LS20N See NS Package Number E20A, J14A, M14A, N14A or W14B

Function Table

$Y = \overline{ABCD}$

		Inpi	Output				
	Α	В	O	D	Y		
	Х	Х	Х	L	Н		
	X	X	L	Х	Н		
N	X	L	Х	Х	Н		
	L	Х	Х	Х	Н		
	Н	Н	Н	Н	L		

 $\mathsf{H} \,=\, \mathsf{High}\,\,\mathsf{Logic}\,\,\mathsf{Level}$

L = Low Logic Level

 $X \,=\, \text{Either Low or High Logic Level}$

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V Input Voltage 7V

Operating Free Air Temperature Range

 $\begin{array}{ccc} \text{DM54LS and 54LS} & -55^{\circ}\text{C to} + 125^{\circ}\text{C} \\ \text{DM74LS} & 0^{\circ}\text{C to} + 70^{\circ}\text{C} \\ \text{Storage Temperature Range} & -65^{\circ}\text{C to} + 150^{\circ}\text{C} \\ \end{array}$

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM54LS20			DM74LS20			Units
Cymbol	T drameter		Nom	Max	Min	Nom	Max	Omis
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.7			0.8	V
	High Level Output Current			-0.4			-0.4	mA
l _{OL}	Low Level Output Current			4			8	mA
T _A	Free Air Operating Temperature	-55		125	0		70	°C

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

		·	•		• •		
Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$				-1.5	٧
V _{OH}	High Level Output	$V_{CC} = Min, I_{OH} = Max,$	DM54	2.5	3.4		V
	Voltage	V _{IL} = Max	DM74	2.7	3.4		
V _{OL}	Low Level Output	V _{CC} = Min, I _{OL} = Max,	DM54		0.25	0.4	V
	Voltage	V _{IH} = Min	DM74		0.35	0.5	
		$I_{OL} = 4 \text{ mA}, V_{CC} = Min$	DM74		0.25	0.4	
II	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$				0.1	mA
I _{IH}	High Level Input Current	$V_{CC} = Max, V_I = 2.7V$				20	μΑ
I _{IL}	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$				-0.36	mA
los	Short Circuit Output Current	V _{CC} = Max (Note 2)	DM54	-20		-100	mA
			DM74	-20		-100	
Іссн	Supply Current with Outputs High	V _{CC} = Max			0.4	0.8	mA
I _{CCL}	Supply Current with Outputs Low	V _{CC} = Max			1.2	2.2	mA

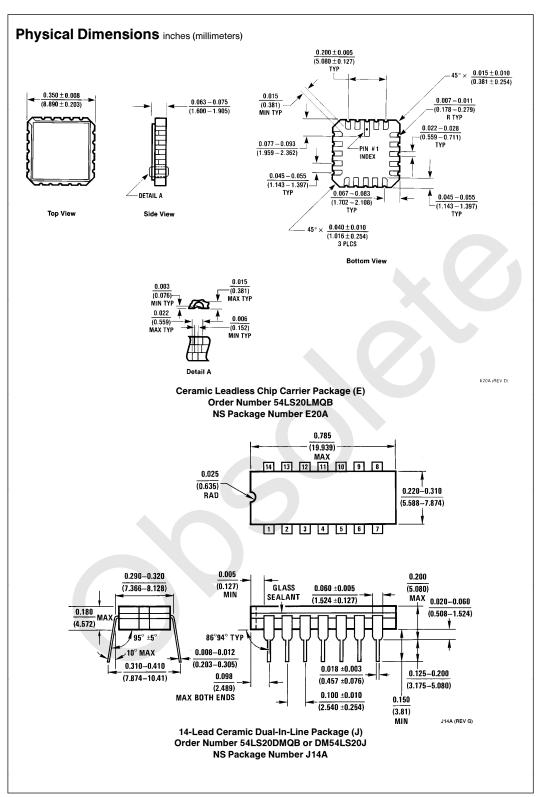
$\textbf{Switching Characteristics} \text{ at V}_{CC} = 5 \text{V and T}_{A} = 25 ^{\circ}\text{C (See Section 1 for Test Waveforms and Output Load)}$

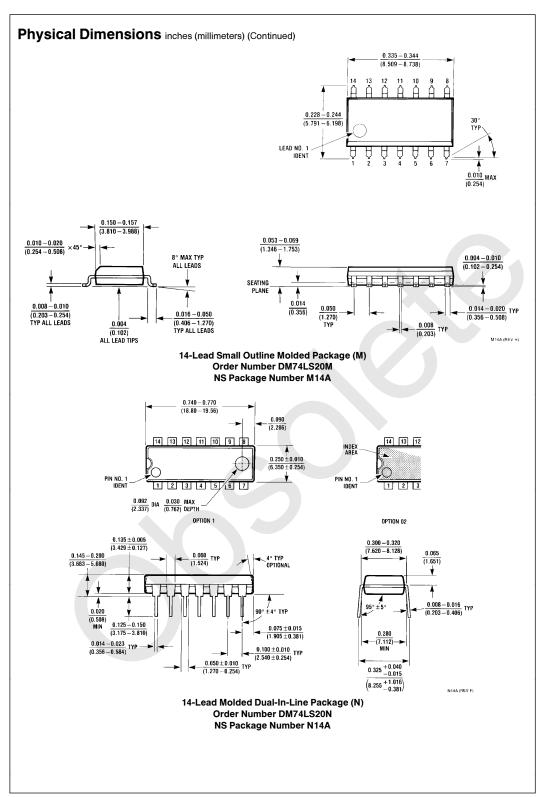
	Parameter					
Symbol		C _L =	15 pF	C _L =	Units	
		Min	Max	Min	Max	
t _{PLH}	Propagation Delay Time Low to High Level Output	3	10	4	15	ns
t _{PHL}	Propagation Delay Time High to Low Level Output	3	10	4	15	ns

Note 1: All typicals are at $V_{CC}=5V$, $T_A=25^{\circ}C$.

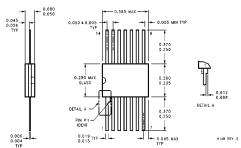
Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.







Physical Dimensions inches (millimeters) (Continued)



14-Lead Ceramic Flat Package (W)
Order Number 54LS20FMQB or DM54LS20W
NS Package Number W14B

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