ONAL AMPLIFIER

LINEAR INTEGRATED CIRCUITS

DES BERTION

The 531 is a fast slewing high performance operational amplifier which retains D.C. performance equal to the best general purpose types while providing far superior large signal A.C. performance. A unique input stage design allows the amplifier to have a large signal response nearly identical to its small signal response. The amplifier can be compensated for truly negligible overshoot with a single capacitor. In applications where fast settling and superior large signal bandwidths are required, the amplifier out performs conventional designs which have much better small signal response. Also, because the small signal response is not extended, no special precautions need be taken with circuit board layout to achieve stability. The high gain, simple compensation and excellent stability of this amplifier allow its use in a wide variety of instrumentation applications.

11455

- 35V/µsec SLEW RATE AT UNITY GAIN
- PIN FOR PIN REPLACEMENT FOR μA709, μA748 **OR LM101**
- COMPENSATED WITH A SINGLE CAPACITOR
- SAME LOW DRIFT OFFSET NULL CIRCUITRY AS **4A741**
- SMALL SIGNAL BANDWIDTH 1 MHz
- LARGE SIGNAL BANDWIDTH 500KHz
- TRUE OP AMP D.C. CHARACTERISTICS MAKE THE 531 THE IDEAL ANSWER TO ALL SLEW RATE LIM-ITED OPERATIONAL AMPLIFIER APPLICATIONS.

MANUAL MAXIMUM RATINGS

Supply Voltage	±22V
Internal Power Dissipation (Note 1)	300mW
Differential Input Voltage	±15V
Common Mode Input Voltage (Note 2)	± 15V
Voltage Between Offset Null and V	±0.5V
Operating Temperature Range	

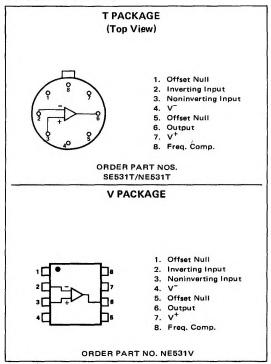
NE531	0°C to +70°C
SE531	-55°C to +125°C

-65°C to +150°C Storage Temperature Range 300°C Lead Temperature (Solder, 60 sec.) Output Short Circuit Duration (Note 3) Indefinite

NOTES:

- 1. Rating applies for case temperatures to 125 °C, derate linearly at 6.5mW/°C for ambient temperatures above +75°C
- 2. For supply voltages less than ±15V, the absolute maximum input voltage is equal to the supply voltage.
- 3. Short circuit may be to ground or either supply. Rating applies to +125°C case temperature or +75°C ambient temperature.

PIN CONFIGURATION



主新原子 化特殊效利子属 **OFFSET NULL CIRCUIT** TRANSIENT RESPONSE TEST CIRCUIT

OF NERAL ELECTRICAL CHARACTERISTICS (V_S = ±15V, T_A = 25°C Unless Otherwise Specified)

Input Offset Voltage Input Olfset Current Input Blas Current Input Besistance Input Voltage Range Common Mode Rejection Ratio	R _S ≤ 10KΩ		2.0 50	6	mV
Input Bias Current Input Resistance Input Voltage Range			50		
Input Resistance Input Voltage Range				200	nA
Input Voltage Range			0.4	1.5	μА
			20		MΩ
December 14 and Defendence Control		± 10			Volts
Common Mode Helection Hatto	R _S ≤ 10KΩ	70	100		dB
Supply Voltage Rejection Ratio	R _S ≤ 10KΩ		10	150	μV/V
Large Signal Voltage Gain	R _L ≥ 2KΩ, V _{OUT} = ±10V	20,000	60,000		1
Output Resistance			75		Ω
Supply Current			5.5	10	mA
Pawer Cansumption			165	300	mW
Full Power Bandwidth	100000000000000000000000000000000000000		500		KHz
Settling Time, 1%	A _V = +1, V _{IN} = ±10V		1.5		µ58C
Settling Time, .01%	AV = +1, VIN = : 10V		2.5		µsec .
Large Signal Overshoot	A _V = +1, V _{IN} = 110V		2		%
Small Signal Overshoot	A _V = +1, V _{IN} = 400mV		5		%
Small Signal Risetime	A _V = +1, V _{IN} = 400mV		300		nsec
The Following Apply for					
0°C < T _A < +70"C:	100				
Input Offset Voltage	R _S ≤ 10KΩ			7.5	mV
Input Offset Current	TA = +70 C			200	nA
	TA = 0°C		1	300	nA
Input Bias Current	TA = +70°C		[1.5	μА
	TA = 0°C			2.0	μА
Large Signal Voltage Gain	R _L ≥ 2KΩ, V _{OUT} = ±10V	15.000			
Output Voltage Swing	R ₁ ≥ 2KΩ	:10	:13		Volts
Slew Rate	Av = 100		35		V/us
	A _V = 10		35		V/us
	A _V = 1 (non-inverting)	20	30		V/µs
	A _V = 1 (inverting)	20 25	35		V/µs
Supply Current	T _A = +70°C	25	4.5	5.5	mA

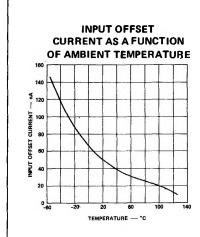
SE531 |

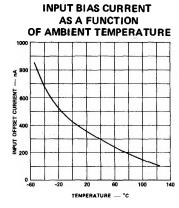
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Input Offset Voltage	R _S <10KΩ		2.0	5.0	m۷
Input Offset Current	1		30	200	nA
Input Bias Current			300	500	n A
Input Resistance			20		MΩ
Input Voltage Range		±10	1		Voits
Large Signal Voltage Gain	R _L ≥ 2KΩ, V _{OUT} = ±10V	50,000	100,000		l .
Output Resistance	2 00.		75		Ω
Supply Current			5.5	7.0	mA
Power Consumption			165	210	mW
Full Power Bandwidth			500		KHz
Settling Time, 1%	A _V = +1, V _{IN} = ±10V		1.5		usec
Settling Time, .01%	A _V = +1, V _{IN} = ±10V		2.5		изес
Large Signal Overshoot	AV = +1, VIN = ±10V		2		%
Small Signal Risetime	A _V = +1, V _{IN} = 400mV		300		nsec
Small Signal Overshoot	A _V = +1, V _{IN} = 400mV		5		%
Siew Rate	A _V = 100		35		V/µs
	Av = 10		35		V/µs
	A _V = 1 (non-inverting)		30		V/µs
	A _V = 1 (inverting)		35		V/µs
The following apply for	-		1 (1
-55°C < T _A < +125°C:			i l		
Input Offset Voltage	R _S ≤ 10KΩ			6	m۷
Input Offsei Current	T_ = +125° C		}	200	n A
	TA = -55"C			500	nA
Input Bias Current	TA = +125°C			500	n A
	T _A = -55°C			1.5	μА
Common Mode Rejection Ratio	R _S ≤ 10KΩ	70	90		dB
Supply Voltage Rejection Ratio	R _S ≤ 10KΩ		10	150	۷/۷یر
Large Signal Voltage Gain	A _L = 2KΩ, V _{OUT} = ±10V	25,000	1		
Output Voltage Swing	H _L = 2KΩ	<u>•</u> 10	113		V
Supply Current	TA - +125°C		4.5	5.5	mA

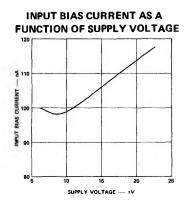
NOTES:

All AC parametric testing is performed using the conditions of the transient response test circuit, page 1.

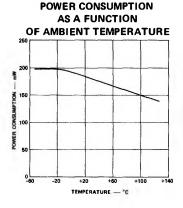
YPICAL PERFORMANCE CHARACTERISTICS(V_S = ±15V, T_A = +25°C unless otherwise noted)

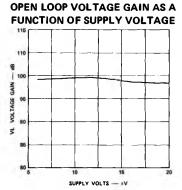


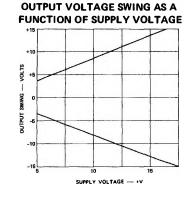


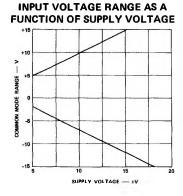


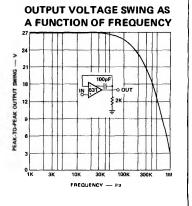
SUPPLY VOLTAGE - ±V



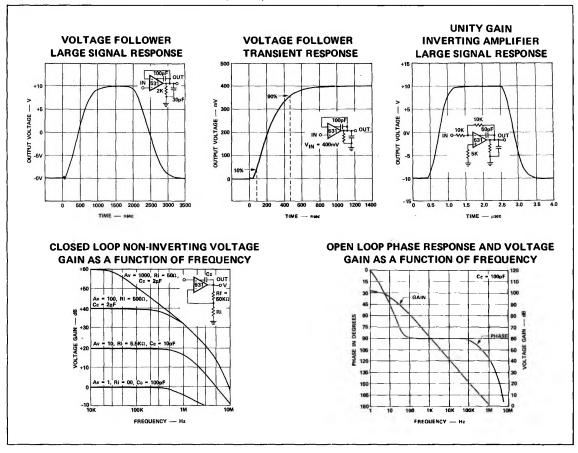


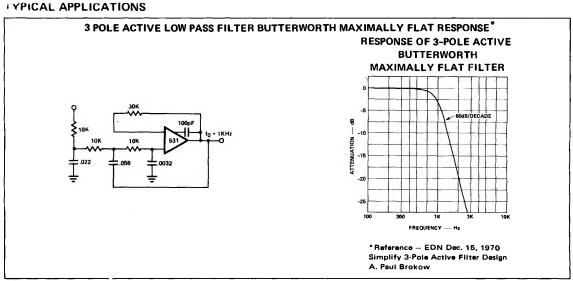




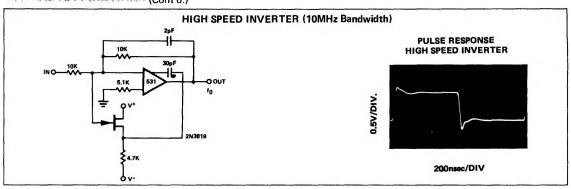


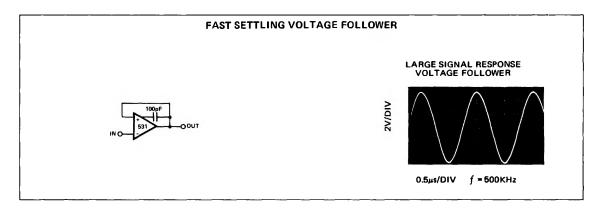
YPICAL CHARACTERISTIC CURVES (Cont'd,)

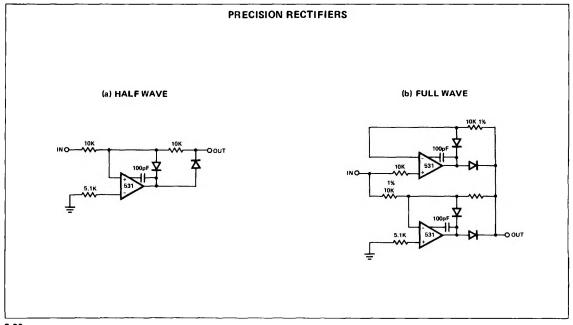




TYPICAL APPLICATIONS (Cont'd.)







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